

Experiment Report

Start of automated test report 2023-05-23 17:22:04

Author=UNKNOWN@workstation obo Luke Vassallo

Machine Information

sysname=Linux

nodename=workstation

release=5.19.0-41-generic

version=#42~22.04.1-Ubuntu SMP PREEMPT_DYNAMIC Tue Apr 18 17:40:00 UTC 2

machine=x86_64

CPU arch : X86_64

CPU bits : 64

CPU brand : Intel(R) Core(TM) i7-10700K CPU @ 3.80GHz

CPU cores : 8

CPU base clock : 3.8000 GHz

CPU boost clock : 3.7920 GHz

System Memory : 31.35GB

Nvidia driver version : 525.105.17

Device 0 : NVIDIA GeForce GTX 1080

Device 0 : 8.0GB

Library Information

python : 3.8.16

torch : 1.13.1+cpu

optuna : 3.1.1

numpy : 1.23.3

pandas : 2.0.1

matplotlib : 3.7.1

seaborn : 0.12.2

pcb library: generation of .pcb files.

Library version : 0.0.12

Library built with : C++14

Library built on : Mar 3 2023 23:10:31

netlist_graph: Graph pre-processing library for PCB component placement.

Library version : 0.1.16

Library built with : C++14

Library built on : Mar 3 2023 23:10:32

Hpyerparameters

/home/gitlab-runner/builds/TfLwHVT9/2/luke/rl_pcb/tests/06_training_td3_cpu_fast/hyperparameters/hp_td3.json

learning_rate:0.001

buffer_size:25000

n_steps:2048

batch_size:128

gamma:0.99

net_arch: {'pi': [400, 300], 'qf': [400, 300]}

activation_fn:relu

expl_noise:0.1

tau:0.005

policy_noise:0.2

noise_clip:0.5

policy_freq:2

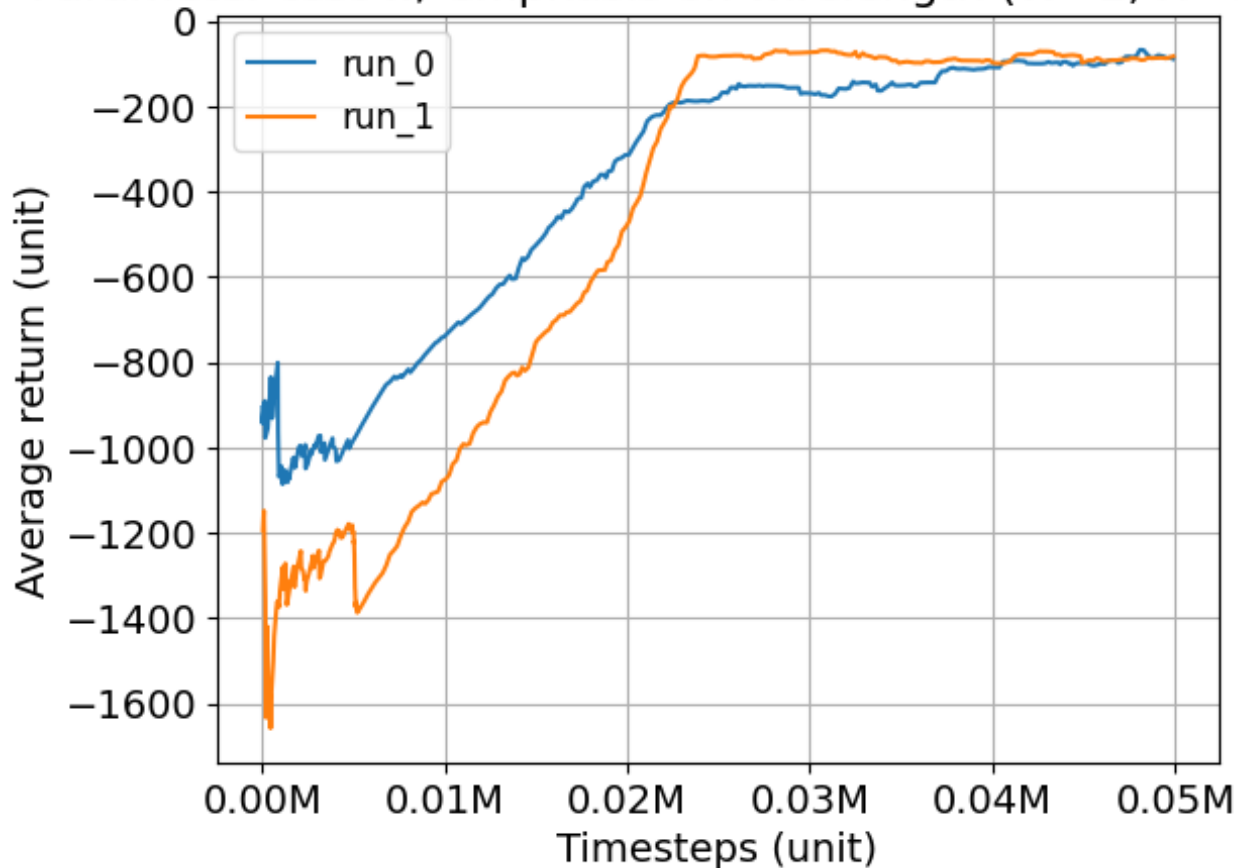
experiment=05_training_td3_cuda_fast_262

experiments=['training_td3_cpu_262']

algorithms=['TD3']

averaging window=100 (user assigned)

Parameter test w/ emphasis on wirelength (W=2, H=6, O=2)



title	td3_cpu_262:TD3
run #0	-87.3252 ± 250.8504
run #1	-38.3814 ± 250.1298
mean	-62.8533 ± 250.4901

runs_involved=['1684830998_0', '1684830998_1']

End of automated test report 2023-05-23 17:22:05