

Experiment Report

Start of automated test report 2023-03-07 07:54:13

Author=luke@workstation obo Luke Vassallo

Machine Information

sysname=Linux

nodename=workstation

release=5.4.0-144-generic

version=#161~18.04.1-Ubuntu SMP Fri Feb 10 15:55:22 UTC 2023

machine=x86_64

CPU arch : X86_64

CPU bits : 64

CPU brand : Intel(R) Core(TM) i7-10700K CPU @ 3.80GHz

CPU cores : 16

CPU base clock : 3.8000 GHz

CPU boost clock : 4.3089 GHz

System Memory : 62.64GB

Nvidia driver version : 470.161.03

Device 0 : NVIDIA GeForce GTX 1080

Device 0 : 7.93GB

Library Information

python : 3.8.13

torch : 1.11.0+cu102

optuna : 3.1.0

numpy : 1.23.3

pandas : 1.5.3

matplotlib : 3.7.0

seaborn : 0.12.2

pcb library: generation of .pcb files.

Library version : 0.0.12

Library built with : C++14

Library built on : Mar 3 2023 23:10:31

netlist_graph: Graph pre-processing library for PCB component placement.

Library version : 0.1.16

Library built with : C++14

Library built on : Mar 3 2023 23:10:32

Hyperparameters

/home/luke/work/rl_pcb/tests/01_training_td3_cpu/hyperparameters/hp_td3.json

learning_rate:0.001

buffer_size:25000

n_steps:2048

batch_size:128

gamma:0.99

net_arch: {'pi': [400, 300], 'qf': [400, 300]}

activation_fn:relu

expl_noise:0.1

tau:0.005

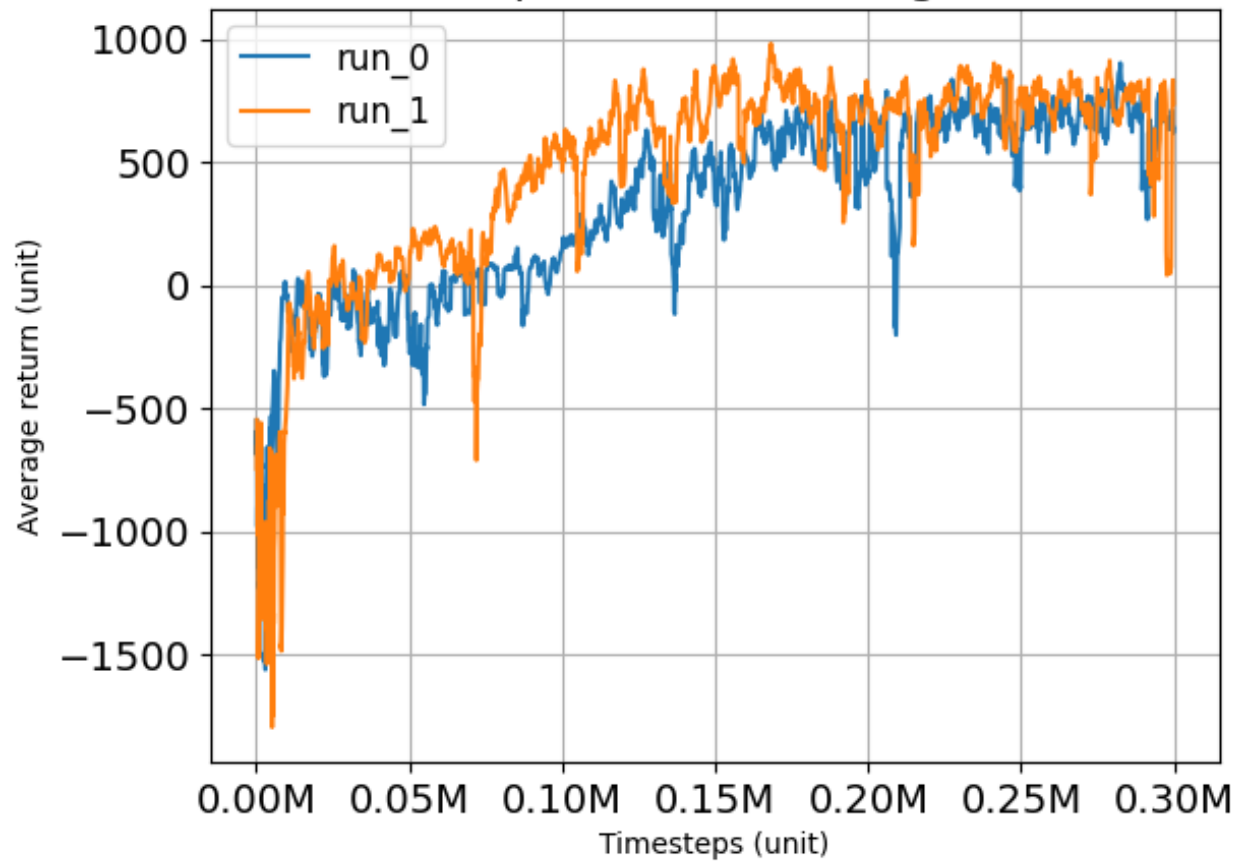
policy_noise:0.2

noise_clip:0.5

policy_freq:2

experiment=01_training_td3_cpu_622
experiments=['training_td3_cpu_622']
algorithms=['TD3']
averaging window=10 (user assigned)

Parameter test w/ emphasis on wirelength (W=6, H=2, O=2)



title	td3_cpu_622:TD3
run #0	664.9250 ± 357.5329
run #1	709.3085 ± 486.5294
mean	687.1168 ± 422.0312

runs_involved=['1678118952_0', '1678118952_1']

End of automated test report 2023-03-07 07:54:15