In a makefile, variable is a name defined in a makefile to represent text, called the variable value.

Ex.: $(FILENAME) = main

These values are added into our executable command line command used by our target or targets of the makefile.

Ex:.

compile:

gcc -o $(FILENAME) $(FILENAME) .C

when declaring variable:

$(your\_variable\_name) = your\_value

When using variable:

$(your\_variable\_name)