

3/23/2025

GPIO pin configurations

★ : different than default

1: GND	no config	
2: 3V3	no config	
3: EN	no config	(external pull-up circuit)
4: I04	input	(rotary encoder A, external pull-up circuit)
5: I05	input	(rotary encoder B, external pull-up circuit)
6: I06	input	(clear button, external pull-up circuit)
7: I07	output	(vibration motor)
8: I015	output	(LED)
9: I016	output	(LED)
10: I017	output	(LED)
11: I018	output	(LED)
12: I08	output	(LED)
13: I019		(default as USB-D-)
14: I020		(default as USB-D+)
15: I03	output	(LED)
16: I046	do not use	
17: I09	★	(FSPI_HD) hold
18: I010	★	(FSPI_CS0) chip select
19: I011	★	(FSPI_D) data in
20: I012	★	(FSPI_CLK) clock
21: I013	★	(FSPI_Q) data out
22: I014	★	(FSPI_WP) write protect

23: I021	unused	
24: I047	unused	
25: I048	unused	
26: I045	do not use	
27: I00	no config	(external pull-up circuit)
28: I035	output	(LED)
29: I036	output	(LED)
30: I037	output	(LED)
31: I038	output	(LED)
32: I039	output ★	(LED)
33: I040	output ★	(LED)
34: I041	output ★	(LED)
35: I042	output ★	(LED)
36: RXD0	no config	
37: TXD0	no config	
38: I02	output	(LED)
39: I01	output	(LED)
40: GND	no config	
41: EPAD	no config	

strapping pins

GPIO0: default high	chip boot mode	✓
GPIO3: default floating ★ may need to look into setup + hold time	JTAG signal source	✓ using as output
GPIO45: default low	vdd_spi voltage	✓ ★ not using
GPIO46: default low	ROM message printing, chip boot mode	✓ ★ not using