2/19/2025

Programmer

VS Code w/ Platform LO extension allows use of Ardnino platform

can use ESP-LDF who changing build platformer well documented

https://www.amazon.com/DSD-TECH-Adapter-FT232RL-Compatible/dp/B07BBPX8B8?source=ps-sl-shoppingads-lpcontext&ref_=fplfs&psc=1&smid=AFLYC5O31PGVX&gQT=1

2/22/2025 RXD Signal list SP input-only GPIO 38 37 JTAG: used to test PCB **VDDA** 1 36 NC (shorts or opens) esp 32- pico- v3 LNA_IN [2] 35 (NC 3 34 VDDA3P3 105 4 VDDA3P3 33 SD1/IO8 ADC: analog to digital conversion channel 5 32 SENSOR_VP/I36 SD0/IO7 DAC: digital to analog conversion channel analog 6 SENSOR_CAPP/I37 31 CLK/IO6 49 GND PINS 30 SENSOR_CAPN/I38 CMD/IO11 SPI, HSPI, VSPI: siquals of SENSOR_VN/I39 8 29 SD3/IO10 SPI modules 9 do NOT leave floating 28 SD2/IO9 high enables VDET 1/I34 10 27 1020 the Chip. RTC GPIO 11 UO, UI, UZ: signals of UART modules VDET_2/I35 26 VDD_SDIO low is off. 32K XP/IO32 12 25 NC could use NC: not connected pull down switch MTMS/I014 /DD3P3_RTC 32K_XN/1033 MTCK/IO13 025 1027 for manual reset * debounce SPIO SD, HS1, HS2: 200 2015 1013 L> low enters program mode on brodrammer; 3 02 when en is high. could also tie this 02 RTS CIS: clear to send RTS CTS telling chip its ready to receive programs when DIR = 0, R15 = 1 RTS: request to send brodrammer programmer asking chip to send data can send can receive

Notes: rotary encoder outputs digital

DTR: data terminal ready tells chip its ready to send or receive data

For programming,

can use 4 pin header or 6 pin header (excluding Vcc, Gnd) (including Vcc, Gnd)

| programmer | between | esp | |
|------------|---------|------|------|
| TXD | 1KD | RXDO | (40) |
| RXD | 14-52 | TXDO | (41) |
| RTS | | 1013 | (20) |
| CTS | | 1015 | (21) |

| programmer | board | |
|------------|----------|--|
| 3.3/5 V | ext. VCC | |
| GND | ext. GND | |

use buttons to enable programming hold 100, press EN button, then release boot

https://docs.espressif.com/projects/esp-dev-kits/en/latest/esp32/esp32-pico-kit/user_guide_v3.html