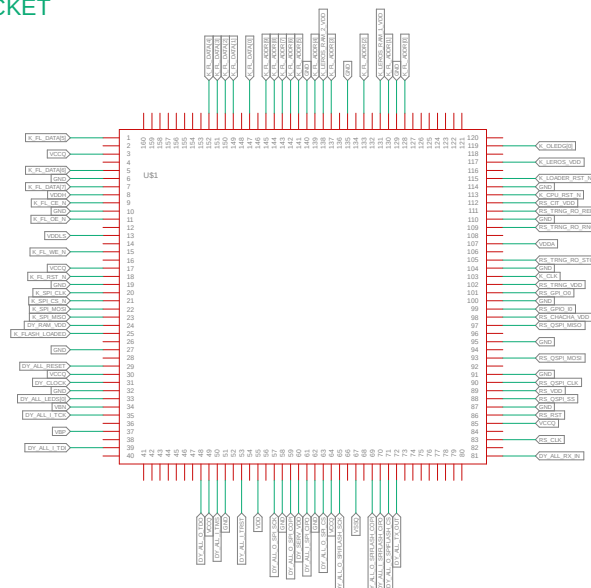
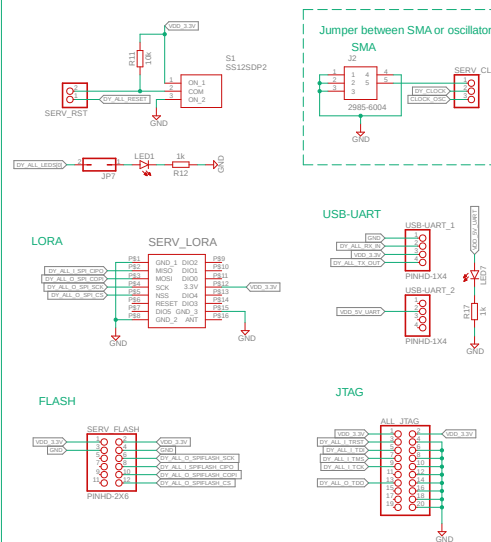


## SOCKET

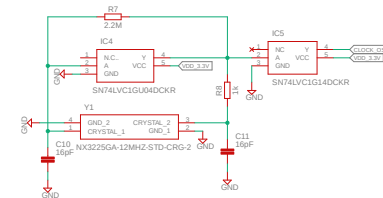


## SERV



## CRYSTAL OSCILLATOR

Generate 12MHz clock



## MOUNTING HOLES

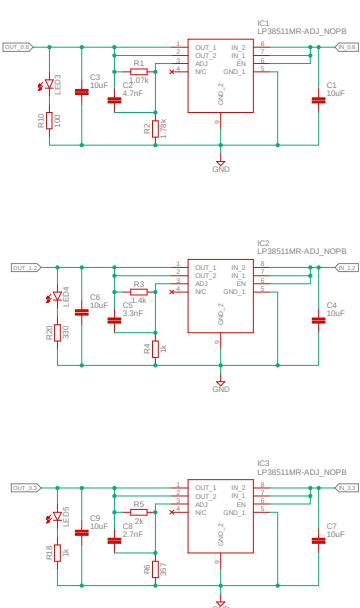
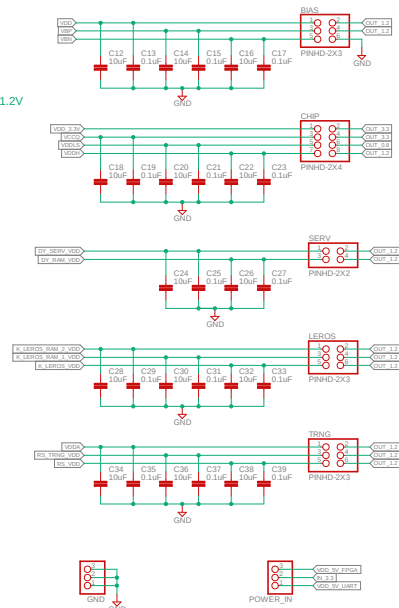


SOTB Feb. 2022  
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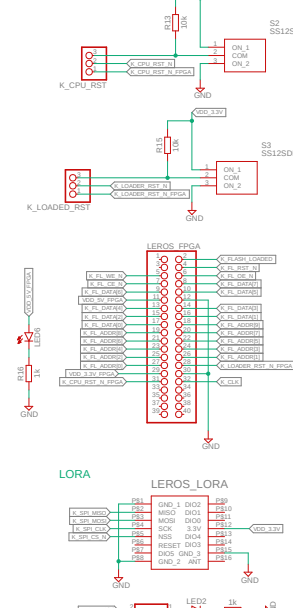
SERV 32RVE  
LEROS  
OLINGUITO + TRNG

## POWER

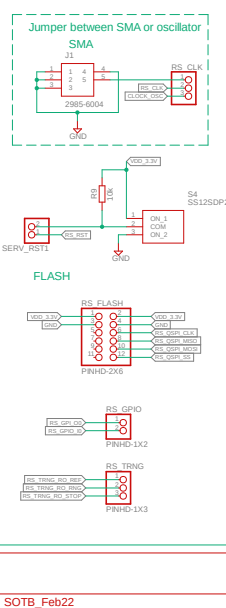
3 regulators of 0.8V, 1.2V and 3.3V  
Input source is 5V from UART  
(if using SERV or OLINGUITO)  
or FPGA (if using LEROS)  
3.3V: VCCQ and peripherals  
1.2V: VDDH, VDD of core and RAM  
0.8V: VDDLs  
Bias voltages: VBN to GND, VBP to 1.2V



## LEROS



## OLINGUITO



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