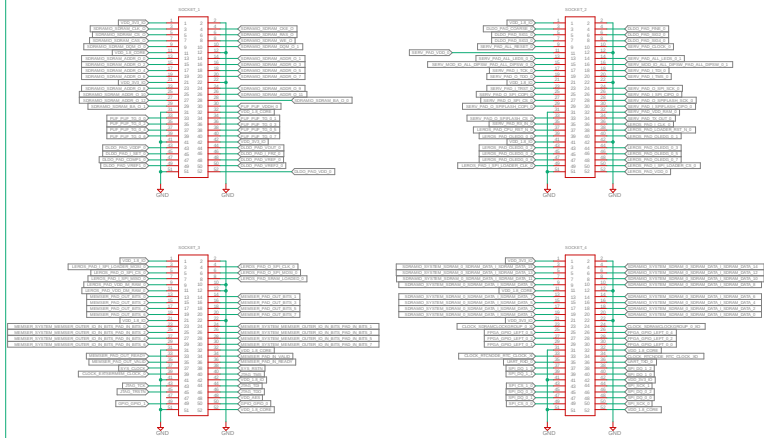
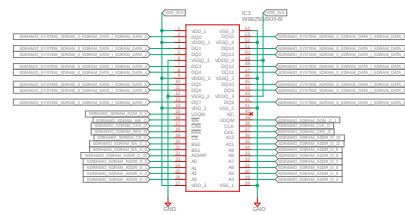


SOCKET



MEMORY

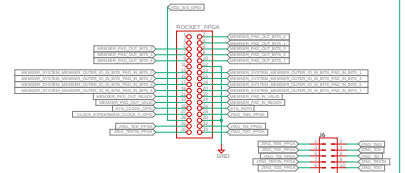


W9825G6KH-6I SDRAM from Winbond 32MB

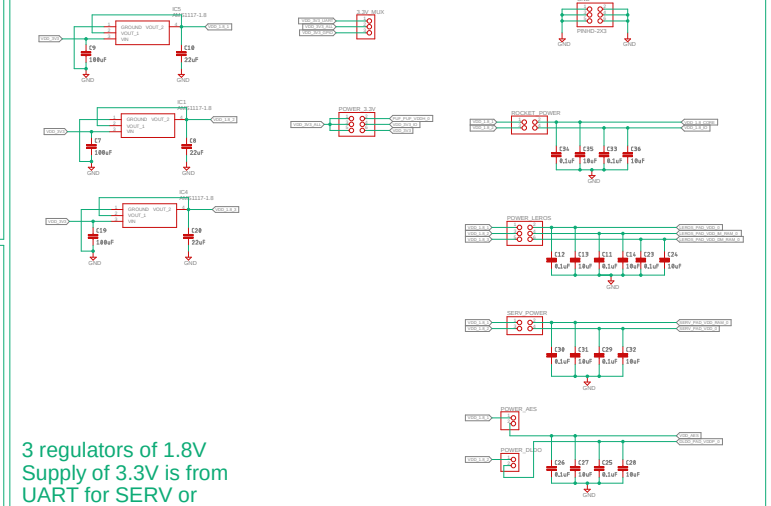
DLDO & PUF



ROCKET_FPGA

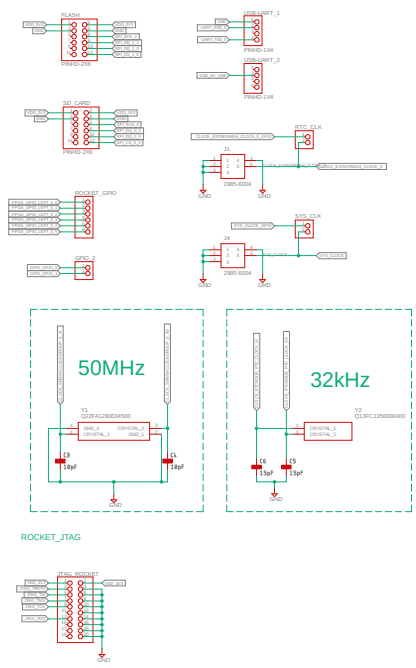


POWER

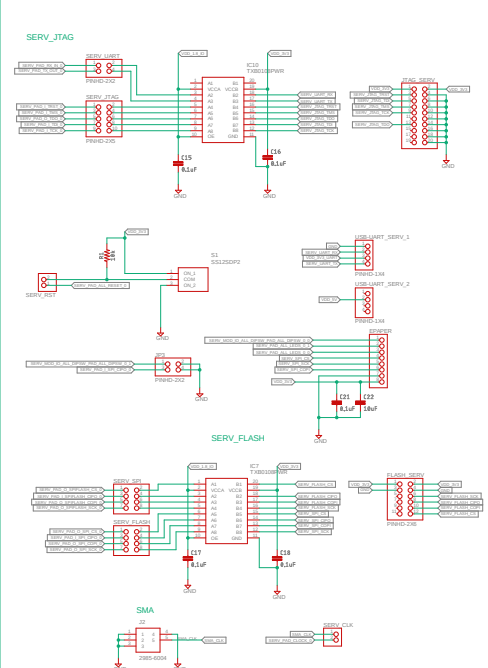


3 regulators of 1.8V
Supply of 3.3V is from
UART for SERV or
GPIO for LEROS and Rocket

ROCKET

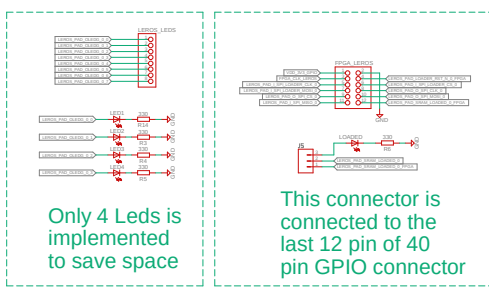


SERV



ROHM 2022
ROCKET_SERV
LEROS
DLDO_PUF
Copyright ©
UEC Pham Lab

LEROS



Only 4 Leds is
implemented to
save space

This connector is
connected to the
last 12 pin of 40
pin GPIO connector

V1 ROCKET-HOLES.3
V2 ROCKET-HOLES.3
V3 ROCKET-HOLES.3
V4 ROCKET-HOLES.3

TITLE: ROHM_102022
Document Number:
Date: 12/2/2022 8:09 PM
REV:
Sheet: 1/4