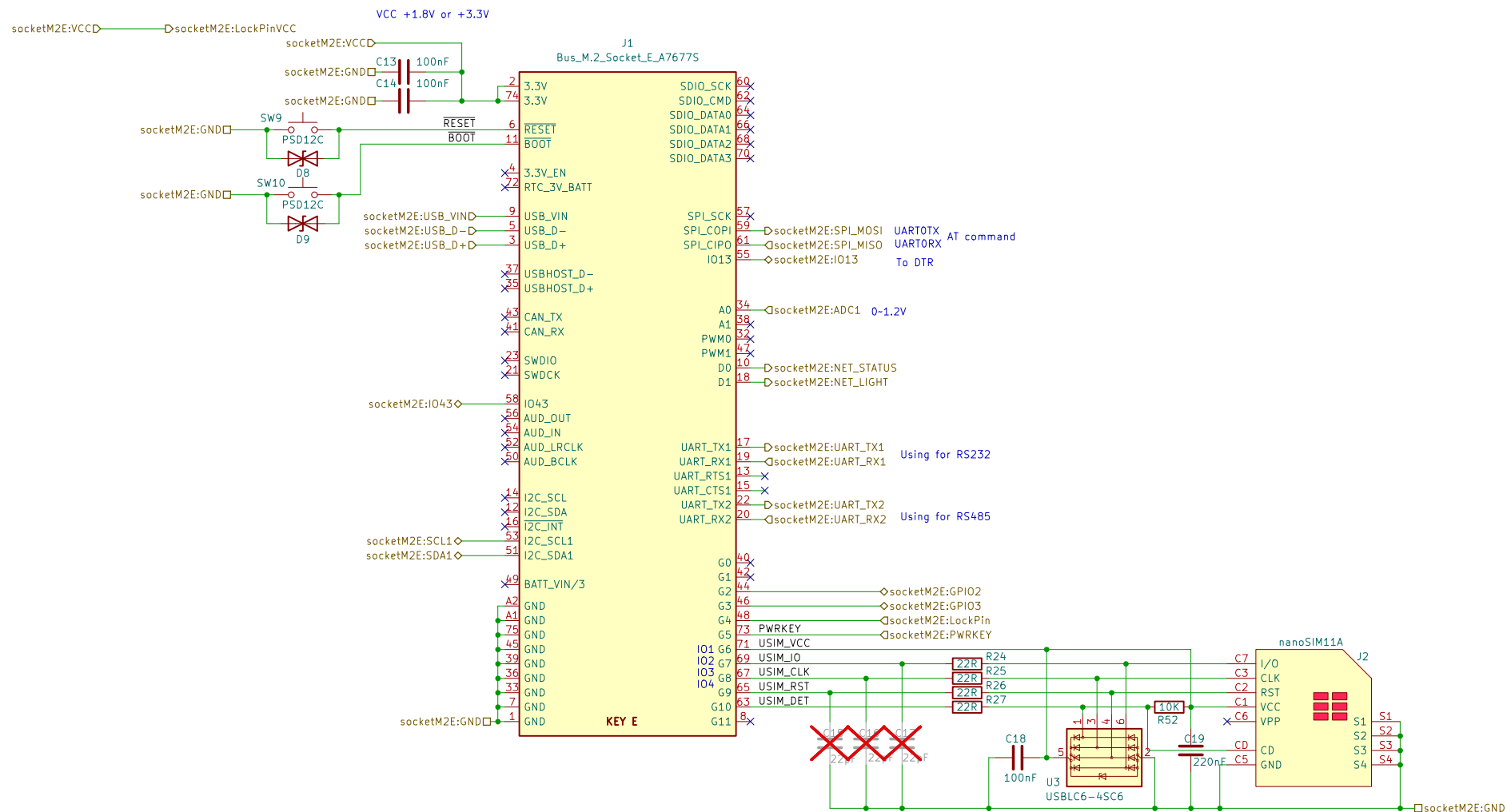
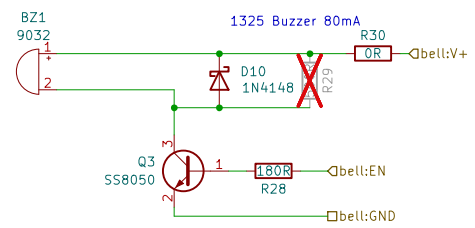
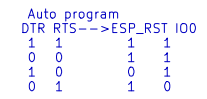


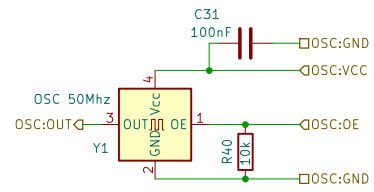
$$V_{out} = 0.768 \times (1 + 33k/10k) = 3.3V$$



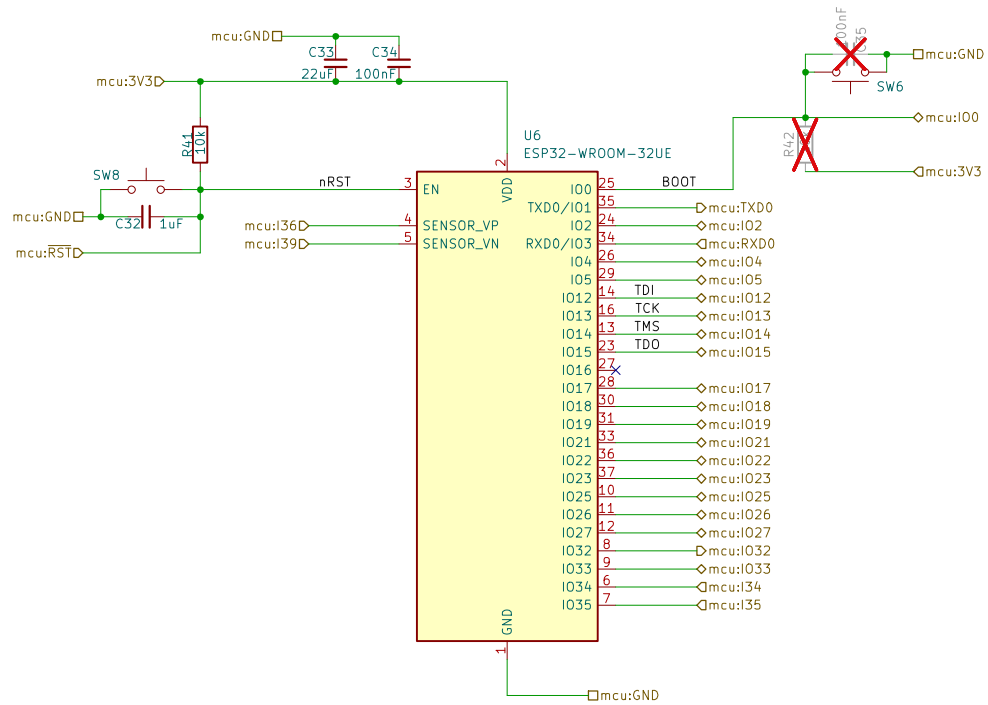


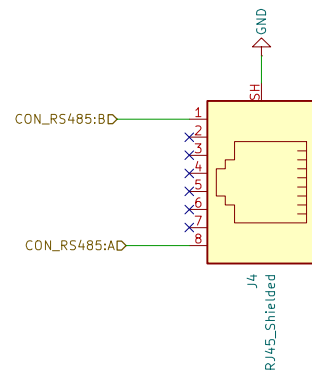


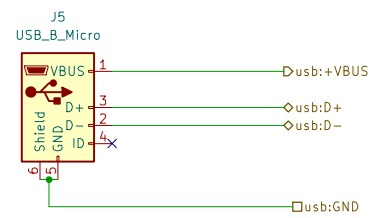


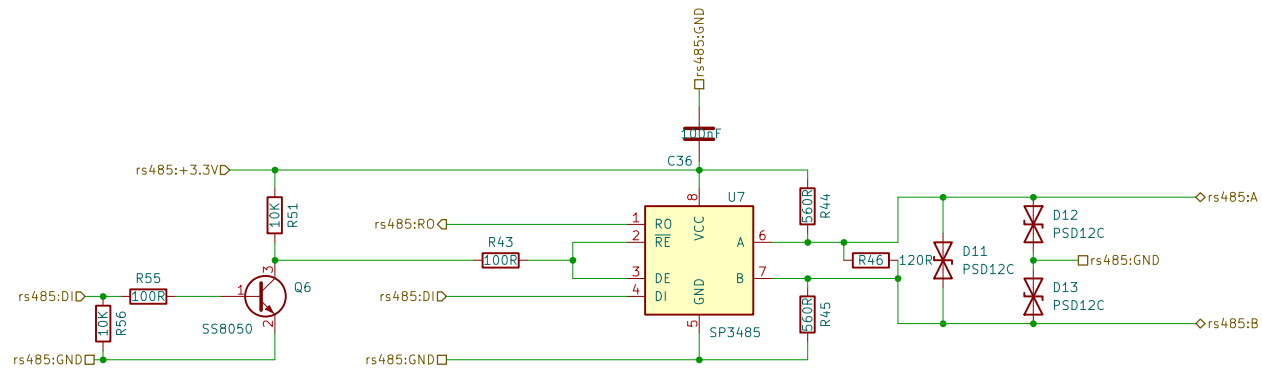


OE LOW to High Impedance
OE HIGH to active









\overline{RE} Receiver output enable Active Low
 DE Driver output enable Active High
 Device is in receive mode when \overline{RE} is logic low,
 and in transmit mode when DE is logic high

when the $A-B > +0.2V$, the RO output logic 1; When $A-B < -0.2V$, the RO outputs logic 0

