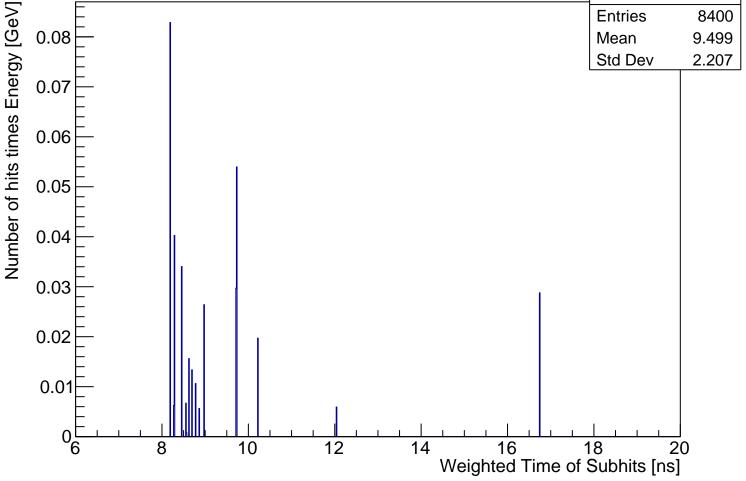
## SiECalEndcap\_T0\_L0:9



SiECalEndcap\_T0\_L0:9\_time