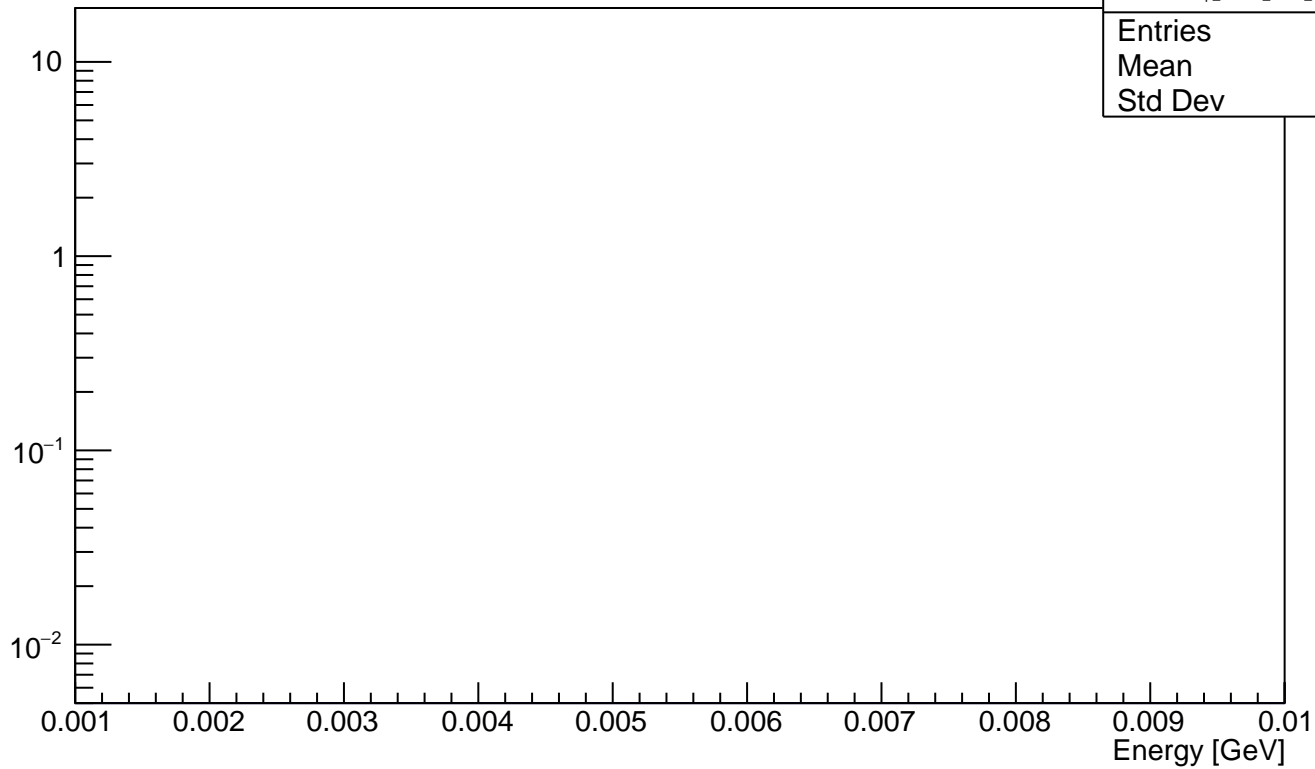


ScHCalEndcap_T12:15_L0:15

Number of hits



ScHCalEndcap_T12:15_L0:15_upper_scale

Entries	0
Mean	0
Std Dev	0