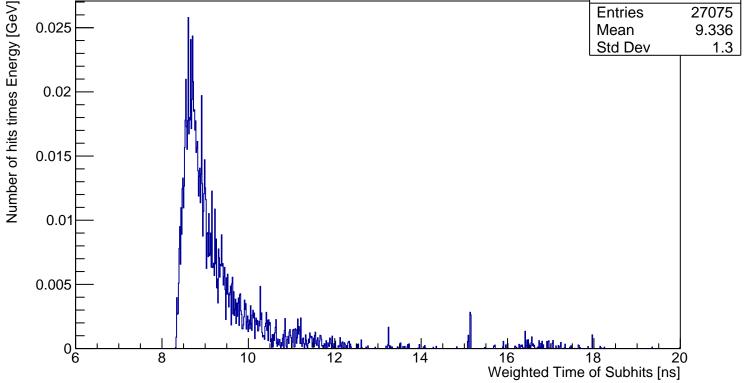
SiECalEndcap\_T1:2\_L0:9



SiECalEndcap\_T1:2\_L0:9\_time