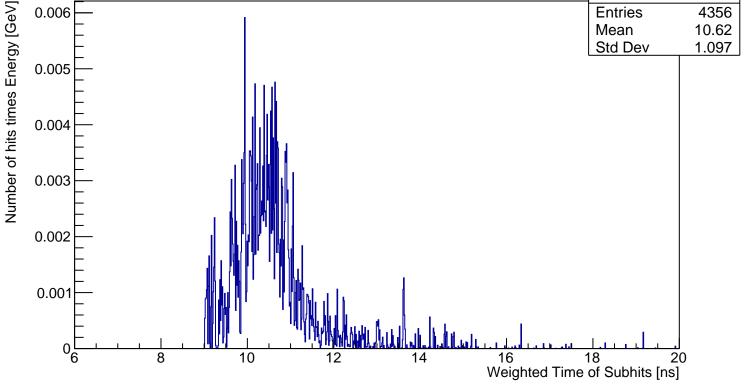
ScHCalEndcap\_T4:7\_L0:15



ScHCalEndcap\_T4:7\_L0:15\_time