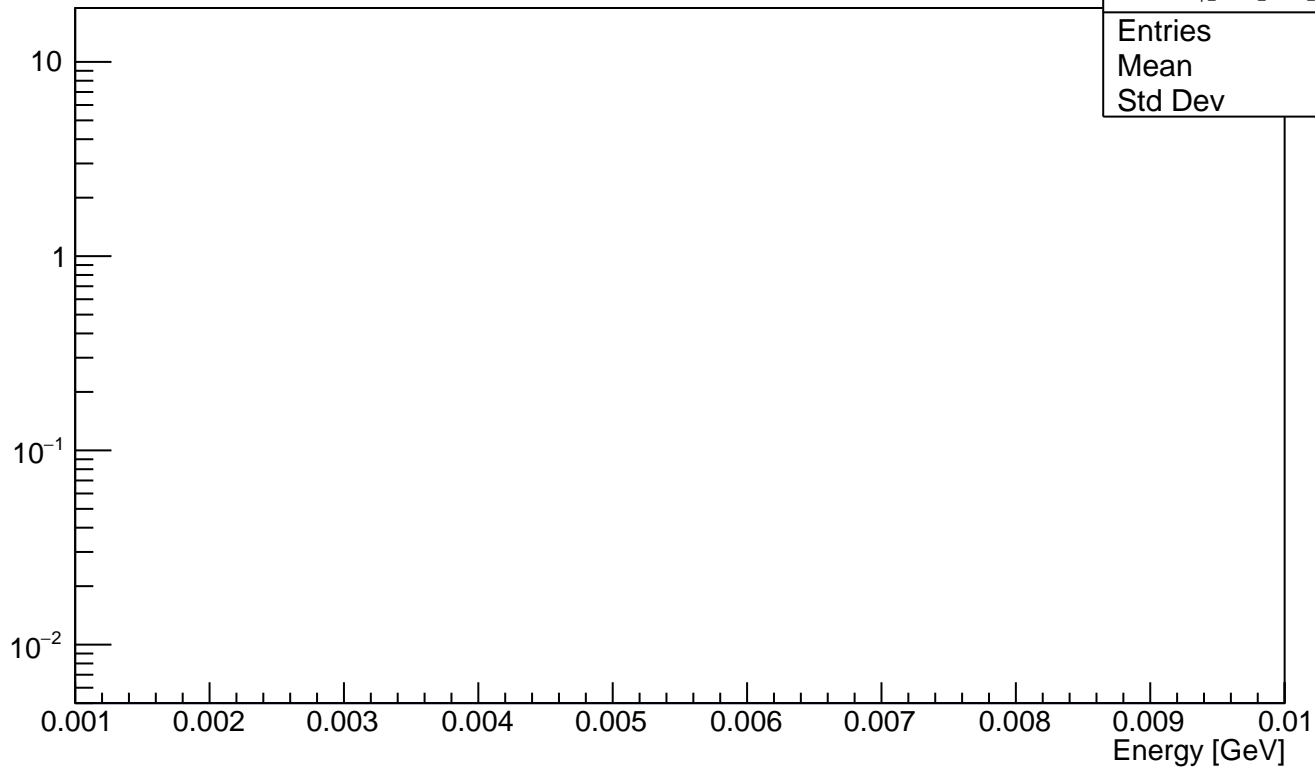


ScHCalEndcap_T12:15_L32:48

Number of hits



ScHCalEndcap_T12:15_L32:48_upper_scale	
Entries	0
Mean	0
Std Dev	0