



Title		
I/O Board for MiSTer		
Size	Number	Revision
A4		6.0
Date:	04.07.2019	Sheet of
File:	C:\Work\...\iobrd.SchDoc	Drawn By:

MiSTer

..... FPGA

I/O Board v6.0 (C)2019 Alexey Melnikov

reset

osd

user

power

disk

user

Ext.Buttons

Ext. LEDs

OVR

AUTO
SOG

RN1 RN2
RN3 RN4

RN10
RN11

J1CJ1CJ1CJ1C

J1

I2S

3.3v

VGA PWR

5v

LRCLK
BCLK
DATA
GND
5v

J2

LED4

Fan

+

-

RN14

Not USB!
User Port

J4

