Digital Design Verification Training DLD

Task: 4-bit ALU

Name	*Khalil Rehman*
Instructor	*Hira Sohail*
Date	*30 July 2025*

1. In-Lab Tasks:

i. Task 1:

SV file attached

ii. Task 2:

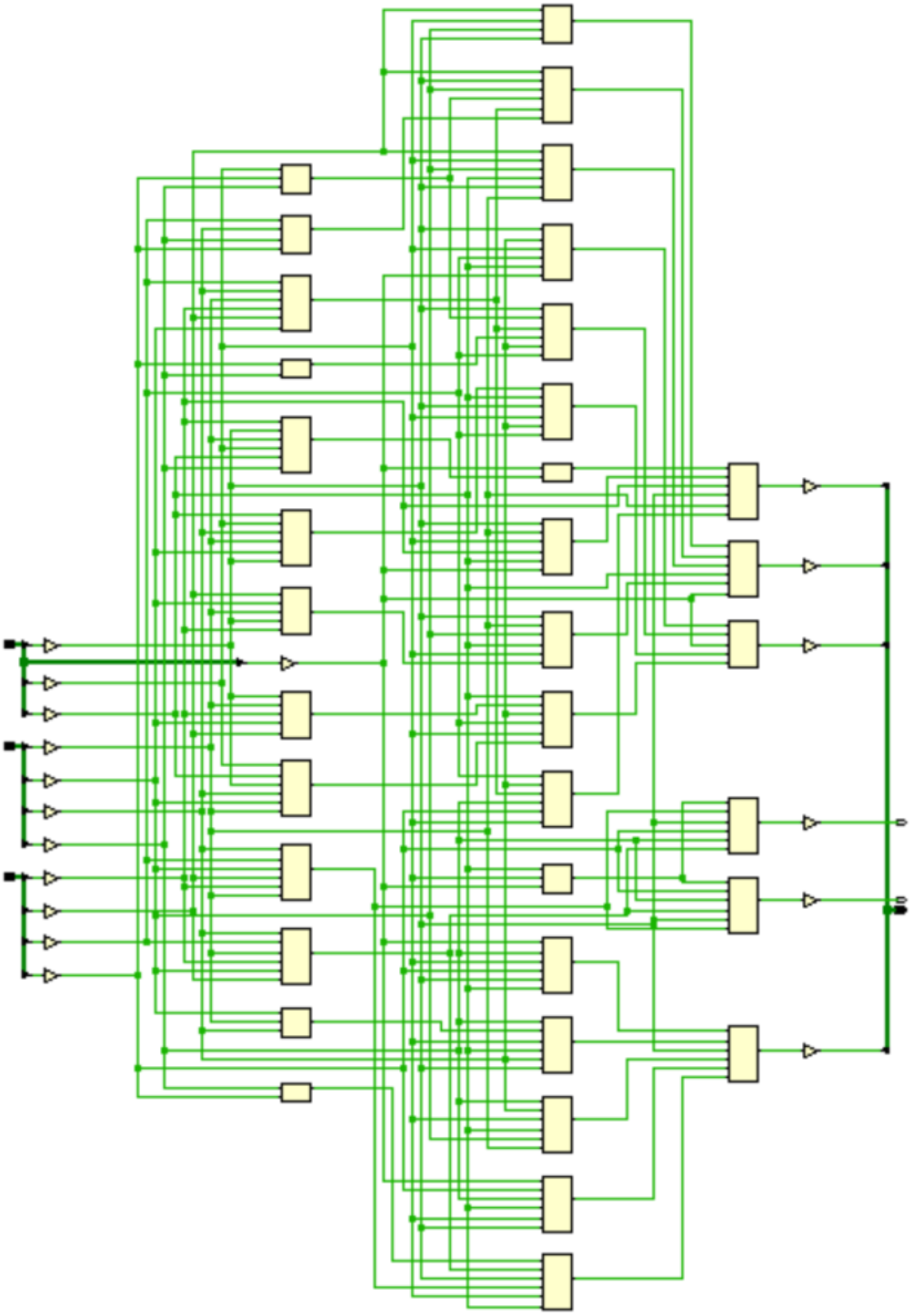
Testbench file attached

iii. Task 3:

Attached

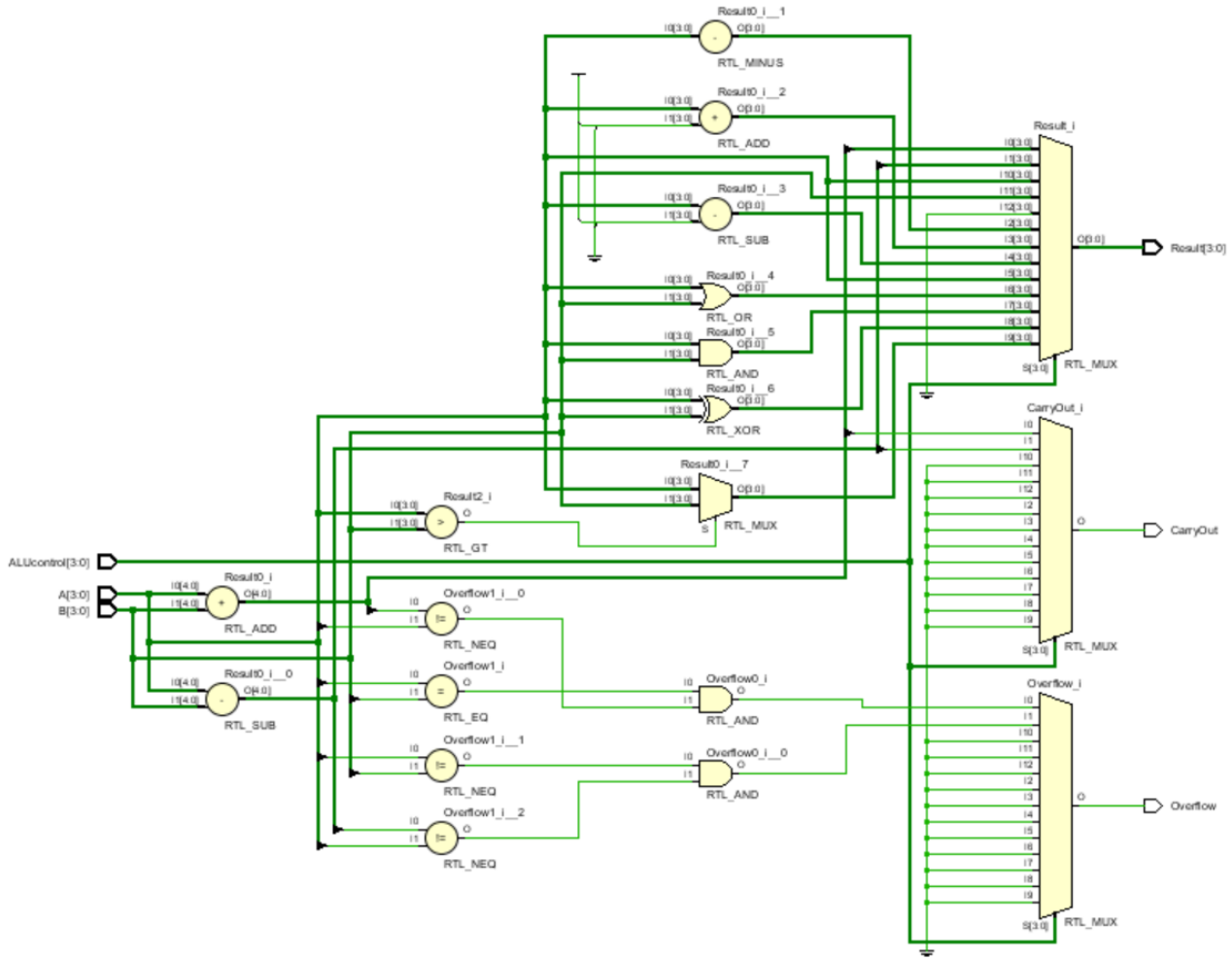
iv. Task 4:

Attached



v. Task 5:

RTL Diagram



vi. Task 6:

Power Report