Digital Design Verification

Assignment # 01

Conditionals

Submitted by:

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Instructor:	**Hira Sohail**

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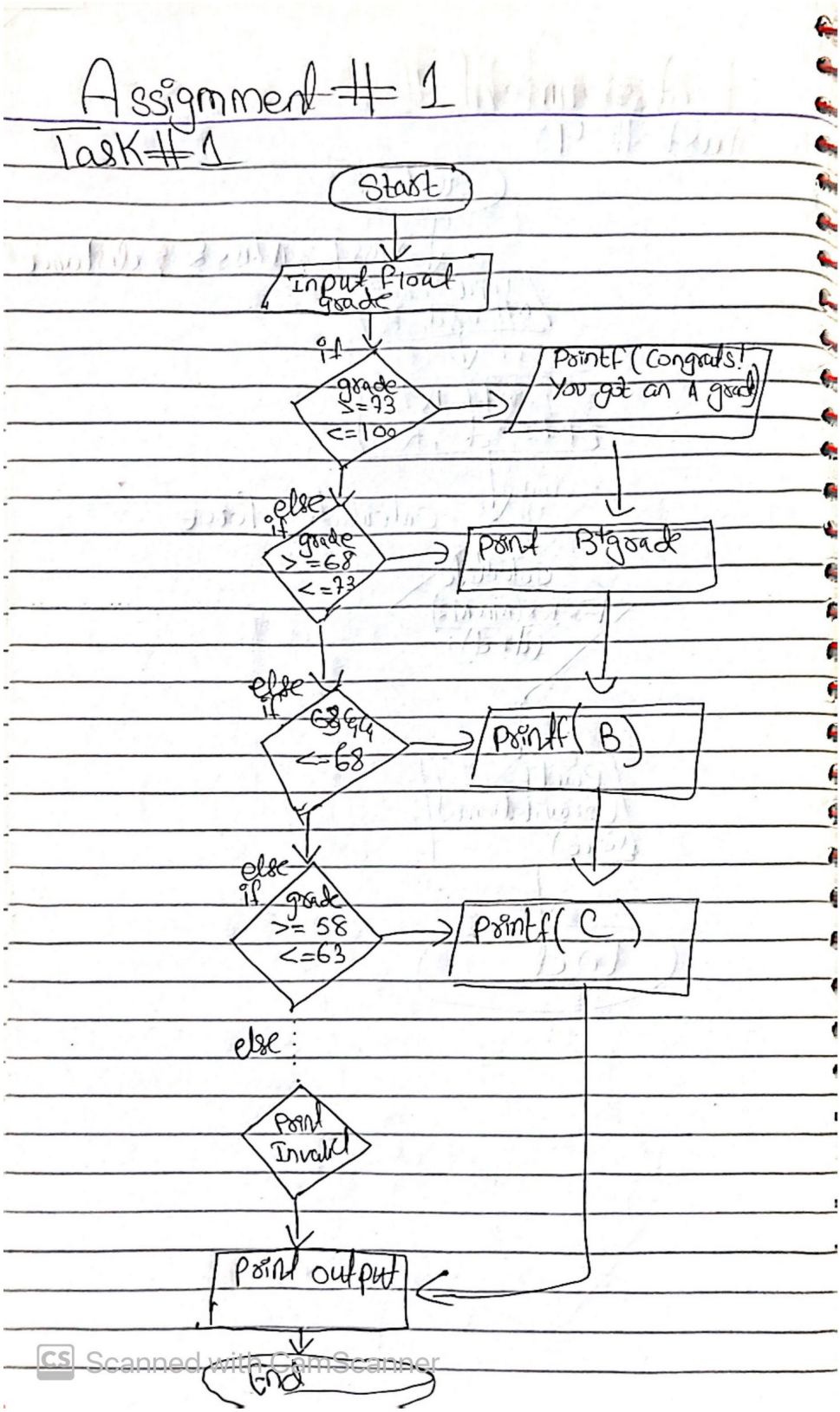
NUST Chip Design Centre (NCDC), Islamabad, Pakistan

TASK # 01:

Flow Chart:



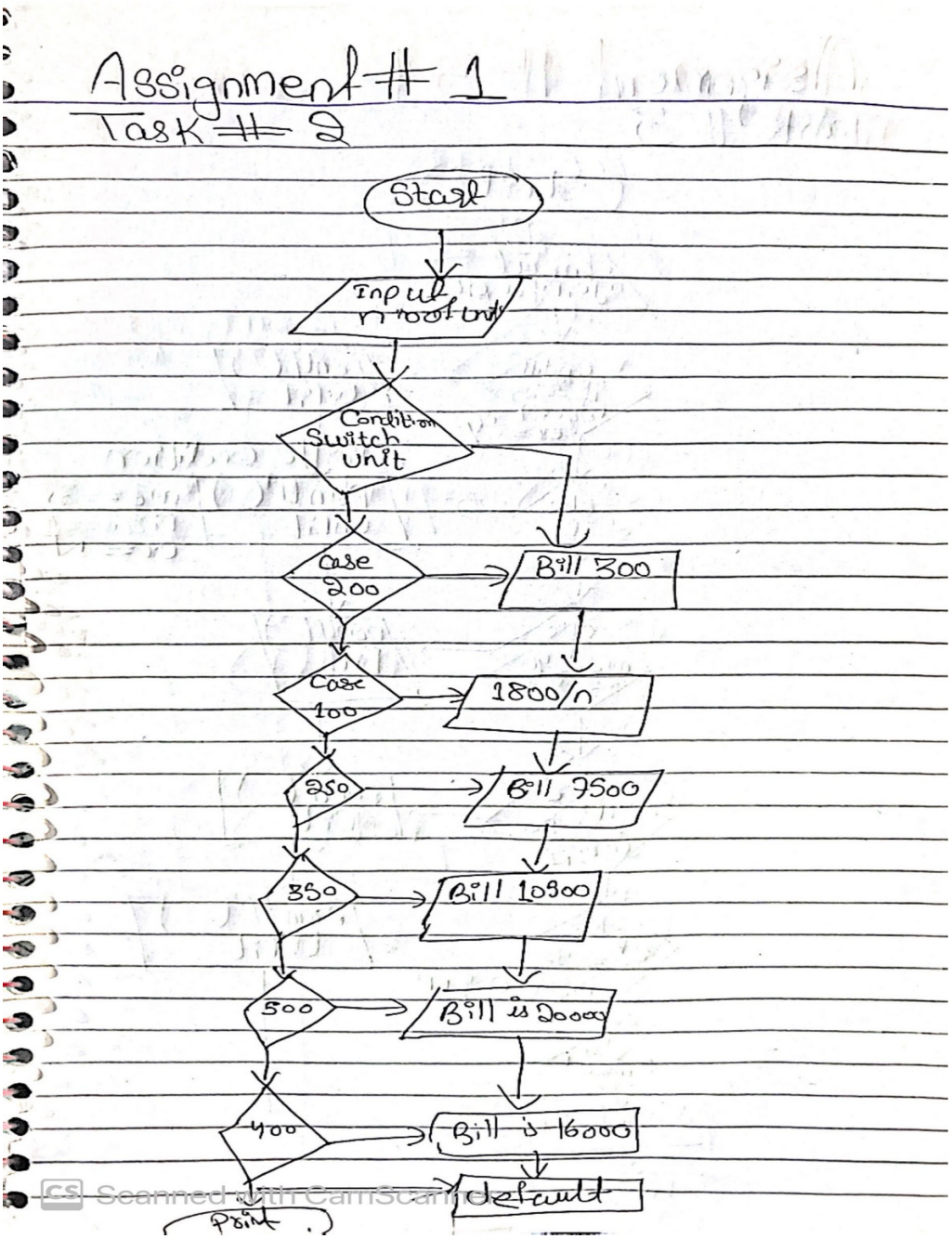
Scanned with CamScanner

Terminal output:

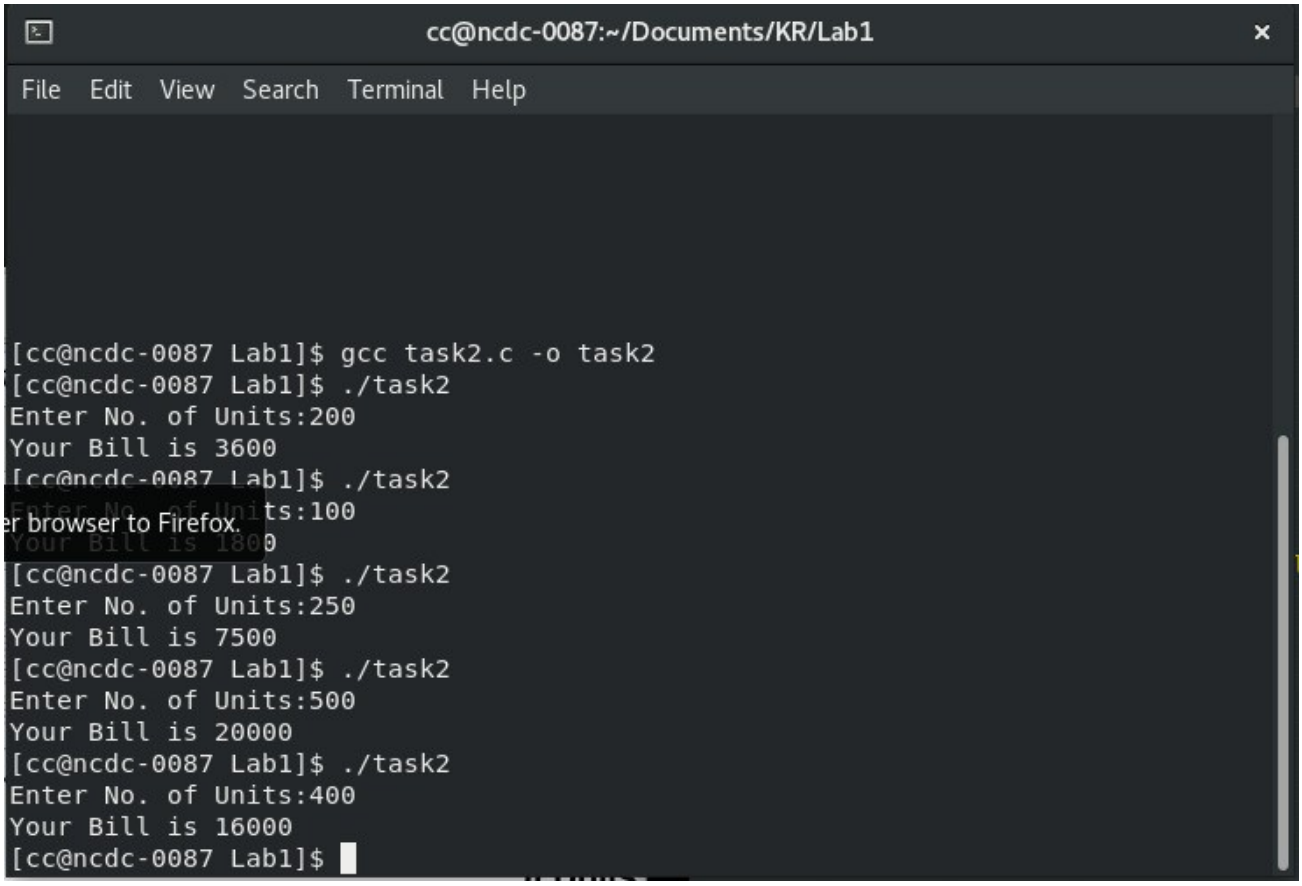
```
cc@ncdc-0087:~/Documents/KR/Lab1
File  Edit  View  Search  Terminal  Help
[cc@ncdc-0087 Lab1]$ gcc task1.c -o task1
[cc@ncdc-0087 Lab1]$ ./task1
Please Enter Your Marks:74
Congrats! You got an A grade
[cc@ncdc-0087 Lab1]$ gcc task1.c -o task1
[cc@ncdc-0087 Lab1]$ ./task1
Please Enter Your Marks:52
You got a C grade
[cc@ncdc-0087 Lab1]$
```

TASK # 02:

Flow Chart:

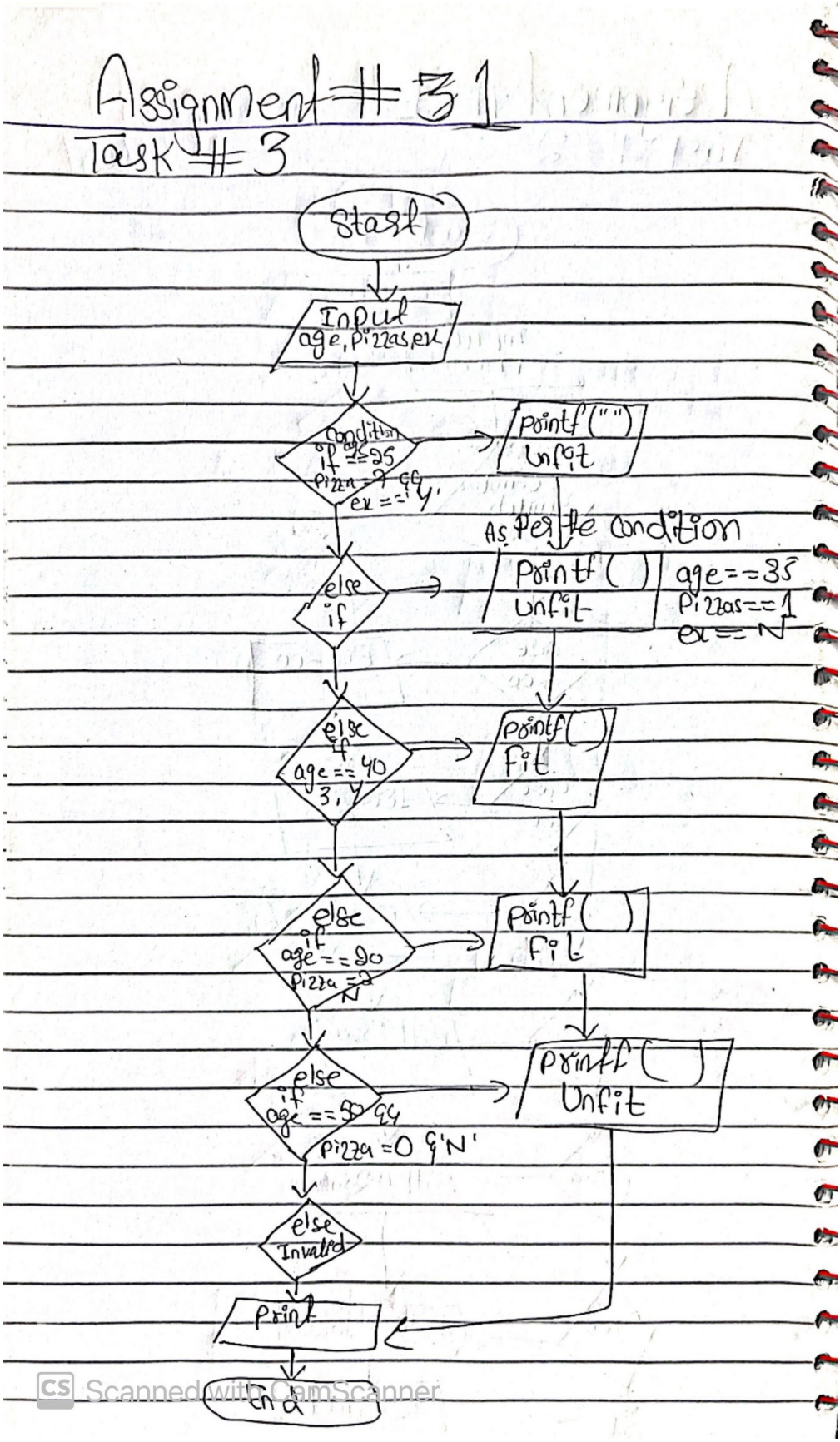


Terminal output:

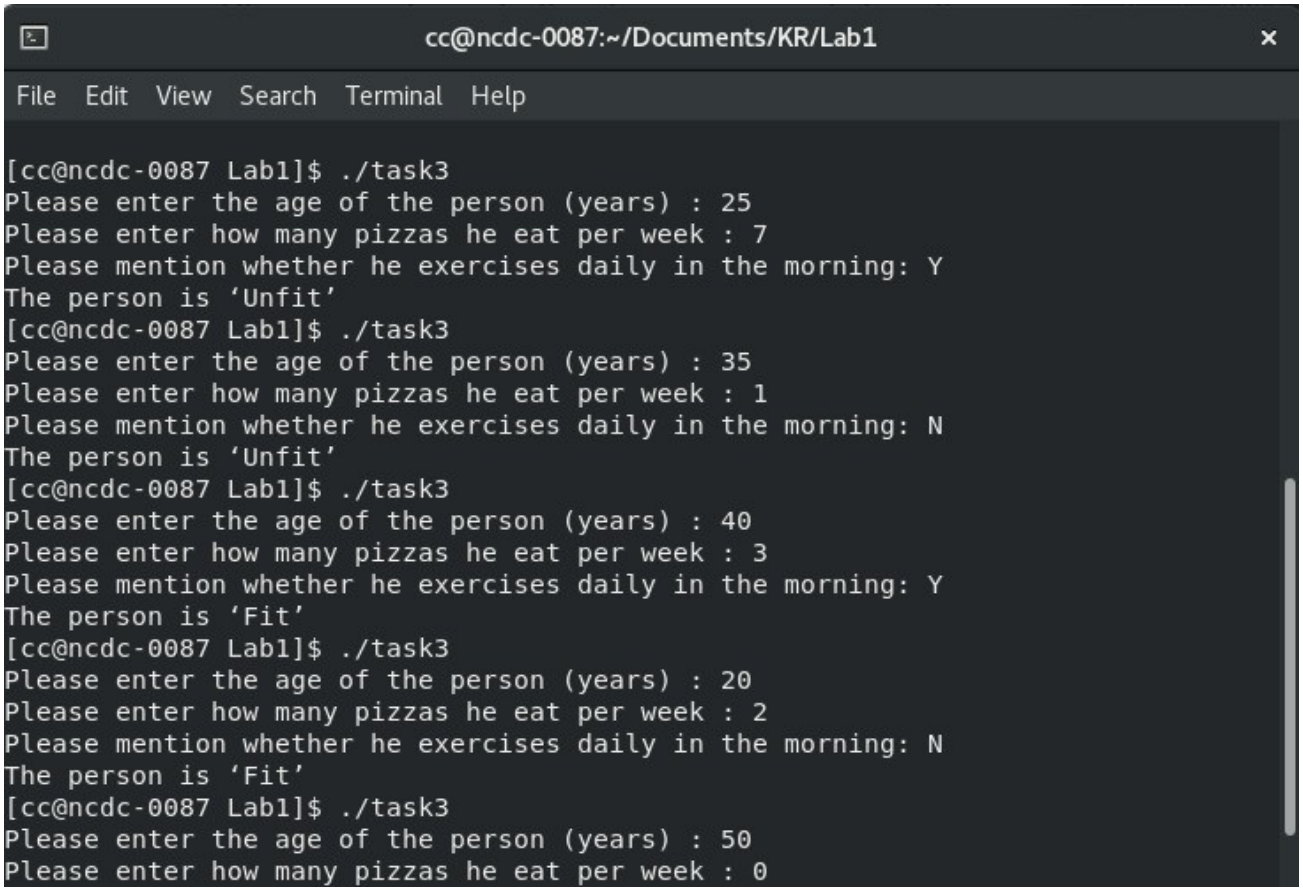


TASK # 03:

Flow Chart:

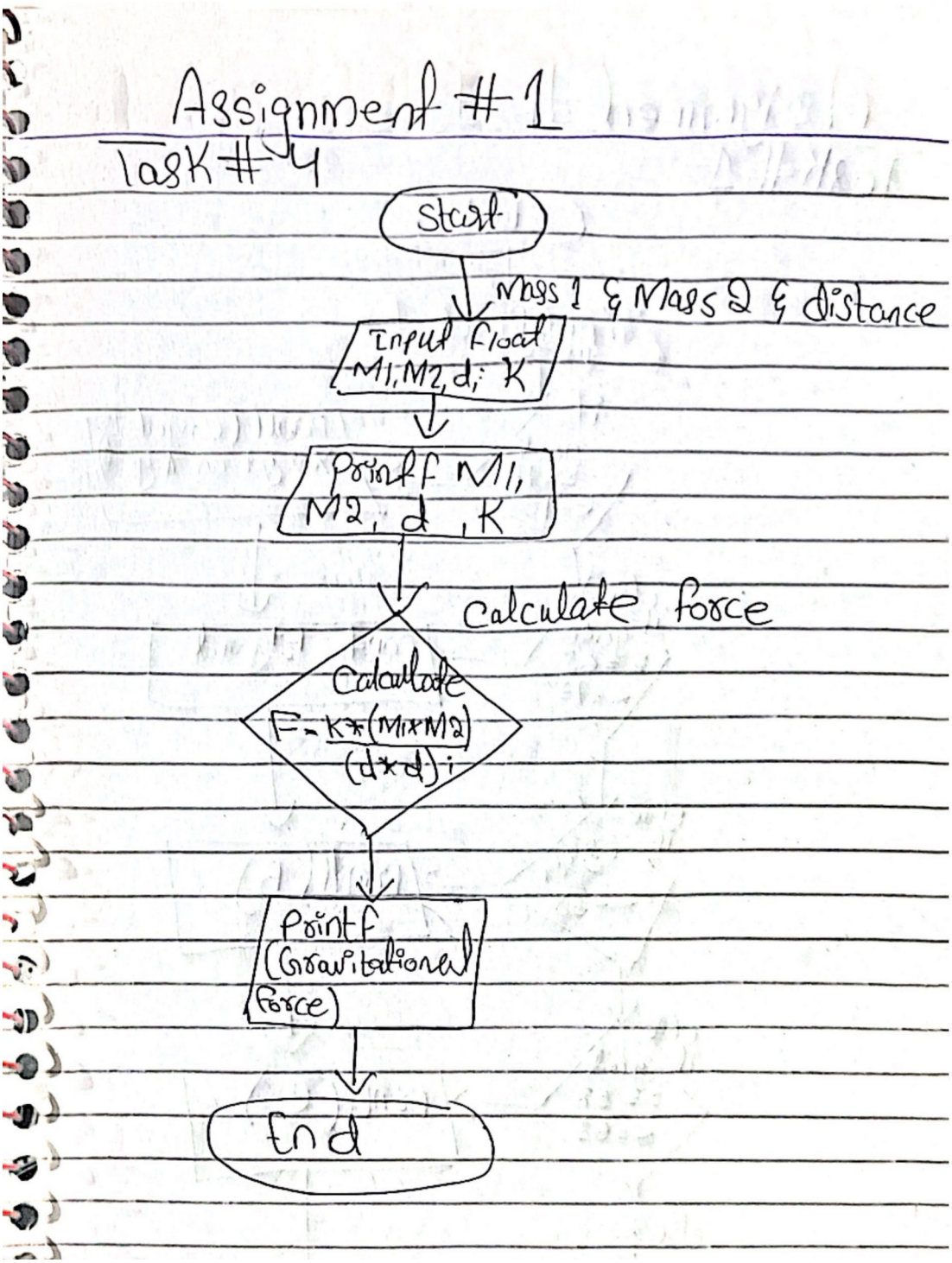
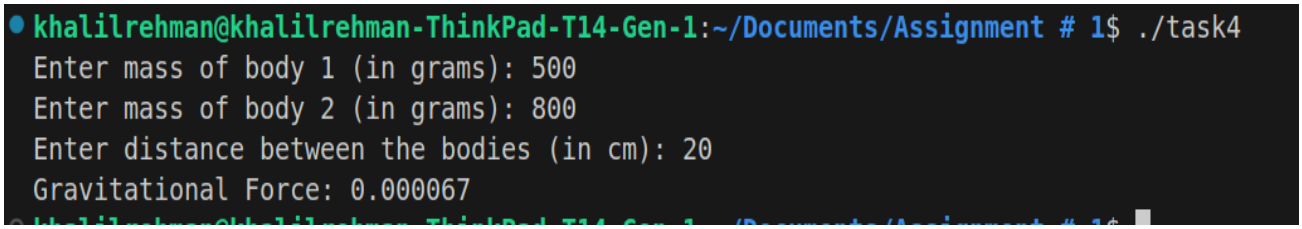


TERMINAL OUTPUT:



TASK # 04:

FLOW CHART:



TERMINAL OUTPUT: