



NUST CHIP DESIGN CENTRE

Digital Design Verification

DLD – Worksheet 2

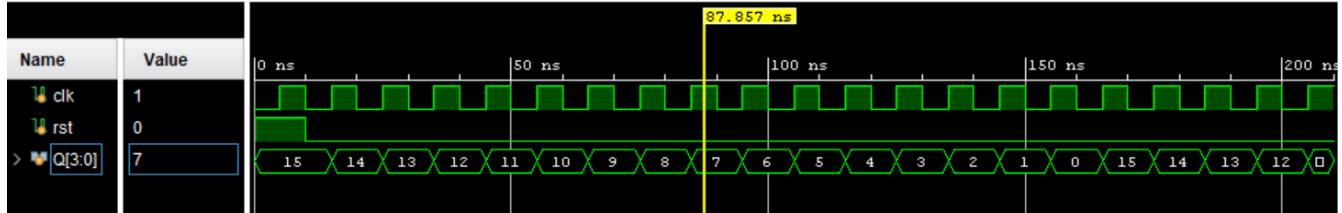
<u>Name</u>	<i><u>Khalil Rehman</u></i>
<u>Instructor</u>	<i><u>Sir Musadiq</u></i>
<u>Date</u>	<u><i>02 Aug 2025</i></u>

1. In-Lab Tasks:

i. Task 1.1:

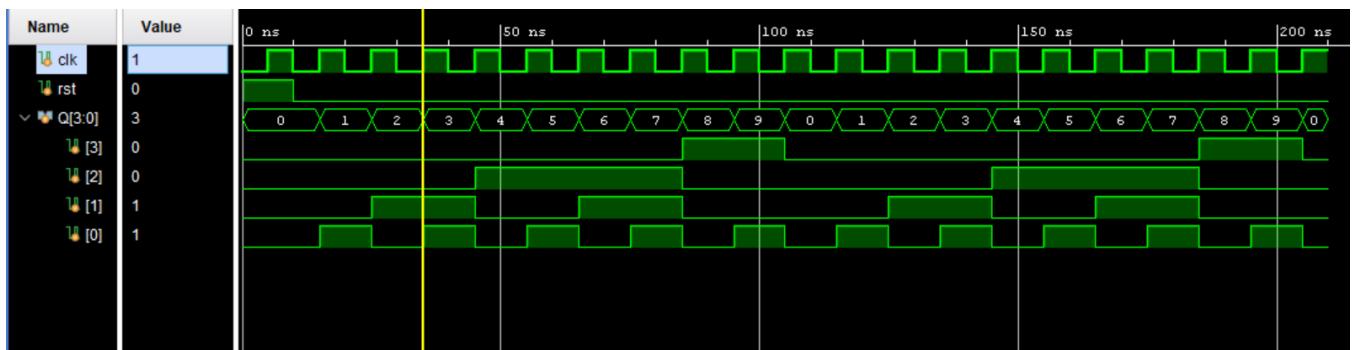
SV file attached

Waveform:



ii. Task 1.2:

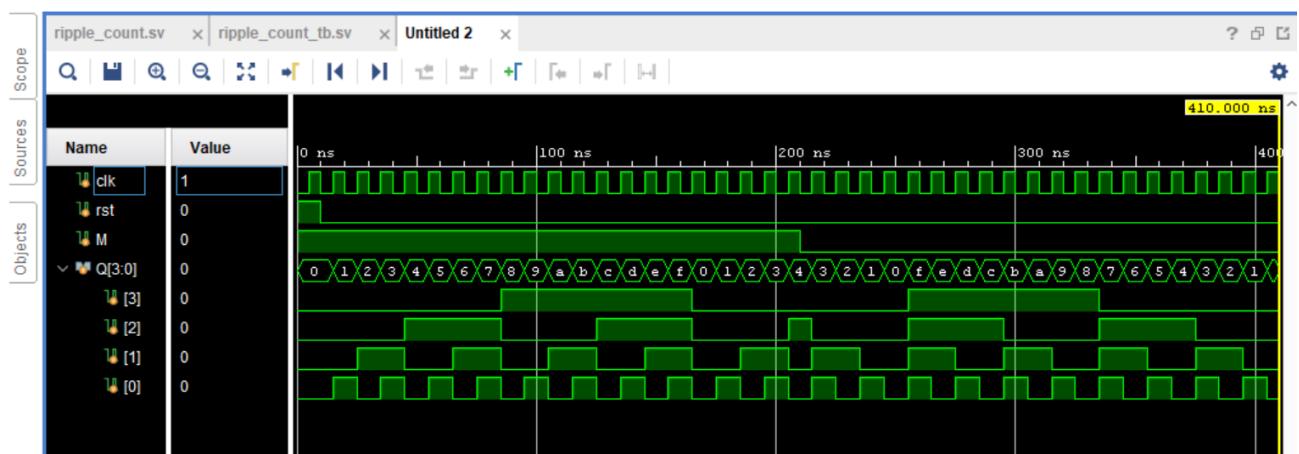
SV file Attached:



iii. Task 3:

SV file attached

Waveform:

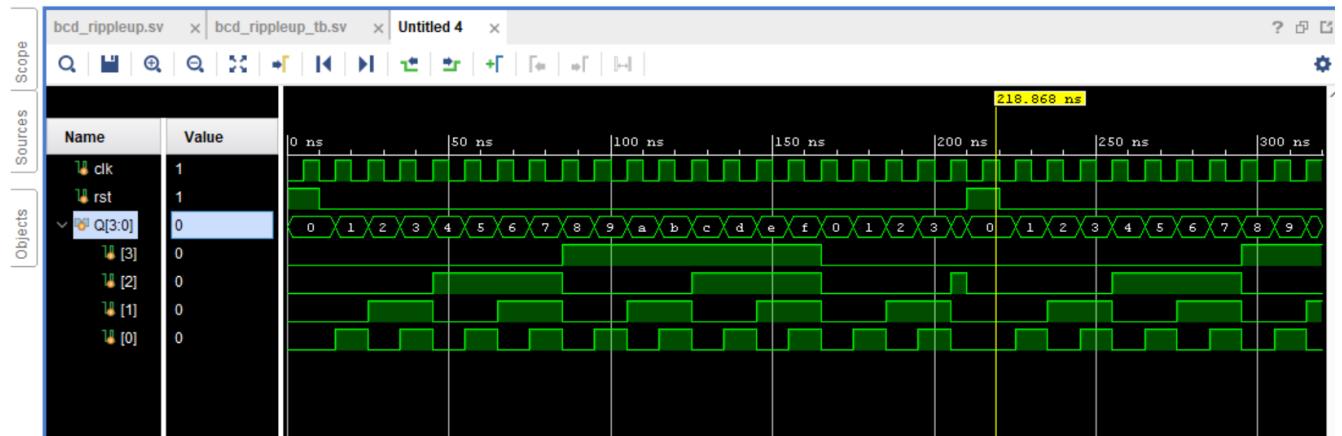


C / C++ Programming

iv. Task 3.2:

SV file Attached:

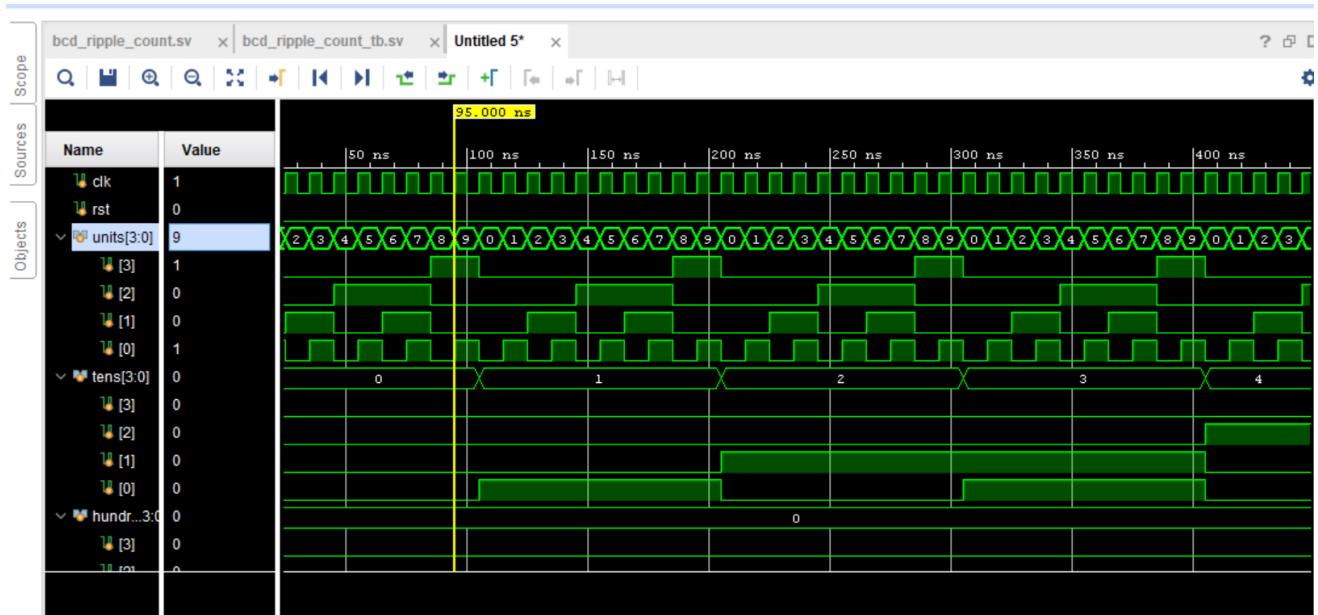
WaveForm:



v. Task 3.3:

SV file attached

Waveform:

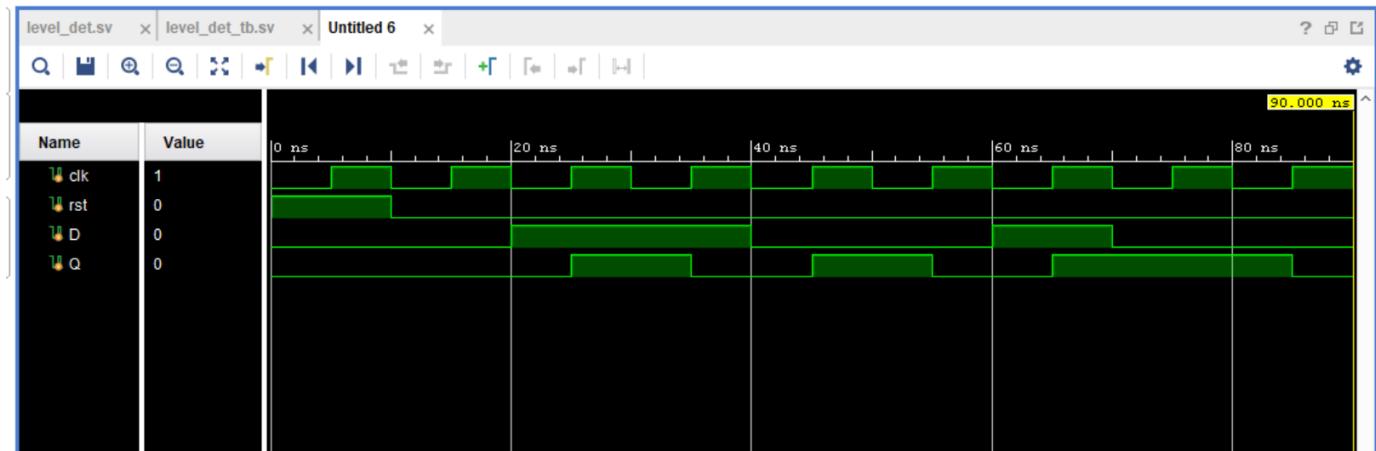


C / C++ Programming

vi. Task 4:

SV file attached:

Waveform:



vii. Task 5

Waveform:

