



NUST CHIP DESIGN CENTRE

## **Digital Design Verification**

### **DLD – Worksheet 2**

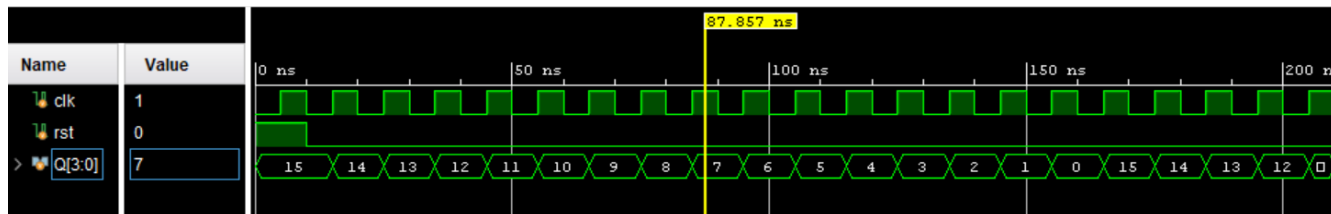
<b><u>Name</u></b>	<i><u>Khalil Rehman</u></i>
<b><u>Instructor</u></b>	<i><u>Sir Musadiq</u></i>
<b><u>Date</u></b>	<i><u>02 Aug 2025</u></i>

## 1. In-Lab Tasks:

### i. Task 1.1:

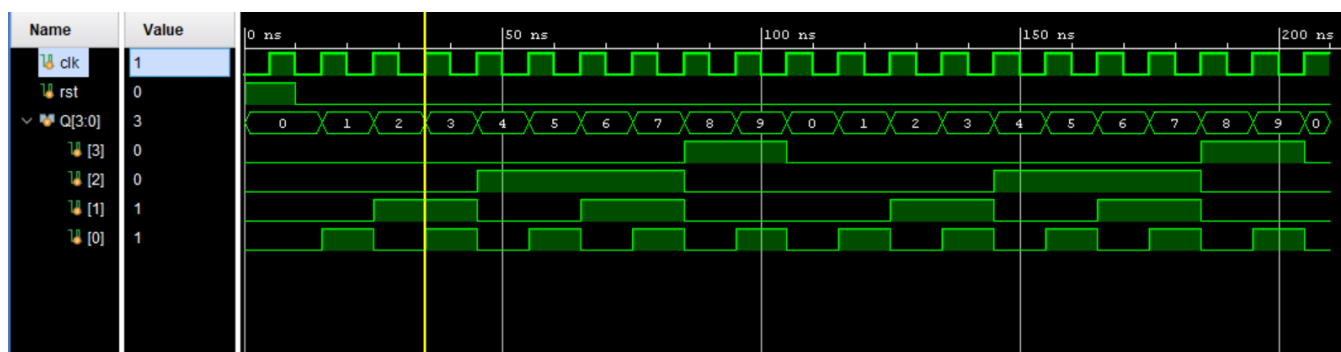
SV file attached

Waveform:



### ii. Task 1.2:

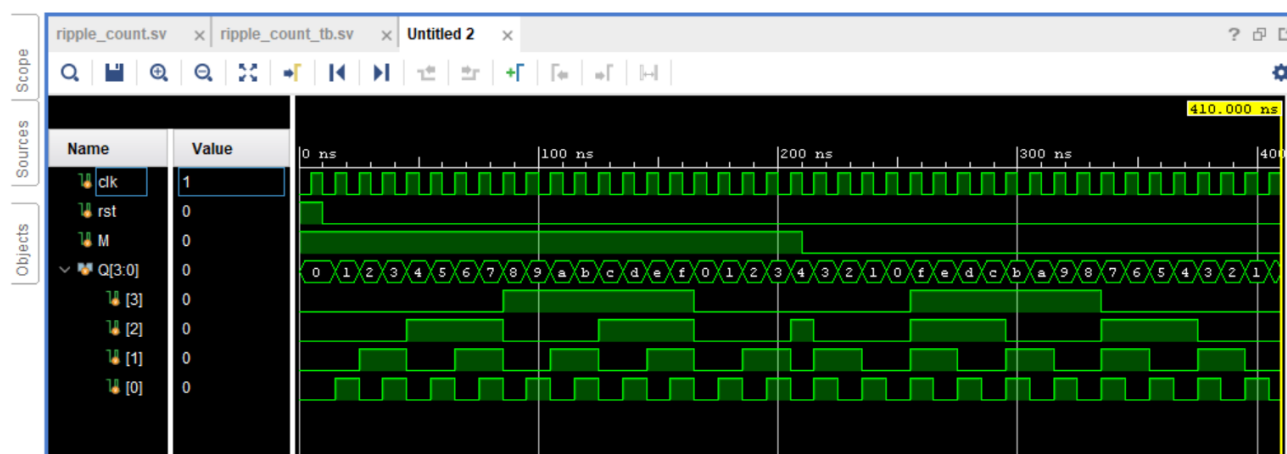
SV file Attached:



### iii. Task 3:

SV file attached

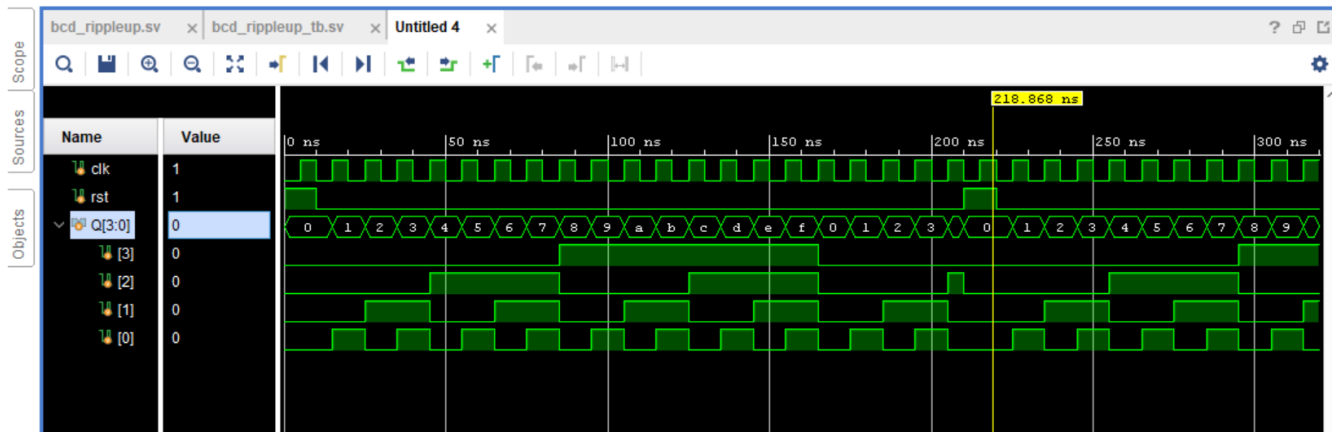
Waveform:



### iv. Task 3.2:

SV file Attached:

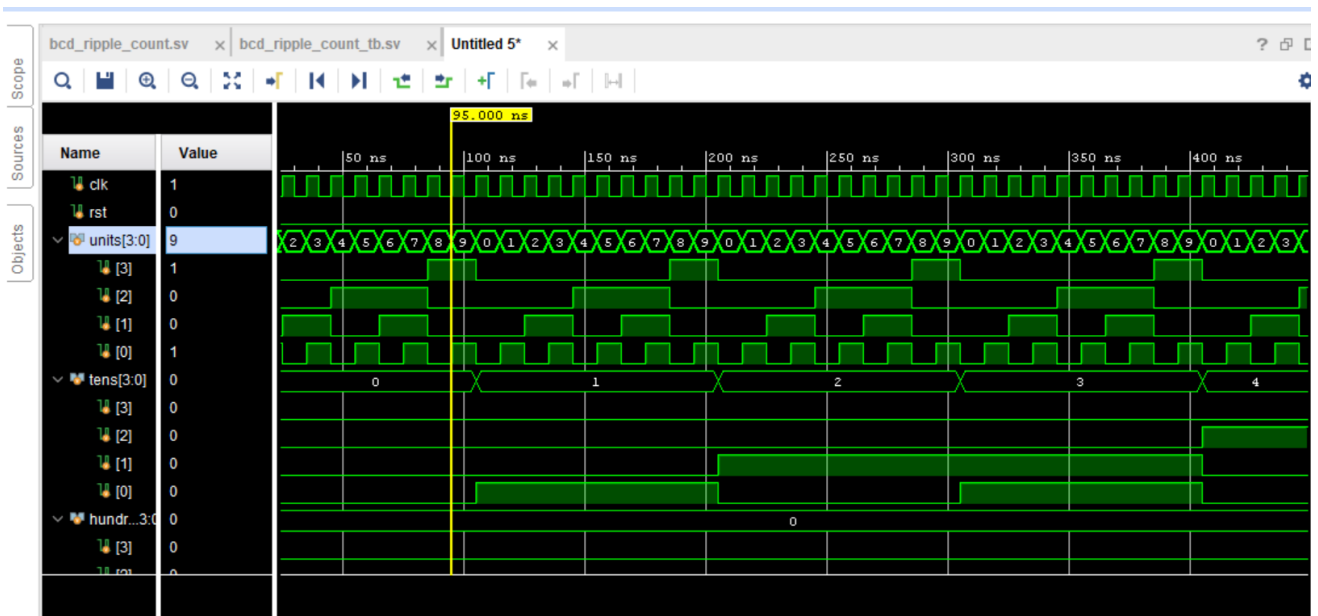
WaveForm:



### v. Task 3.3:

SV file attached

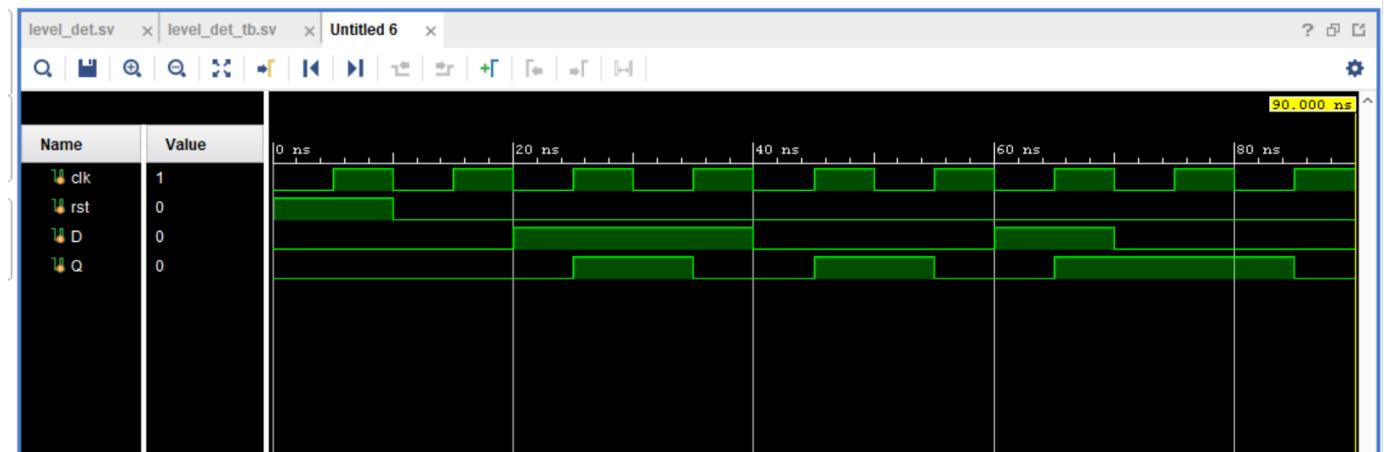
Waveform:



### vi. Task 4:

SV file attached:

Waveform:



### vii. Task 5

Waveform:

