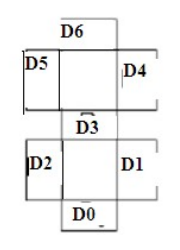
|  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| I3 | I2 | I1 | I0 | D6 | D5 | D4 | D3 | D2 | D1 | D0 |
| 0 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 1 |
| 0 | 0 | 0 | 1 | 0 | 0 | 1 | 0 | 0 | 1 | 0 |
| 0 | 0 | 1 | 0 | 1 | 0 | 1 | 1 | 1 | 0 | 1 |
| 0 | 0 | 1 | 1 | 1 | 0 | 1 | 1 | 0 | 1 | 1 |
| 0 | 1 | 0 | 0 | 0 | 1 | 1 | 1 | 0 | 1 | 0 |
| 0 | 1 | 0 | 1 | 1 | 1 | 0 | 1 | 0 | 1 | 1 |
| 0 | 1 | 1 | 0 | 1 | 1 | 0 | 1 | 1 | 1 | 1 |
| 0 | 1 | 1 | 1 | 1 | 0 | 1 | 0 | 0 | 1 | 0 |
| 1 | 0 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 1 | 1 |
| 1 | 0 | 0 | 1 | 1 | 1 | 1 | 1 | 0 | 1 | 1 |

Bảng Chân Trị:



|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
|  |  |  | I1 | I1 |  |
|  | 0 | 1 | 3 | 2 |  |
|  | 4 | 5 | 7 | 6 | I2 |
| I3 | 12 | 13 | 15 | 14 | I2 |
| I3 | 8 | 9 | 11 | 10 |  |
|  |  | I0 | I0 |  |  |

D0 = ∑ (0, 2, 3, 5, 6, 8, 9)

D0 = I3 + I1.I0’ + I1.I2’ + I0’.I2’ + I0.I1’.I2

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
|  |  |  | I1 | I1 |  |
|  | 0 | 1 | 3 | 2 |  |
|  | 4 | 5 | 7 | 6 | I2 |
| I3 | 12 | 13 | 15 | 14 | I2 |
| I3 | 8 | 9 | 11 | 10 |  |
|  |  | I0 | I0 |  |  |

D1 = ∑ (0, 1, 3, 4, 5, 6, 7, 8, 9)

D1 = I1’ + I0 + I2

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
|  |  |  | I1 | I1 |  |
|  | 0 | 1 | 3 | 2 |  |
|  | 4 | 5 | 7 | 6 | I2 |
| I3 | 12 | 13 | 15 | 14 | I2 |
| I3 | 8 | 9 | 11 | 10 |  |
|  |  | I0 | I0 |  |  |

D2 = ∑ (0, 2, 6, 8)

D2 = I0’.I2’ + I1.I0’

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
|  |  |  | I1 | I1 |  |
|  | 0 | 1 | 3 | 2 |  |
|  | 4 | 5 | 7 | 6 | I2 |
| I3 | 12 | 13 | 15 | 14 | I2 |
| I3 | 8 | 9 | 11 | 10 |  |
|  |  | I0 | I0 |  |  |

D3 = ∑ (2, 3, 4, 5, 6, 8, 9)

D3 = I3 + I2.I0’ + I1.I2’ + I2.I1’

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
|  |  |  | I1 | I1 |  |
|  | 0 | 1 | 3 | 2 |  |
|  | 4 | 5 | 7 | 6 | I2 |
| I3 | 12 | 13 | 15 | 14 | I2 |
| I3 | 8 | 9 | 11 | 10 |  |
|  |  | I0 | I0 |  |  |

D4 = ∑ (0, 1, 2, 3, 4, 7, 8, 9)

D4 = I2’ + I0’.I1’ + I0.I1

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
|  |  |  | I1 | I1 |  |
|  | 0 | 1 | 3 | 2 |  |
|  | 4 | 5 | 7 | 6 | I2 |
| I3 | 12 | 13 | 15 | 14 | I2 |
| I3 | 8 | 9 | 11 | 10 |  |
|  |  | I0 | I0 |  |  |

D5 = ∑ (0, 4, 5, 6, 8, 9)

D5 = I3 + I2.I1’ + I2.I0’ + I0’.I1’

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
|  |  |  | I1 | I1 |  |
|  | 0 | 1 | 3 | 2 |  |
|  | 4 | 5 | 7 | 6 | I2 |
| I3 | 12 | 13 | 15 | 14 | I2 |
| I3 | 8 | 9 | 11 | 10 |  |
|  |  | I0 | I0 |  |  |

D6 = ∑ (0, 2, 3, 5, 6, 7, 8, 9)

D6 = I1 + I3 + I0.I2 + I0’.I2’

Thiết kế mạch:

D1 = ~I1 + I0 + I2

D2 = ~I0 (I1 + ~I2)

D0 = I3 + ~I0 (I1 + ~I2) + I1.~I2 + I0.I2.~I1

D3 = I3 + I2 (~I0 + ~I1) + I1.~I2

D5 = I3 + I2 (~I0 + ~I1) + ~I0.~I1

D6 = I3 + I1 + (I0 XOR ~I2)

D4 = ~I2 + (I0 XOR ~I1)