# THI Kiến trúc máy tính và hợp ngữ (Thi Chung)

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Bắt đầu vào lúc Monday, 28 May 2018, 1:10 PM
            State Finished
      Kết thúc lúc Monday, 28 May 2018, 2:14 PM
   Thời gian thực 1 giờ 4 phút
Câu hỏi oldsymbol{1}
               Consider the following assembly instruction sequence
Hoàn thành
                    CMP DL, 0
                    JB x_label
Đạt điểm 1.00
                    CMP DL, 9
                    JA a_label
                    ADD DL, 30h
                    JMP x_label
               a_label:
                    CMP DL, 0Fh
                    JA x_label
                    ADD DL, 31h
               x_label:
                    MOV AL, DL
               watch point:
               Choose correct value of AL register at watch point for different value of DL?
                        85h
               DL=55h
                                    => AL=55h
               DL=0FFh 41h
                                    => AL=FFh
                        38h
               DL=10
                                     => AL=3Bh
                                     => AL=38h
                        0FFh
               DL=8
Câu hỏi 2
               Select correct match for AX (Decimal) at watch points:
Hoàn thành
                              MOV AX, 1BC
                                                                 1BC=444
                                                                 166=1010 0110b
Đạt điểm 1,00
                              MOV CL, 2
                               SHL AX, CL
               watch point #1:
                              ADD AX, 166
               watch point #2:
                              SHR AX, CL
               watch point #3:
                              SHR AX, CL
                                         => 444*(2^2)=1776=0110 1111 0000b
               watch point #1: 1064
                                         => 1776 & 166 = 160
               watch point #2: 266
                                          => 160/(2^2) = 40
               watch point #3: 266
```

<b>Câu hỏi 3</b> Hoàn thành	if the location to which the control is to be transferred lies in a segment other than the current one, then the jump instruction is called
Đạt điểm 0,50	Select one:
	o intrasegment mode
	intersegment mode
	○ intrasegment indirect mode
	intrasegment direct mode
<b>Câu hỏi 4</b> Hoàn thành	Structural components of computer include:
Đạt điểm 1,00	Select one or more:  ☑ System interconnection
	☐ Interrupt
	☑ Central processing unit
	☑ I/O
	☑ Memory
	□ DMA
<b>Câu hỏi 5</b> Hoàn thành	Which could be correct ones for the destination operand in a data movement instruction?
Đạt điểm 0,50	Select one or more:
	☐ immediate data
	☐ all choices are correct
	☑ register
	✓ memory location
	inemory location
<b>Câu hỏi 6</b> Hoàn thành	the instruction, JMP C008:2000h is an example of
Đạt điểm 0,50	Select one or more:
	☐ intrasegment mode
	☑ near jump
	☐ intersegment jump
	☑ far jump
Câu hỏi <b>7</b>	Given a row of memory image in debug
Hoàn thành	0AE8:0120 13 96 D0 E0 00 40 08 42 - 99 80 3E 20 99 00 75 24
Đạt điểm 1,00	SI = 120
,	S1 = 120 The following instruction is executed:
	_
	MOV EAX, [SI+4]  Assume the value in EAX is a 32-bit floating-point binary, what is the value of
	EAX in decimal?
	Answer: 4000
	Allower. 4000

## Câu hỏi **8** Given a code snippet: Hoàn thành int n = 10; Đạt điểm 1,00 do { n--; } while (n > 0); Which ones are the equivalent logic sequence of instructions in Assembly Select one or more: a\_label: loop a\_label □ mov cx, 10 a\_label: dec cx cmp cx,0 jz a\_label a\_label: dec cx loop a\_label a\_label: dec cx cmp cx, 0 jz e\_label jmp a\_label e\_label: Câu hỏi **9** The following sequence of instructions are executed. What is the correct value of AX, CX, DX at watch point? Hoàn thành MOV AX,30 Đạt điểm 1,00 MOV CX,FFFF MUL CX watch point: CX FFFF AXFFD0 DX 002F Câu hỏi **10** Write mask byte (in hex) to set higher 4 bits in a byte value with OR instruction (LSB is the 1st bit). Không trả lời Đạt điểm 0,50 Answer: F0

<b>Câu hỏi 11</b> Hoàn thành	After executing PUSH EAX instruction, the stack pointer
Đạt điểm 0,50	Select one:
	O increment by 1
	<ul><li>decrements by 4</li></ul>
	O decrement by 1
	increment by 2
Câu hỏi <b>12</b>	
Không trả lời	Given an assembly code copying the memory buffer Buff1 to Buff2:
Đạt điểm 1,00	PUSH DS POP ES
Dat diem 1,00	LEA SI, Buff1
	LEA DI, Buff2
	MOV CX,20 ; Start of block
	cp_loop:
	MOV AL, Byte Ptr [SI]
	MOV Byte Ptr ES:[DI], AL
	INC SI INC DI
	LOOP cp_loop
	;End of block
	Choose equivalent string operations in place of block
	Select one or more:
	□ CLD
	cp_loop: MOVSB
	LOOP cp_loop
	□ STD
	cp_loop:
	MOVSB LOOP cp_loop
	CLD cp_loop:
	REP MOVSB
	LOOP cp_loop
	☐ CLD REP MOVSB
Câu hỏi <b>13</b>	the instruction that is used as prefix to an instruction to execute it repeatedly until the CX register becomes zero is
Hoàn thành	until the CA register becomes zero is
Đạt điểm 0,50	Select one:
	○ CMPS
	○ SCAS
	○ CMPS
	⊚ REP
Câu hỏi <b>14</b>	Write mask byte (in hex) to clear all the lower 7 bits of a byte value with AND
Hoàn thành	instruction.
Đạt điểm 0,50	
Sac areni 0,30	Answer: AND AL, 011111111B

		- C 1
Câu hỏi 15 Không trả lời	Convert -89.2345 to IEEE 32-bit floating point format (1 sign+ 8 exponent + 23 mantissa) in hex	S=1 E=exponent + bias =7+(2^7-1)=134=1000 0100b
Đạt điểm 1,00	Answer: 1   1000 0100   000 1001 0010 0011 0100 0101	M=0001 0010 0100 0110 1000 101b
Câu hỏi <b>16</b> Không trả lời	Given a row of memory image in debug	
	072C:FFF0 00 00 00 01 00 00 2C 07 - 07 01 2C 07 17 72 00 00	
Đạt điểm 1,50	SS=072C, SP=FFF8, DS = 072C	
	Assume the stack now stores two (2) 16-bit parameters and one (1) 16-bit return address in following order: stack top (return address) >> parameter #1 >> parameter #2.	
	The following sequence of instructions are executed. What is the correct values at watch points?	
	MOV BP, SP	
	watch point #1 (BP):	
	MOV AX, [BP+2]	
	watch point #2 (AX):	
	ADD AX, [BP+4]	
	watch point #3 (AX):	
	MOV DI, 120	
	MOV [DI], AX	
	watch point #1:	
	watch point #2:	
	watch point Chọn ∨	
Câu hỏi 17 Hoàn thành	The instruction that subtracts 1 from the contents of the specified register/memory location is	
Đạt điểm 0,50	Select one:	
	DEC	
	○ SUB	
	○ SBB	
	○ INC	

Câu hỏi <b>18</b>	Memory dump at 1D20:0200 shown as below:
Không trả lời	1D20:0200 00 20 10 5D 55 47 00 90 - 00 10 20 30 40 50 60 70
Đạt điểm 1,00	Given value of registers:
	DS = 1D20, ES = 1D20, DI = 20A
	The following sequence of instructions are executed:
	MOV SI,208h
	MOV AX,0040h
	MOV CX,000Ah
	CLD
	REPNZ SCASB
	watch point:
	What is the correct value of AX, SI, DI registers at watch point?
	DI Chọn 🗸
	AX Chọn 🗸
	SI Chọn ∨
Câu hỏi <b>19</b>	What is the growing of Angletille law in any angletic and a planting?
doàn thành	What is the meaning of Amdahl's law in processor performance evaluation?
Dạt điểm 1,00	Select one:
Dật tiem 1,00	the cost reduce when moving from single-core to multicore processor
	<ul><li>the maximum speedup of a multicore processor</li></ul>
	O the potential speedup of a program using multiple processor compared to
	a single processor
	the speedup of a multicore processor when increasing system bus speed
	1
<b>Câu hỏi 20</b> Hoàn thành	Which are the correct actions for LODSW string operation if DF is reset (=0)
Đạt điểm 0,50	Select one or more:
gạt diem 0,50	☐ decrease DI by 2
	☐ Load 16-bit value at memory location pointed by ES:[DI] into AX
	☑ increase SI by 2
	☑ Load 16-bit value at memory location pointed by DS:[SI] into AX
Câu hỏi <b>21</b>	When many devices of different transmission speed connect to the same bus,
Không trả lời	the overall system performance suffers. How did the design engineers resolved this:
Đạt điểm 1,00	uns.
	Select one:
	O PCI Express bus
	<ul> <li>Multiple-Bus hierarchies</li> </ul>
	○ PCI bus
	Split system bus into local bus and memory bus

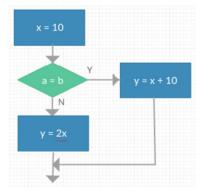
<b>Câu hỏi 22</b> Hoàn thành	the instruction, CMP to compare source and destination operands by
Đạt điểm 0,50	Select one:
	o adding
	○ comparing
	○ dividing
	<ul><li>subtracting</li></ul>
Câu hỏi <b>23</b>	To balance the super speed of CPU with the slow response of memory, which
Hoàn thành	of the following measures have been made by engineers in system design?
Đạt điểm 1,00	Select one or more:
	☐ Make use of both on-chip and off-chip cache memory
	☑ Make wider data bus path
	Using higher-speed bus and us hierarchy
	☑ To move data directly by DMA
Câu hỏi <b>24</b>	The following acquence of instructions are executed. What is the courset
Hoàn thành	The following sequence of instructions are executed. What is the correct value of AX, DX at watch point?
Đat điểm 1,00	MOV DL,FF
24c diciii 1/00	MOV AL,42
	IMUL DL
	watch point:
	AX = FFBE Y
	DX 0000 V
Câu hỏi <b>25</b>	In the DCD instruction, the contents of the destination or according to a
Hoàn thành	In the RCR instruction, the contents of the destination operand undergoes function as
Đạt điểm 0,50	Select one:
, ,	carry flag is pushed into LSB then MSB is pushed into carry flag
	overflow flag is pushed into MSB then LSB is pushed into carry flag
	<ul><li>overflow flag is pushed into MSB then LSB is pushed into carry flag</li><li>carry flag is pushed into MSB then LSB is pushed into carry flag</li></ul>
Câu hỏi <b>26</b>	<ul> <li>carry flag is pushed into MSB then LSB is pushed into carry flag</li> <li>auxiliary flag is pushed into LSB then MSB is pushed into carry flag</li> </ul>
<b>Câu hỏi 26</b> Hoàn thành	carry flag is pushed into MSB then LSB is pushed into carry flag
Hoàn thành	<ul> <li>carry flag is pushed into MSB then LSB is pushed into carry flag</li> <li>auxiliary flag is pushed into LSB then MSB is pushed into carry flag</li> <li>Which could be correct ones for the source operand in an instruction?</li> <li>Select one or more:</li> </ul>
	<ul> <li>carry flag is pushed into MSB then LSB is pushed into carry flag</li> <li>auxiliary flag is pushed into LSB then MSB is pushed into carry flag</li> <li>Which could be correct ones for the source operand in an instruction?</li> <li>Select one or more:</li> <li>immediate data</li> </ul>
Hoàn thành	<ul> <li>carry flag is pushed into MSB then LSB is pushed into carry flag</li> <li>auxiliary flag is pushed into LSB then MSB is pushed into carry flag</li> <li>Which could be correct ones for the source operand in an instruction?</li> <li>Select one or more:</li> </ul>
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Hoàn thành	<ul> <li>carry flag is pushed into MSB then LSB is pushed into carry flag</li> <li>auxiliary flag is pushed into LSB then MSB is pushed into carry flag</li> <li>Which could be correct ones for the source operand in an instruction?</li> <li>Select one or more:</li> <li>immediate data</li> <li>memory location</li> </ul>
Hoàn thành Đạt điểm 0,50	<ul> <li> carry flag is pushed into MSB then LSB is pushed into carry flag</li> <li> auxiliary flag is pushed into LSB then MSB is pushed into carry flag</li> <li>Which could be correct ones for the source operand in an instruction?</li> <li>Select one or more:</li> <li> immediate data</li> <li> memory location</li> <li> indirect data</li> <li> register</li> </ul>
Hoàn thành	<ul> <li> carry flag is pushed into MSB then LSB is pushed into carry flag</li> <li> auxiliary flag is pushed into LSB then MSB is pushed into carry flag</li> <li>Which could be correct ones for the source operand in an instruction?</li> <li>Select one or more:</li> <li> immediate data</li> <li> memory location</li> <li> indirect data</li> <li> register</li> <li>Convert the 32-bit floating point number A3358000 (in hex) to decimal.</li> </ul>
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Hoàn thành Đạt điểm 0,50 Câu hỏi <b>27</b>	<ul> <li> carry flag is pushed into MSB then LSB is pushed into carry flag</li> <li> auxiliary flag is pushed into LSB then MSB is pushed into carry flag</li> <li>Which could be correct ones for the source operand in an instruction?</li> <li>Select one or more:</li> <li> immediate data</li> <li> memory location</li> <li> indirect data</li> <li> register</li> <li>Convert the 32-bit floating point number A3358000 (in hex) to decimal.</li> </ul>
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watch p #2: watch p #1:   âu hỏi 29 oàn thành at điểm 0,50  What is 01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 80  AL 80  âu hỏi 31 oàn thành Select to I dec	All - 23 *
watch p watch p watch p #2: watch p #1:  au hỏi 29 oàn thành at điểm 0,50  What is Oi: com poin incr com poin incr com poin Si 04: Loo O5: O6: O7: O8: watch p  SI 8I AL 8I =  au hỏi 31 Select t are in h at điểm 1,00  Step 1:	point #1:  ADD AH, 10  point #2: $AH = 25 \lor$ point $AL = 6C \lor$ are the correct actions for SCASW string operation if DF is set (=1)
watch p #2: watch p #1:  au hoi 29 oàn thành at diểm 0,50  which a Select c com poin  incr com poin  02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 84 AL 81  au hoi 31 oàn thành at diểm 1,00  Step 1:	ADD AH, 10  point #2:   AH = 25 \times  point AL = 6C \times  are the correct actions for SCASW string operation if DF is set (=1)
watch p #2: watch p #1:  Select c ode compoin incr compoin incr compoin incr compoin incr compoin incr compoin Si au hòi 30 Obàn thành at diểm 1,00 O5: O6: O7: O8: watch p SI 8I AL 8I =  Select t are in h Step 1:	noint #2:  AH = 25 \times  Noint AL = 6C \times  AL = 6C \times  Are the correct actions for SCASW string operation if DF is set (=1)
watch p #2: watch p #1:  Select c or compoin incr compoin incr compoin at diém 1,00  What is 01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 8I AL 8I =  Select t are in h Step 1:	ooint AH = 25 \rightarrow  ooint AL = 6C \rightarrow  are the correct actions for SCASW string operation if DF is set (=1)
#2: watch p #1:  watch p #1:  watch p #1:  Select c compoin compoin compoin what is 01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 8t  AL 8t  au hói 31 oàn thành at diểm 1,00  Step 1:	ooint AH = 25 \rightarrow  Ooint AL = 6C \rightarrow  One the correct actions for SCASW string operation if DF is set (=1)
#2: watch p #1:  watch p #1:  Select c compoin	or the correct actions for SCASW string operation if DF is set (=1)
#2: watch p #1:  watch p #1:  watch p #1:  Select c compoin compoin compoin what is 01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 8t  AL 8t  au hói 31 oàn thành at diểm 1,00  Step 1:	or the correct actions for SCASW string operation if DF is set (=1)
#2: watch p #1:  watch p #1:  watch p #1:  Select c compoin compoin compoin what is 01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 8t  AL 8t  au hói 31 oàn thành at diểm 1,00  Step 1:	or the correct actions for SCASW string operation if DF is set (=1)
watch p #1:  Au hỏi 29 oàn thành lạt diễm 0,50  Which a Select c  ✓ com poin  incr com poin  Oàn thành lật diễm 1,00  What is 01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 84  AL 81  au hỏi 31 oàn thành lật diễm 1,00  Step 1:	ore the correct actions for SCASW string operation if DF is set (=1)
#1:  #1:  #1:  #1:  #1:  #1:  #1:  #1:	are the correct actions for SCASW string operation if DF is set (=1)
Câu hỏi 29 doàn thành Dạt diểm 0,50  Which a  Select c  ☑ com poin  ☐ incr ☐ com poin  Câu hỏi 30  doàn thành Dạt diểm 1,00  What is  01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 8!  AL 8!  Eâu hỏi 31  doàn thành Dạt diểm 1,00  Step 1:	are the correct actions for SCASW string operation if DF is set (=1)
Select c  dec  dec  dec  dec  dec  dec  dec  d	
Select c  dec  compoin  incr compoin  Sau hói 30  What is  01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 8/ AL 8/ =  Select t are in h  sat diém 1,00  Step 1:	
oàn thành at điểm 0,50  Select c  ✓ dec  ✓ com poin  incr  ☐ com poin   30  What is  01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 8i  AL 8i  =  âu hỏi 31  Select t are in h at điểm 1,00  Step 1:	
Select compoin decompoin d	ne or more:
☐ dec ☐ com ☐ poin ☐ incr ☐ com ☐ poin ☐ incr ☐ com ☐ poin ☐ 02: 03: 04: Loo 05: 06: 07: 08: watch p SI 80 AL 80 ☐ at diểm 1,00  Step 1:	
poin  poin  incr corr poin  30  what is  01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 80  AL 80  au hòi 31 oàn thành at diểm 1,00  Step 1:	rease DI by 2
incr   compoin	pare the value in AX register with 16-bit value at the memory location
□ com poin  Câu hỏi 30  Hoàn thành Dạt điểm 1,00  What is 01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 8!  AL 8!  Eâu hỏi 31 Hoàn thành Dạt điểm 1,00  Step 1:	ted by ES:[DI] and set/clear flag bits accordingly
poin  Câu hỏi 30  Hoàn thành Dạt điểm 1,00  What is  01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 8(  AL 8(  =  Câu hỏi 31 Hoàn thành Dạt điểm 1,00  Step 1:	ease DI by 2
Câu hỏi 30 Hoàn thành Dạt điểm 1,00  What is 01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 8( AL 8( =  Câu hỏi 31 Hoàn thành Dạt điểm 1,00  Step 1:	pare the value in AX register with 16-bit value at the memory location ted by DS:[SI] and set/clear flag bits accordingly
10 1: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 80  AL 80  Câu hỏi 31  Hoàn thành Dạt điểm 1,00  Step 1:	
01: 02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 8I  AL 8I  atau hòi 31 shành bat diểm 1,00  Step 1:	
02: 03: 04: Loo 05: 06: 07: 08: watch p  SI 80  AL 80  Eâu hỏi 31 Ioàn thành Oạt điểm 1,00  Step 1:	the correct value of SI, AL (in hex) at watch point:
03: 04: Loo 05: 06: 07: 08: watch p SI 88  AL = 10àn hỏi 31 shoàn thành Dạt điểm 1,00 Step 1:	MOV AL 10h
04: Loo 05: 06: 07: 08: watch p SI 88  AL = 10àn hỏi 31 Hoàn thành Oạt điểm 1,00 Step 1:	MOV AL, 10h MOV CX, 7
06: 07: 08: watch p SI 80 AL 80 =  Câu hỏi 31 Hoàn thành Dạt điểm 1,00  Step 1:	p_label:
07: 08: watch p SI 80 AL 81 = Câu hỏi 31 Hoàn thành Dạt điểm 1,00 Step 1:	MOV [SI], AL
watch p  SI 8I  AL 8I  =  Câu hỏi 31  Hoàn thành  Đạt điểm 1,00  Step 1:	ADD AL,10h INC SI
SI 80  AL 80  = Select t are in h  Opt diểm 1,00  Step 1:	LOOP Loop_label
AL = 80  Câu hỏi 31  Hoàn thành Dạt điểm 1,00  Step 1:	oint:
AL 80  Câu hỏi 31  Hoàn thành Dạt điểm 1,00  Step 1:	
= Select t are in h	oh v
= Select t are in h	26
doàn thành are in h	<u>ш</u> ,
doàn thành are in h	
doàn thành are in h	he correct coguence of instructions to compute 1034/139 (all velve-
Dạt điểm 1,00 Step 1:	he correct sequence of instructions to compute -1024/128 (all values ex).
Step 1:	
Step 2:	CWD
Step 2:	
	1004 04 00
Step 3:	MOV CX,80 V
Step 4:	MOV CL,80 V
	MOV CL,80 V
	MOV CL,80 V

âu hỏi <b>32</b>	Select correct match for AL and carry flag at watch point #1:	
oàn thành	MOV BL, 8C	
ạt điểm 1,00	MOV AL, 7E	
	ADD AL, BL	
	watch point #1:	
	AL OA ~	
	Carry set v	
	flag	

## Câu hỏi **33** Hoàn thành Đạt điểm 1,00

### Given a flowchart of an algorithm:



Select the correct instruction sequence:

### Select one or more:

- mov dl,10
  cmp al,bl
  jnz n\_label
  add dl,10
  jmp e\_label
  n\_label:
  - n\_label: mov cl,1 shl dl,cl
- e\_label: mov dh,dl
- mov dl,10
  cmp al,bl
  jnz n\_label
  add dl,10
  mov dh,dl
  jmp e\_label
- n\_label: mov cl,1 shl dl,cl
- e\_label: mov dh,dl
- mov dl,10
  cmp al,bl
  jnz n\_label
  add dl,10
  jmp e\_label
  n\_label:
  - mov cl,1 shr dl,cl e\_label:
- mov dh,dl

  mov dl,10
- ☐ mov dl,10 cmp al,bl jz n\_label mov cl,1 shl dl,cl jmp e\_label n\_label:

add dl,10 e\_label: mov dh,dl

Hoàn thành Đạt điểm 0,50 Câu hỏi <b>36</b>	Select one:     decrements by 4     decrements by 2     increments by 4     increment by 1  Sign-extend number BF (8-bit binary) to 16-bit. Write result in hex  Answer: 191
Câu hỏi 35 Hoàn thành Đạt điểm 0,50  Câu hỏi 36 Hoàn thành	<ul> <li>decrements by 2</li> <li>increments by 4</li> <li>increment by 1</li> </ul> Sign-extend number BF (8-bit binary) to 16-bit. Write result in hex
Hoàn thành Đạt điểm 0,50 Câu hỏi <b>36</b>	<ul> <li>increments by 4</li> <li>increment by 1</li> </ul> Sign-extend number BF (8-bit binary) to 16-bit. Write result in hex
Hoàn thành Đạt điểm 0,50 Câu hỏi <b>36</b>	increment by 1  Sign-extend number BF (8-bit binary) to 16-bit. Write result in hex
Hoàn thành Đạt điểm 0,50 Câu hỏi <b>36</b>	Sign-extend number BF (8-bit binary) to 16-bit. Write result in hex
Hoàn thành Đạt điểm 0,50 Câu hỏi <b>36</b>	
Hoàn thành Đạt điểm 0,50 Câu hỏi <b>36</b>	
Đạt điểm 0,50 Câu hỏi <b>36</b>	Answer: 191
Câu hỏi <b>36</b>	Answer: 191
	Which of the following instructions are not valid?
	which of the following instructions are not value:
Đạt điểm 0,50	Select one or more:
Dực diem 0/30	☑ MOV DS, B800h
	☐ MOV AX, [BP+2]
	☑ MOV SP, SS:[SI+2]
	☐ MOV AX, SI
Câu hỏi <b>37</b>	The following sequence of instructions are executed. What is the correct
Hoàn thành	value of flag bits at watch point?
Đạt điểm 1,00	MOV AL, 0F
	ADD AL, F1
	watch point:
	Zero flag (OF) reset
	= Teset
	Carry flag set
	(CF) =
20	
<b>Câu hỏi 38</b> Hoàn thành	Major structural components of the CPU include:
	Select one or more:
Đạt điểm 1,00	☑ Registers
	☑ Arithmetic and Logic Unit
	☐ Instruction Pointer (PC)
	☑ Interconnections
	☐ Control Unit
	☐ Instruction Register
Câu hỏi <b>39</b>	Consider a magnetic disk drive with 8 surfaces, 512 tracks per surface, and 64
Hoàn thành	sectors per track. Sector size is 1 kB. What is the disk capacity
Đạt điểm 1,00	. [
	Answer: 512 KB

Câu hỏi 40 Hoàn thành	What best describe the Spatial and Temporal Locality?
Đạt điểm 1,00	Tempor al be exploited by keeping recently used instruction and data in cache memory and by exploiting a cache hierarchy
	locality
	Spatial be exploited by moving data between cache and memory more efficient
	locality
Câu hỏi <b>41</b>	Given a code snippet:
Hoàn thành	int ax, bx;
Đạt điểm 1,00	
	if $(ax \ge bx)$
	ax -=bx;
	else
	bx -=ax;
	What is the equivalent logic sequence of instructions in Assembly
	Select one:
	jl a_label
	sub ax,bx jmp x_label
	a_label:
	sub bx,ax x_label:
	<pre>cmp ax,bx jbe a_label</pre>
	sub ax,bx
	jmp x_label
	a_label: sub bx,ax
	x_label:
	○ cmp ax,bx
	ja a_label sub ax,bx
	jmp x_label
	a_label:
	sub bx,ax
	x_label:
	cmp ax,bx jge a_label
	sub ax,bx
	jmp x_label
	a_label: sub bx,ax
	x_label:
Câu hỏi <b>42</b>	Which of the following is not a data convitance for instruction:
Hoàn thành	Which of the following is not a data copy/transfer instruction?
Đạt điểm 0,50	Select one or more:
Dạt uleili 0,30	☑ ADC
	□ MOV
	□ LEA
	☑ DAS
	Return to: General ◆