

# BÀI BÁO CÁO TUẦN 10

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MSSV: 20210284

## *Assignment 1:*

### CODE:

```
.eqv SEVENSEG_LEFT 0xFFFF0011 # Địa chỉ của đèn led
7 đoạn trái.

.eqv SEVENSEG_RIGHT 0xFFFF0010 # Địa chỉ của đèn
led 7 đoạn phải

.text

main:

# MSSV 20210284 => 2 số cuối là 84

# số 8 khi chuyển sang đèn led 7 đoạn : 0111 1111
(bit) = 7F (hex)

# số 4 khi chuyển sang đèn led 7 đoạn : 0110 0110
(bit) = 66 (hex)

    li    $a0, 0x7F    # set value for segments
    jal    SHOW_7SEG_LEFT # show
    nop

    li    $a0, 0x66    # set value for segments
    jal    SHOW_7SEG_RIGHT # show
    nop

exit:    li    $v0, 10
        syscall
```

```
endmain:
```

```
SHOW_7SEG_LEFT:
```

```
li $t0, SEVENSEG_LEFT # assign port's address
```

```
sb $a0, 0($t0) # assign new value
```

```
nop
```

```
jr $ra
```

```
nop
```

```
SHOW_7SEG_RIGHT:
```

```
li $t0, SEVENSEG_RIGHT # assign port's address
```

```
sb $a0, 0($t0) # assign new value
```

```
nop
```

```
jr $ra
```

```
nop
```

## Kết quả:

The screenshot displays the Digital Lab Sim interface, showing the execution of MIPS assembly code. The main window is divided into several panels:

- Text Segment:** Displays the assembly code with addresses and sources. The code includes labels like `main`, `exit`, `endmain`, `SHOW_7SEG_LEFT`, and `SHOW_7SEG_RIGHT`.
- Data Segment:** Shows memory values for addresses ranging from `0x10010000` to `0x10010140`.
- Registers:** Shows the state of registers, including `$8 (vaddr)`, `$12 (status)`, `$13 (cause)`, and `$14 (epc)`.
- Digital Lab Sim:** A window showing the 7-segment display output, which displays the number `8.8`.

The Digital Lab Sim window also includes a numeric keypad and a "Tool Control" section with buttons for "Disconnect from MIPS", "Reset", "Help", and "Close".

## Assignment 2

### CODE:

```
.eqv SEVENSEG_LEFT 0xFFFF0011 # Dia chi cua den led
7 doan trai.

.eqv SEVENSEG_RIGHT 0xFFFF0010 # Dia chi cua den
led 7 doan phai

.data

    nhapso: .ascii "Nhap so nguyen n = "

.text

main:
    li    $v0, 4
    la    $a0, nhapso
    syscall
    li    $v0, 5
    syscall
    add $s1, $v0, $zero # n = $s1
    div $s2, $s1, 10
    mfhi    $t1    # so hang don vi
    div $s3, $s2, 10
    mfhi    $t2    # so hang chuc
    addi    $s4, $zero, -1 # khoi tao check = -1
    add $t3, $zero, $t2 # $t3 = so hang chuc

Check_so:
So_0:    bne $t3, 0, So_1
```

```
        li    $a0, 0x3F
        j     in_so
So_1:   bne   $t3, 1, So_2
        li    $a0, 0x06
        j     in_so
So_2:   bne   $t3, 2, So_3
        li    $a0, 0x5B
        j     in_so
So_3:   bne   $t3, 3, So_4
        li    $a0, 0x4F
        j     in_so
So_4:   bne   $t3, 4, So_5
        li    $a0, 0x66
        j     in_so
So_5:   bne   $t3, 5, So_6
        li    $a0, 0x6D
        j     in_so
So_6:   bne   $t3, 6, So_7
        li    $a0, 0x7D
        j     in_so
So_7:   bne   $t3, 7, So_8
        li    $a0, 0x07
        j     in_so
So_8:   bne   $t3, 8, So_9
```

```

        li    $a0, 0x7F
        j     in_so
So_9:    bne   $t3, 9, exit
        li    $a0, 0x6F
in_so:
        beq   $s4, $zero, in_so_don_vi # if check = -1 in
so hang chuc
                                   # if check = 0 in so hang don
vi
in_so_chuc:
        jal   SHOW_7SEG_LEFT  # show
        nop
        addi   $s4, $s4, 1
        add    $t3, $zero, $t1 # $t3 = so hang don vi
        j     Check_so

in_so_don_vi:
        jal   SHOW_7SEG_RIGHT # show
        nop
exit:    li    $v0, 10
        syscall
endmain:

SHOW_7SEG_LEFT:
        li    $t0, SEVENSEG_LEFT # assign port's address

```

```
sb $a0, 0($t0)      # assign new value
```

```
nop
```

```
jr $ra
```

```
nop
```

SHOW\_7SEG\_RIGHT:

```
li $t0, SEVENSEG_RIGHT # assign port's address
```

```
sb $a0, 0($t0)      # assign new value
```

```
nop
```

```
jr $ra
```

```
nop
```

## Kết quả:

TH1: Khi nhập số có 1 chữ số:

Nhap so nguyen n = 1

The screenshot shows the Digital Lab Sim software interface. The main window displays the Text Segment with assembly instructions and their addresses. The Data Segment shows memory values. The Registers window on the right shows the status of registers. The bottom panel shows the 7-segment display output '0.8' and the input 'Nhap so nguyen n = 1'.

Label	Address
main	0x00400000
Check_so	0x00400044
So_0	0x00400044
So_1	0x00400054
So_2	0x00400064
So_3	0x00400074
So_4	0x00400084
So_5	0x00400094
So_6	0x004000a4
So_7	0x004000b4
So_8	0x004000c4

Name	Number	Value
\$8 (vaaddr)	8	0x00000000
\$12 (status)	12	0x0000ff11
\$13 (cause)	13	0x00000000
\$14 (epc)	14	0x00000000

Digital Lab Sim, Version 1.0 (Didier Teireto)

0.8

Nhap so nguyen n = 1

-- program is finished running --

Clear

Tool Control: Disconnect from MIPS, Reset, Help, Close

TH2: Khi nhập một số có 2 chữ số:

Nhap so nguyen n = 25

**Text Segment**

Bkpt	Address	Code	Basic	Source
0x00400000	0x24020004	addiu \$2,\$0,0x00000004	8: li \$v0,4	
0x00400004	0x3c011001	lui \$1,0x00001001	9: la \$a0,nhapso	
0x00400008	0x34240000	ori \$4,\$1,0x00000000	10: syscall	
0x0040000c	0x0000000c	syscall	11: li \$v0,5	
0x00400010	0x24020005	addiu \$2,\$0,0x00000005	12: syscall	
0x00400014	0x0000000c	syscall	13: add \$s1,\$v0,\$zero # n = \$s1	
0x00400018	0x00408820	add \$17,\$2,\$0	14: div \$s2,\$s1,10	
0x0040001c	0x2001000a	addi \$1,\$0,0x0000000a		
0x00400020	0x0221001a	div \$17,\$1		
0x00400024	0x00009012	mflw \$18		
0x00400028	0x00004810	mflw \$9	15: mflw \$t1 # so hang don vi	
0x0040002c	0x2001000a	addi \$1,\$0,0x0000000a	16: div \$s3,\$s2,10	
0x00400030	0x0241001a	div \$18,\$1		

**Data Segment**

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x7061684e	0x206f7320	0x7975676e	0x6e206e65	0x00203d20	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

**Registers**

Name	Number	Value
\$8 (vaddr)	8	0x00000000
\$12 (status)	12	0x0000ff11
\$13 (cause)	13	0x00000000
\$14 (epc)	14	0x00000000

**Mars Messages**

```
-- program is finished running --
Nhap so nguyen n = 25
-- program is finished running --
```

**Digital Lab Sim**

25

0 1 2 3  
4 5 6 7  
8 9 a b  
c d e f

Tool Control: Disconnect from MIPS, Reset, Help, Close

TH3: Khi nhập một số có 3 chữ số:

Nhap so nguyen n = 789

**Text Segment**

Bkpt	Address	Code	Basic	Source
0x00400000	0x24020004	addiu \$2,\$0,0x00000004	8: li \$v0,4	
0x00400004	0x3c011001	lui \$1,0x00001001	9: la \$a0,nhapso	
0x00400008	0x34240000	ori \$4,\$1,0x00000000	10: syscall	
0x0040000c	0x0000000c	syscall	11: li \$v0,5	
0x00400010	0x24020005	addiu \$2,\$0,0x00000005	12: syscall	
0x00400014	0x0000000c	syscall	13: add \$s1,\$v0,\$zero # n = \$s1	
0x00400018	0x00408820	add \$17,\$2,\$0	14: div \$s2,\$s1,10	
0x0040001c	0x2001000a	addi \$1,\$0,0x0000000a		
0x00400020	0x0221001a	div \$17,\$1		
0x00400024	0x00009012	mflw \$18		
0x00400028	0x00004810	mflw \$9	15: mflw \$t1 # so hang don vi	
0x0040002c	0x2001000a	addi \$1,\$0,0x0000000a	16: div \$s3,\$s2,10	
0x00400030	0x0241001a	div \$18,\$1		

**Data Segment**

Address	Value (+0)	Value (+4)	Value (+8)	Value (+c)	Value (+10)	Value (+14)	Value (+18)	Value (+1c)
0x10010000	0x7061684e	0x206f7320	0x7975676e	0x6e206e65	0x00203d20	0x00000000	0x00000000	0x00000000
0x10010020	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010040	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010060	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010080	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100a0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100c0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x100100e0	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010100	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010120	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000
0x10010140	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000	0x00000000

**Registers**

Name	Number	Value
\$8 (vaddr)	8	0x00000000
\$12 (status)	12	0x0000ff11
\$13 (cause)	13	0x00000000
\$14 (epc)	14	0x00000000

**Mars Messages**

```
-- program is finished running --
Nhap so nguyen n = 789
-- program is finished running --
```

**Digital Lab Sim**

789

0 1 2 3  
4 5 6 7  
8 9 a b  
c d e f

Tool Control: Disconnect from MIPS, Reset, Help, Close

## Assignment 3

### CODE:

```
.eqv SEVENSEG_LEFT 0xFFFF0011 # Dia chi cua den led
7 doan trai.

.eqv SEVENSEG_RIGHT 0xFFFF0010 # Dia chi cua den
led 7 doan phai

.data

    nhapkytu: .ascii "Nhap ky tu: "

.text

main:
    li $v0, 4
    la $a0, nhapkytu
    syscall
    li $v0, 12
    syscall
    add $s1, $v0, $zero # n = $s1
    div $s2, $s1, 10
    mfhi $t1 # so hang don vi
    div $s3, $s2, 10
    mfhi $t2 # so hang chuc
    addi $s4, $zero, -1 # khoi tao check = -1
    add $t3, $zero, $t2 # $t3 = so hang chuc

Check_so:
So_0: bne $t3, 0, So_1
```



```
        li    $a0, 0x3F
        j     in_so
So_1:   bne   $t3, 1, So_2
        li    $a0, 0x06
        j     in_so
So_2:   bne   $t3, 2, So_3
        li    $a0, 0x5B
        j     in_so
So_3:   bne   $t3, 3, So_4
        li    $a0, 0x4F
        j     in_so
So_4:   bne   $t3, 4, So_5
        li    $a0, 0x66
        j     in_so
So_5:   bne   $t3, 5, So_6
        li    $a0, 0x6D
        j     in_so
So_6:   bne   $t3, 6, So_7
        li    $a0, 0x7D
        j     in_so
So_7:   bne   $t3, 7, So_8
        li    $a0, 0x07
        j     in_so
So_8:   bne   $t3, 8, So_9
```

```

        li    $a0, 0x7F
        j     in_so
So_9:    bne   $t3, 9, exit
        li    $a0, 0x6F
in_so:
        beq   $s4, $zero, in_so_don_vi # if check = -1 in
so hang chuc
                                   # if check = 0 in so hang don
vi
in_so_chuc:
        jal   SHOW_7SEG_LEFT  # show
        nop
        addi   $s4, $s4, 1
        add    $t3, $zero, $t1 # $t3 = so hang don vi
        j     Check_so

in_so_don_vi:
        jal   SHOW_7SEG_RIGHT # show
        nop
exit:    li    $v0, 10
        syscall
endmain:

SHOW_7SEG_LEFT:
        li    $t0, SEVENSEG_LEFT # assign port's address

```

```
sb $a0, 0($t0)      # assign new value
```

```
nop
```

```
jr $ra
```

```
nop
```

SHOW\_7SEG\_RIGHT:

```
li $t0, SEVENSEG_RIGHT # assign port's address
```

```
sb $a0, 0($t0)      # assign new value
```

```
nop
```

```
jr $ra
```

```
nop
```

## Kết quả:

Nhập ký tự thường:

Nhap ky tu: u

The screenshot displays the Digital Lab Sim software interface, which is used for simulating MIPS assembly code. The interface is divided into several panels:

- Text Segment:** Shows the MIPS assembly code being executed. The code includes instructions like `addiu $2,$0,0x00000004`, `li $v0,4`, `la $a0,nhapkytu`, `ori $4,$1,0x00000000`, `syscall`, `addiu $2,$0,0x0000000c`, `li $v0,12`, `syscall`, `add $1,$v0,$zero`, `div $s2,$s1,10`, `mfhi $9`, and `div $s3,$s2,10`.
- Data Segment:** Shows the memory addresses and values of the data segment. The values are mostly zeros, except for `0x7061684e` at address `0x10010000`.
- Registers:** Shows the values of the MIPS registers. The `$zero` register is `0x00000000`, and the `$a0` register is `0x7061684e`.
- 7-segment display:** Shows the output of the program. The display shows the number `8.8`.
- Mars Messages:** Shows the output of the program. The message is `Nhap ky tu: u`.

Nhập ký tự in hoa:

Nhap ky tu: J

The screenshot displays the Digital Lab Sim MIPS simulator interface. The main window is divided into several panels:

- Text Segment:** A table showing assembly instructions with columns for Bkpt, Address, Code, Basic, and Source. The instructions include `addiu $2,$0,0x00000004`, `lui $1,0x00001001`, `ori $4,$1,0x00000000`, `syscall`, `addiu $2,$0,0x0000000c`, `syscall`, `add $s1,$v0,$zero`, `div $s2,$s1,10`, `mflw $f8`, `mflw $f9`, and `div $s3,$s2,10`.
- Data Segment:** A table showing memory addresses and their corresponding values in different bases (hex, decimal, octal, etc.).
- Registers:** A table showing the state of MIPS registers, including `$zero`, `$at`, `$v0`, `$v1`, `$a0`, `$a1`, `$a2`, `$t0`, `$t1`, `$t2`, `$t3`, `$t4`, `$t5`, `$t6`, `$t7`, `$s0`, `$s1`, `$s2`, `$s3`, `$s4`, `$s5`, `$s6`, `$s7`, `$s8`, `$s9`, `$k0`, `$k1`, `$gp`, `$sp`, `$fp`, `$ra`, `pc`, `hi`, and `lo`.
- Labels:** A table showing labels and their addresses, including `main` at `0x00400000` and `Check_so` at `0x00400044`.
- Mars Messages:** A text area showing the output of the program, which includes the text "Nhap ky tu: J" and "-- program is finished running --".
- Digital Lab Sim:** A small window in the foreground showing a digital display with the number "8.8" and a numeric keypad.

## Assignment 4

### CODE:

```
.eqv MONITOR_SCREEN 0x10010000 #Dia chi bat dau cua
bo nho man hinh

.eqv RED 0x00FF0000 #Cac gia tri mau thuong su dung
.eqv GREEN 0x0000FF00
.eqv BLUE 0x000000FF
.eqv WHITE 0x00FFFFFF
.eqv YELLOW 0x00FFFF00

.text

    li    $k0, MONITOR_SCREEN    #Nap dia chi bat
dau cua man hinh

    li    $t2, 2                  # khoi tao 2
    li    $t4, 8                  # khoi tao 8
    li    $t1, -1                 # i = -1
    li    $t4, 0                  # j = 0
    li    $s1, 4                  # $s1 = 4
    add   $k0, $k0, -4            # $k0 = $k0 - 4
FOR:    addi    $t1, $t1, 1        # i = 0, i ++
        addi    $t4, $t4, 1        # j++
        beq    $t1, 72, EXIT        # i = 72 stop
        add    $k0, $k0, $s1        # $k0 += 4
        div    $t1, $t2            # i / 2
        mfhi    $t3                # $t3 = i % 2
```

```

        bne $t4, 8, continue    # j = 8 => i++
        li  $t4, 0              # j = 0
        addi $t1, $t1, 1        # i++
continue:
        beq $t3, $zero, doi_mau
        li  $t0, RED
        sw  $t0, 0($k0)
        nop
        j   FOR

doi_mau:
        li  $t0, BLUE
        sw  $t0, 0($k0)
        nop
        j   FOR

EXIT:    li  $v0, 10
        syscall

```

## Giải thích code:

- Dùng một biến i chạy từ 0 – 71 thì dừng
- Dùng một biến j chạy từ 1 – 8
- Khi  $i/2$  dư 1 thì sẽ in màu đỏ,  $i/2$  dư 0 thì in màu xanh
- Vì bảng là 8x8 nên mỗi lần  $j = 8$  sẽ tăng i thêm 1 => i sẽ tăng 2 đơn vị khi xuống dòng => khi xuống dòng i sẽ giữ nguyên màu. Vì có 8 dòng nên sẽ tăng i lên 8 đơn vị nên phải đặt cho i dừng khi  $i = 72$

## Kết quả:

The screenshot displays a MIPS simulator interface with three main panels:

- Text Segment:** Shows assembly code for 'Assignment4.asm'. The code includes labels like 'FOR', 'continue', 'doi\_mau', and 'EXIT', with instructions such as 'lui \$1, 0x00001001', 'ori \$26, 0x00000000', 'addiu \$10, \$0, 0x0000...', 'addiu \$12, \$0, 0x0000...', 'addiu \$9, \$0, 0xffffffff', 'addiu \$12, \$0, 0x0000...', 'addiu \$17, \$0, 0x0000...', 'addiu \$26, \$26, 0xffff...', 'addiu \$9, \$9, 0x00000001', 'addiu \$12, \$12, 0x0000...', 'addiu \$17, \$17, 0x00000001', 'addiu \$26, \$26, \$17', and 'add \$k0, \$k0, \$s1'.
- Registers:** A table showing the state of MIPS registers. The 'Coproc 0' column is active, displaying values for registers \$s260 through \$lo. For example, \$s260 is 0x00000000, \$s261 is 0x00000001, and \$s262 is 0x0000000a.
- Bitmap Display:** A window titled 'Bitmap Display, Version 1.0' showing a 32x32 pixel grid. The grid is filled with a checkerboard pattern of red and blue squares. The 'Unit Width in Pixels' and 'Unit Height in Pixels' are both set to 32. The 'Display Width in Pixels' and 'Display Height in Pixels' are both set to 256. The 'Base address for display' is set to '0x10010000 (static data)'.

## Assignment 5

### CODE:

```
.eqv MONITOR_SCREEN 0x10010000
.eqv RED              0x00FF0000
.eqv GREEN           0x0000FF00
.data
    x1: .asciiz "Nhap x1: "
    y1: .asciiz "Nhap y1: "
    x2: .asciiz "Nhap x2: "
    y2: .asciiz "Nhap y2: "
    error1: .asciiz "Error: x2 phai khac x1. Moi
nhap lai!\n"
    error2: .asciiz "Error: y2 phai khac y1. Moi
nhap lai!\n"
.text
    li    $k0, MONITOR_SCREEN

    li    $v0, 4
    la    $a0, x1
    syscall
    li    $v0, 5
    syscall
    move   $s0, $v0

    li    $v0, 4
```



```
    la    $a0, y1
    syscall
    li    $v0, 5
    syscall
    move   $s1, $v0
```

```
NhapX2: li    $v0, 4
        la    $a0, x2
        syscall
        li    $v0, 5
        syscall
        move   $s2, $v0
        beq    $s2, $s0, Error1
```

```
NhapY2: li    $v0, 4
        la    $a0, y2
        syscall
        li    $v0, 5
        syscall
        move   $s3, $v0
        beq    $s3, $s1, Error2
        j      Tsugi
```

```
Error1: li    $v0, 4
```

```

        la  $a0, error1
        syscall
        j   NhapX2
Error2: li  $v0, 4
        la  $a0, error2
        syscall
        j   NhapY2
Tsugi:
        slt $t0, $s0, $s2
        slt $t1, $s1, $s3

        beq $t0, 0, Case3
        beq $t1, 0, Case2
Case1:  add $v0, $s1, $zero
For1:   bgt $v0, $s3, Exit
        add $v1, $s0, $zero
For2:   bgt $v1, $s2, EndFor2
        beq $v0, $s1, InVien1
        beq $v0, $s3, InVien1
        beq $v1, $s0, InVien1
        beq $v1, $s2, InVien1
        sll $t8, $v0, 6
        add $t8, $t8, $v1
        sll $t8, $t8, 2

```

```

        li    $a1, GREEN
        add   $a2, $k0, $t8
        sw    $a1, 0($a2)
        add   $v1, $v1, 1
        j     For2
InVien1:    sll $t8, $v0, 6
        add   $t8, $t8, $v1
        sll   $t8, $t8, 2
        li    $a1, RED
        add   $a2, $k0, $t8
        sw    $a1, 0($a2)
        add   $v1, $v1, 1
        j     For2
EndFor2:
        add   $v0, $v0, 1
        j     For1

Case2:    add $v0, $s3, $zero
For3:     bgt $v0, $s1, Exit
        add   $v1, $s0, $zero
For4:     bgt $v1, $s2, EndFor4
        beq   $v0, $s1, InVien2
        beq   $v0, $s3, InVien2
        beq   $v1, $s0, InVien2

```

```

        beq $v1, $s2, InVien2
        sll $t8, $v0, 6
        add $t8, $t8, $v1
        sll $t8, $t8, 2
        li  $a1, GREEN
        add $a2, $k0, $t8
        sw  $a1, 0($a2)
        add $v1, $v1, 1
        j   For4
InVien2: sll $t8, $v0, 6
        add $t8, $t8, $v1
        sll $t8, $t8, 2
        li  $a1, RED
        add $a2, $k0, $t8
        sw  $a1, 0($a2)
        add $v1, $v1, 1
        j   For4
EndFor4:
        add $v0, $v0, 1
        j   For3
Case3:   beq $t1, 0, Case4
        add $v0, $s1, $zero
For5:    bgt $v0, $s3, Exit
        add $v1, $s2, $zero

```

```
For6:    bgt $v1, $s0, EndFor6
```

```
    beq $v0, $s1, InVien3
```

```
    beq $v0, $s3, InVien3
```

```
    beq $v1, $s0, InVien3
```

```
    beq $v1, $s2, InVien3
```

```
    sll $t8, $v0, 6
```

```
    add $t8, $t8, $v1
```

```
    sll $t8, $t8, 2
```

```
    li  $a1, GREEN
```

```
    add $a2, $k0, $t8
```

```
    sw  $a1, 0($a2)
```

```
    add $v1, $v1, 1
```

```
    j   For6
```

```
InVien3:sll $t8, $v0, 6
```

```
    add $t8, $t8, $v1
```

```
    sll $t8, $t8, 2
```

```
    li  $a1, RED
```

```
    add $a2, $k0, $t8
```

```
    sw  $a1, 0($a2)
```

```
    add $v1, $v1, 1
```

```
    j   For6
```

```
EndFor6:
```

```
    add $v0, $v0, 1
```

```
    j   For5
```

```

Case4:  add $v0, $s3, $zero
For7:   bgt $v0, $s1, Exit
        add $v1, $s2, $zero
For8:   bgt $v1, $s0, EndFor8
        beq $v0, $s1, InVien4
        beq $v0, $s3, InVien4
        beq $v1, $s0, InVien4
        beq $v1, $s2, InVien4
        sll $t8, $v0, 6
        add $t8, $t8, $v1
        sll $t8, $t8, 2
        li  $a1, GREEN
        add $a2, $k0, $t8
        sw  $a1, 0($a2)
        add $v1, $v1, 1
        j   For8
InVien4:sll $t8, $v0, 6
        add $t8, $t8, $v1
        sll $t8, $t8, 2
        li  $a1, RED
        add $a2, $k0, $t8
        sw  $a1, 0($a2)
        add $v1, $v1, 1
        j   For8

```

```
EndFor8:
    add $v0, $v0, 1
    j    For7
Exit:    li    $v0, 10
        syscall
```

### **Kết quả:**

