Timer for SPI delay							
	TIM configuration			SPI configuration			Debug
STM32 F103	TIM1		STM32 F103	SPI1		SYS Mode and Configuration	
Mode			Mode			Debug	Serial Wire
Clock Source	Internal Clock		Mode	Full-Duplex Master		Timebase Source	Sys Tick
Counter Settings			Hardware NSS	Disable			
Prescaler	71		Basic Parameters				
Counter Mode	Up		Frame Format	Motorola			
Counter Preiod	0xFFFE		Data Size	8 Bits			
CKD	No Division		First Bit	MSB first			
RCR	0		Clock Parameters				
auto-reload preload	Enable		Prescaler(for Baud Rate)	16			
Tigger Output Parameters			Baud Rate	4.5MBit/s			
MSM bit	Disable		CPOL	Low			
Trigger Event Selection	Reset		CPHA	1 Edge			
			Advance Parameters				
			CRC Calculation	Disabled			
			NSS Signal Type	Software			

Timer for SPI delay							
	TIM configuration			**SPI configuration**			
STM32 F411	**TIM1**		**STM32 F411**	**SPI1**			
Mode			**Mode**				
Clock Source	**Internal Clock**		**Mode**	**Full-Duplex Master**			
Counter Settings			**Hardware NSS**	**Disable**			
Prescaler	**71**		**Basic Parameters**				
Counter Mode	**Up**		**Frame Format**	**Motorola**			
Counter Preiod	**0xFFFE**		**Data Size**	**8 Bits**			
CKD	**No Division**		**First Bit**	**MSB first**			
RCR	**0**		**Clock Parameters**				
auto-reload preload	**Enable**		**Prescaler(for Baud Rate)**	**16**			
Tigger Output Parameters			**Baud Rate**	**4.5MBit/s**			
MSM bit	**Disable**		**CPOL**	**Low**			
Trigger Event Selection	**Reset**		**CPHA**	**1 Edge**			
			Advance Parameters				
			CRC Calculation	**Disabled**			
			NSS Signal Type	**Software**			