

Team Backlog				
Task Name	Assignee	Status	ETF (Hrs)	ATF (Hrs)
Design				
High Level Design				
Modules' Description	All Team	Done	4	4
Project Circuit Schematic	Mowafwy	Done	1	2
Drivers' Documentation	All Team	Done	2	1
Low Level Design				
Functions' Flowchart	All Team	Done	3	4
Summary				
Document File	Mowafey	Done	1	1
Implementation				
MCAL Module				
DIO Driver	Momen Hassan	Done	2	3
EXI Driver	Sherif Ashraf Khadr	Done	2	1
TMR Driver	Mahmoud Mowafey	Done	2	2
ICU Driver	Mahmoud Mowafey	Done	3	4
PWM Driver	SharpeI Malak	Done	1	3
HAL Module				
Ultrasonic Driver	Sherif Ashraf Khadr	Done	1	1.5
LCD Driver	Momen Hassan	Done	2	3
Motors Driver	Mahmoud Mowafey	Done	2	2
KPD Driver	SharpeI Malak	Done	1	1
Push Buttons Driver	Sherif Ashraf Khadr	Done	1	1
APP Module				
Car Operation	All Team	Done	5	12
APP	All Team	Done	1	4
Testing				
Documentation				
Test Protocol Excel Sheet	All Team	Done	1	1
Unit Testing				
Test MCAL	All Team	Done	4	4
Test HAL	All Team	Done	4	4
System Testing				
Test APP	All Team	Done	3	3

Final Requirements Review	All Team	Done	2	2
Video				
Video Recording	All Team	Done	3	3
Video Editing	Sharpel	Done	3	3