

From	Our Reference Ingo Ehler	Tel 07062/911-4007	Fax	Abstatt 11.01.2011
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## Requirements specification

Recipient

Cc

Topic **Bootloader CMP Integration Specification**

### 1 Introduction

The purpose of this document is to describe the changes required in a CC-AS project to integrate a Bootloader CMP.

***Important: This document serves as a template parts in italic need to be adapted as defined according to the project specific needs.***

### 2 Component & File List

The following components and files have to be integrated and/or adapted:

Name	Type	Version	Path
MTC_Configuration_BBxxxxx.XML	revision		\PRJ\Gen9Config
MTC_MKS_MakeOptions_BBxxxxx.XML	revision		\PRJ\Gen9Config
MTC_MKS_AddHexBlockConfig_Flash09_BBxxxxx.XML	revision		\PRJ\Gen9Config
MTC_MKS_AddHexBlockConfig_Flashdrv09_BBxxxxx.XML	revision		\PRJ\Gen9Config
BBxxxxx_ECA.lcf	revision		\PRJ
BBxxxxx_ECB.lcf	revision		\PRJ
BBxxxxx_ECC.lcf	revision		\PRJ
BBxxxxx_ECD.lcf	revision		\PRJ
BBxxxxx_ECE.lcf	revision		\PRJ
CMHSW09_CSWPSettings_BBxxxxx.h	revision		\Psw\HSW\CMHSW_Config\HDR\HSW
CMPSW09_Settings_BBxxxxx.h	revision		\Psw\CMPSW_Config\HDR\PSW
LCF09_HEXBLOCKS_ALLOCATION_BBxxxxx.C	revision		\Psw\HSW\CMHSW_Config\SRC
CM_SWIdentification_BBxxxxx.h	revision		\Hdr
FLASH09_BBxxxxx	configuration		\Psw\DN\FLASH
FLASH09_BootManager_BBxxxxx	configuration		\Psw\DN\FLASH
FLASH09_COMMON	configuration		\Psw\DN\FLASH
FLASH09_CUBAS_CAN	configuration		\Psw\DN\FLASH
FLASH09_RB01	configuration		\Psw\DN\FLASH
FLASH09_TMS570_COMMON	configuration		\Psw\DN\FLASH
FLASHDRV09_BBxxxxx	configuration		\Psw\DN\FLASH



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### 3 Detailed Description

#### 3.1 Build and MakeOptions

MTC_Configuration_BBxxxxx.XML
MTC_MKS_MakeOptions_BBxxxxx.XML
MTC_MKS_AddHexBlockConfig_Flash09_BBxxxxx.XML
MTC_MKS_AddHexBlockConfig_Flashdrv09_BBxxxxx.XML

##### 3.1.1 Additional Hexblock builds

The Bootloader and the Flashdriver are build in an external build run. Therefore the AddHexBlockConfig for Flash09 and FlashDrv09 configures the included and excluded files / folders.

Also the direct link to the build specific linker files and the build name has to be given.

Example:

```
?xml version="1.0" encoding="UTF-8" standalone="yes"?>
<XMLAddHexBlocks xmlns="http://de.bosch.com/SharCC/DB">
  <AddHexBlockList>
    <UsedDefineName>FLASH09</UsedDefineName>
    <LCFFile>..\PSW\DNC\FLASH\FLASH09_BBxxxxx\prj\Flash09_BBxxxxx.lcf</LCFFile>
    <Configuration>
      <DynamicRules>
        <IncludeFolderList>PSW\DNC\FLASH.*</IncludeFolderList>
        <ExcludeFolderList>PSW\DNC\FLASH\FLASHDRV.*</ExcludeFolderList>
      </DynamicRules>
      <StaticRules>
        <IncludeFileList>MIC09_BootblockServiceFunc_COMMON.c</IncludeFileList>
        ..
        <ExcludeFileList>FLASH09_InterfaceFsw_BBxxxxx.c</ExcludeFileList>
      </StaticRules>
    </Configuration>
  </AddHexBlockList>
</XMLAddHexBlocks>
```

The path to the lcf file has to be exact, the include component names can take use of the ‘.\*’ place-holder. (‘.\*’ is a logical expression meaning any character in any number)

To use this additional HexBlock configurations the MTC\_Configuration\_BB file has to configure both addhexblock.xml files in its AddHexBlockConfigList.

Example:

```
<AddHexBlockConfig>
  <AddHexBlockConfigFileList> ..\PRJ\Gen9Config\MTC_MKS_AddHexBlockConfig_Flash09_BBxxxxx.XML
</AddHexBlockConfigFileList>
  <AddHexBlockConfigFileList> ..\PRJ\Gen9Config\MTC_MKS_AddHexBlockConfig_Flashdrv09_BBxxxxx.XML
</AddHexBlockConfigFileList>
</AddHexBlockConfig>
```

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### 3.1.2 Customer-, BBNumer Map and Zipfile Handling

The MTC\_MKS\_MakeOptions gives the possibility to add replacement names for Project and Customer.

The required BB-Number for *RB01* CMP is **xxxxxx**.

The required Customer Name for *RB01* CMP is **RB01**.

A specific Layout name is not required.

Example:

```
<CustomerMap>CanRef, RB, RRef, RRefCanDev, RRefCanStd, RRefFlxrDev, RB01</CustomerMap>
<BBMap>29888, 39887, 29828, 29930, 90112, xxxxxx, </BBMap>
```

Furthermore the MTC\_MKS\_MakeOptions may define zip files which should be unpacked during the TMK run.

For the Bootloader CMPs at least the extraction of the CUBAS configuration is required.

```
<ZipFileInfoList>
  <ZipFileName>FLASH09_CUBAS_BBxxxxx.zip</ZipFileName>
  <FileList>..\Psw\DNC\FLASH\FLASH09_BBxxxxx\cubas\FLASH09_CUBAS_BBxxxxx.tfl</FileList>
  <Password></Password>
  <ExtSource></ExtSource>
  <TFLUsage>RELATIVE_PRJ</TFLUsage>
</ZipFileInfoList>
```

The path to the filelist (tfl) has to match.

### 3.2 Linker Files

The linker file defines the layout of the ROM and RAM area. The Bootloader part is usually defined by the bootblock CR-P. **Any changes need to be reviewed by the Bootloader expert as the linker file is depending on the settings made in the local lcf for Bootloader and FlashDriver build.**

```
BBxxxxx_ECA.lcf
BBxxxxx_ECB.lcf
BBxxxxx_ECC.lcf
BBxxxxx_ECD.lcf
BBxxxxx_ECE.lcf
```

### 3.3 Project Specific Configuration

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The project specific configuration need to enable and set switches required to switch on the bootloader features.

CMHSW09_CSWPRSettings_BBxxxx.h
CMPSW09_Settings_BBxxxx.h
LCF09_HEXBLOCKS_ALLOCATION_BBxxxx.C
CM_SWIdentification_BBxxxx.h

### 3.3.1 CMHSW09\_CSWPRSettings\_BBxxxx.h

<b>Switch</b>	<b>Value</b>
FS_BootblockSize	FS_BootblockSize_32KB
FS_CalblockSize	FS_CalblockSize_32KB
FS_HexBlksOrder	FS_HexBlksOrder_BootCalFsw
FS_BoschSignature	FS_BoschSignature_Off

### 3.3.2 CMPSW09\_Settings\_BBxxxx.h

<b>Switch</b>	<b>Value</b>
FS_RBBootblock	FS_RBBootblock_EnabledWithEeprom
FS_FlashDriverAutoSAR	FS_FlashDriverAutoSAR_Off
FS_FlashDriverSingleByteAccess	FS_FlashDriverSingleByteAccess_Yes
FS_FlashDriverAutoAlignment	FS_FlashDriverAutoAlignment_Yes
C_maxNumberOfBytes2Program_ul	4095

### 3.3.3 LCF09\_HEXBLOCKS\_ALLOCATION\_BBxxxx.C

This file defines the constant data field according to the hexfile handling document.

"C:\tsdst\CHK\hexfile\_handling\_in\_Gen9\_projects.pdf"

**As there are major dependencies to the linker file changes here need to be reviewed by the Flash CR-P.**

### 3.3.4 CM\_SWIdentification\_BBxxxx.h

The SW-Identification file defines the customer specific logistic data. The define names have to remain constant. **The parameters have to be reviewed and changed upon each baseline by the CSW-PR.**

<b>ParameterName</b>	<b>Description</b>	<b>Format</b>	<b>Example Value</b>
CM_DataCompatibilityN	DataCompabilityNumber	2 Byte Binary: 0xYYYY	0x0100



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*umber*

*has to be changed each time  
the application and/or calibration  
blocks are no longer compatible  
to a prior application  
baseline*

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## 4 Flash Bootloader Components

The bootloader components need to be placed in the path defined by the additional hexblock configuration and make option files.

FLASH09_BBxxxxx
FLASH09_BootManager_BBxxxxx
FLASH09_COMMON
FLASH09_CUBAS_CAN
FLASH09_GM01
FLASH09_TMS570_COMMON
FLASHDRV09_BBxxxxx

If the components are used in other projects (different BB Number) and changes to the BB specific components are necessary the changed files and the component has to be renamed to the new BB- Number.

Changes to the CMP requires a feedback from CMP-RP.

Changes to a COMMON component have to be discussed with the Bootloader Mainstream responsible.