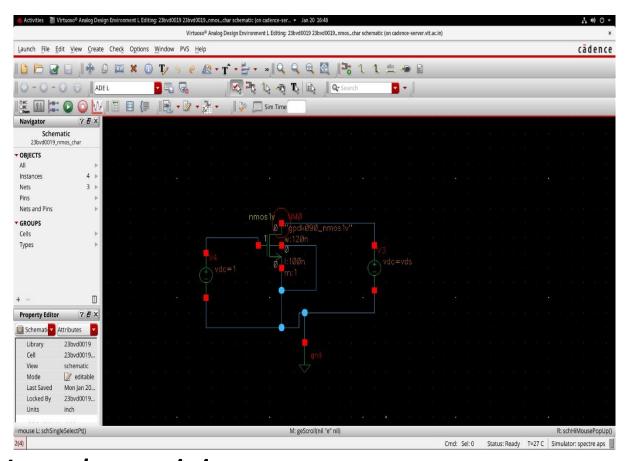
VLSI SYSTEM DESIGN

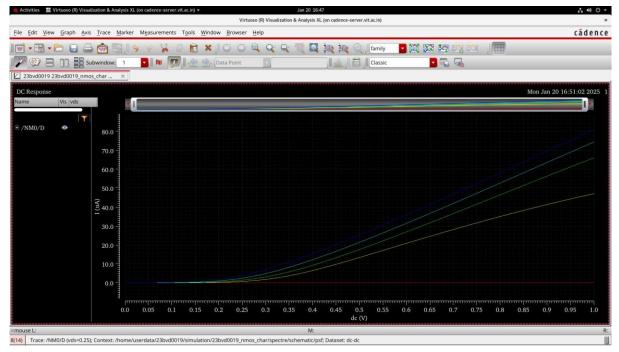
Aim: <u>Plot I-V characteristics for NMOS and PMOS. Determine Vth, Lambda, body bias effect</u>

NMOS CHARACTERISTICS:

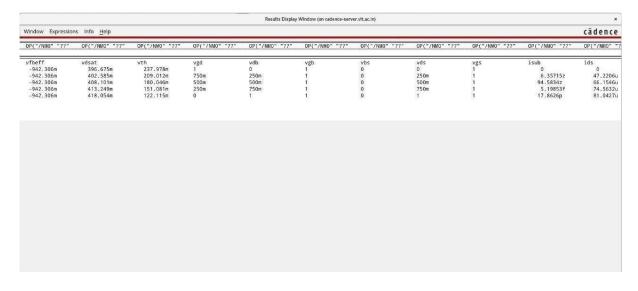
1) IDS Vs. VGS (for different values of VDS) Schematic:



Input characteristics:

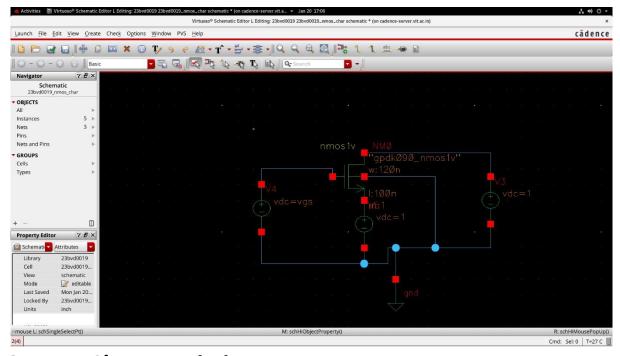


VTH (Threshold values):

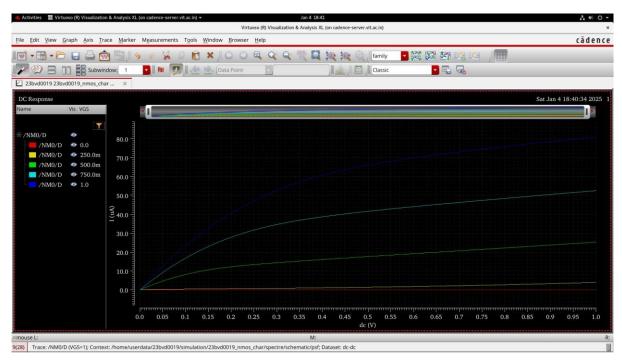


2)IDS Vs VDS (for different values of VGS)

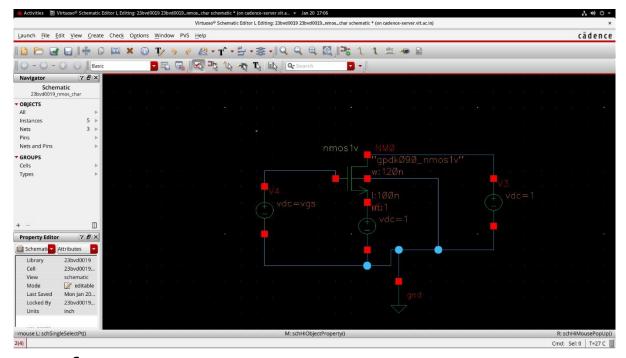
Schematic:



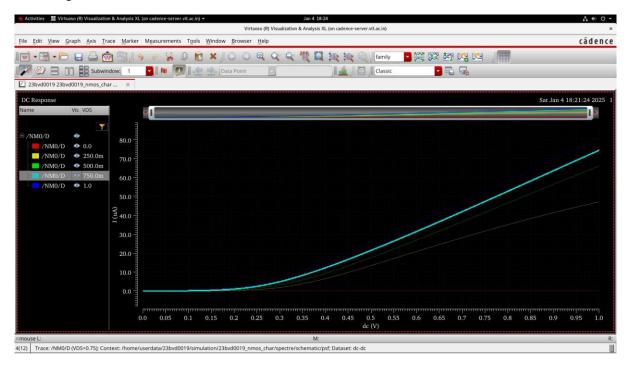
Output Characteristics:



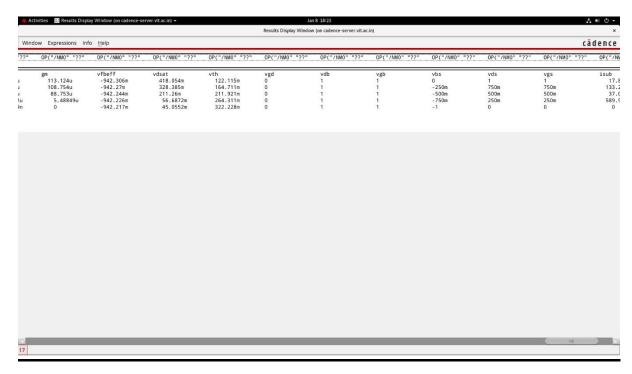
3)IDS VS VGS (for different VSB values) Schematic:



Waveform:



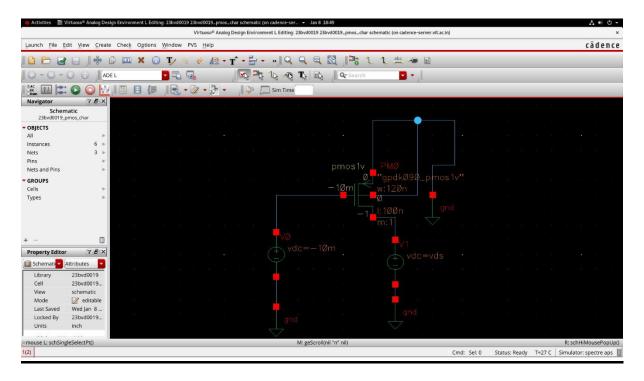
Vth Values:



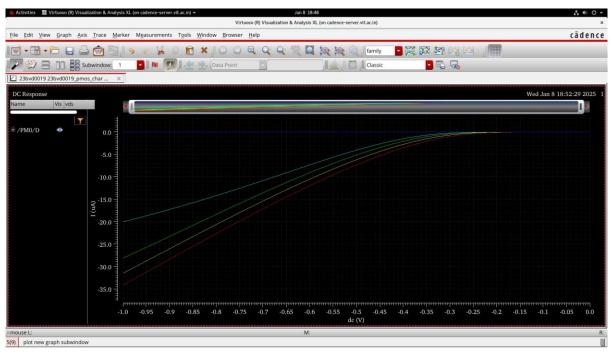
PMOS CHARACTERISTICS:

1)IDS Vs. VGS (for different values of VDS)

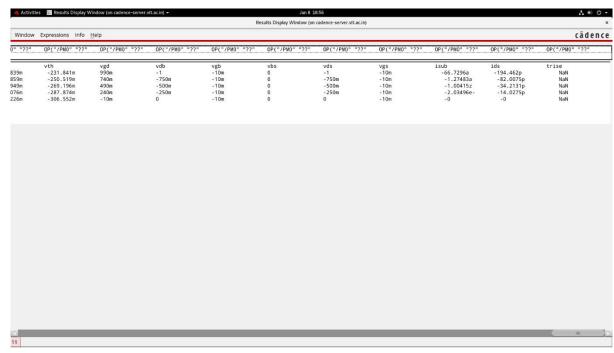
Schematic:



Input Characteristics:

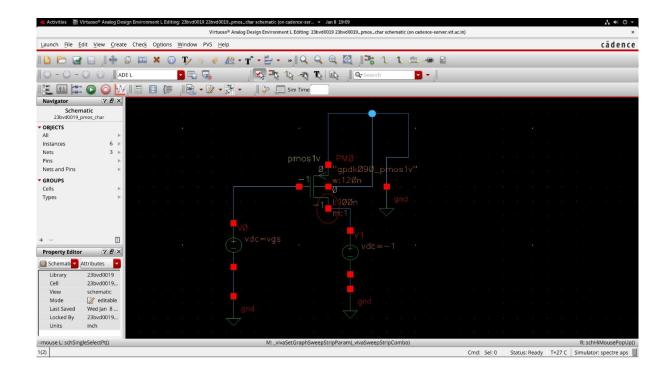


Vth Values:

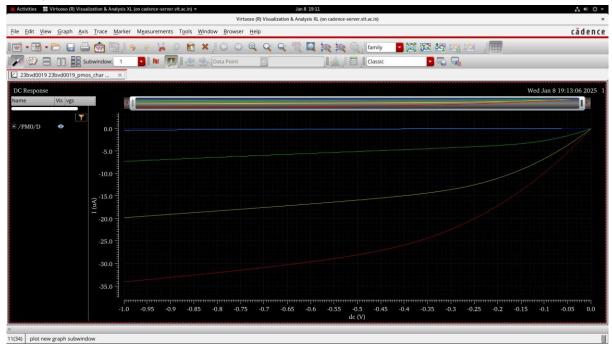


2)IDS Vs VDS (for different values of VGS)

Schematic:

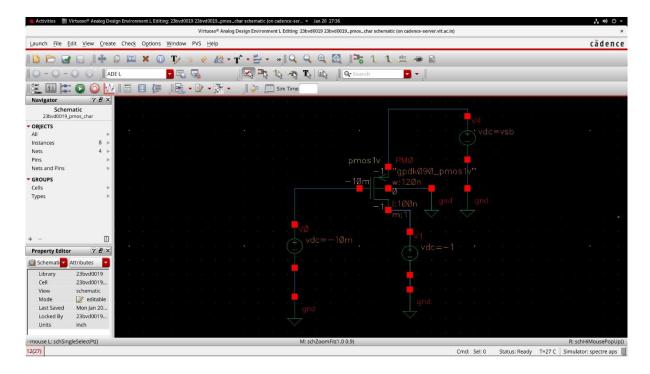


Output Characteristics:



Value of Lambda

3)IDS VS VGS (for different VSB values)



Waveform:



Values of Vth

Activities ■ Results Display Window (on codence-server vitac.in) → Jan 8 18:56											
0" "??"	OP("/PM0" "??"	OP("/PM0" "??"	OP("/PMO" "??"	OP("/PMO" "??"	OP("/PMO" "??"	OP("/PMO" "??"	OP("/PMO" "??"	OP("/PMO" "??"	OP("/PMO" "??"	OP("/PM0" "?	?"
839m 859m 949m 076m 226m	vth -231.841m -250.519m -269.196m -287.874m -306.552m	vgd 990m 740m 490m 240m	vdb -1 -750m -500m -250m 0	vgb -10m -10m -10m -10m -10m	vbs 0 0 0 0 0	vds -1 -750m -500m -250m 0	vgs -10m -10m -10m -10m -10m	isub -66.7296a -1.27483a -1.00415z -2.03496e- -0	ids -194.462p -82.0075p -34.2131p -14.0275p -0	trise NaN NaN NaN NaN NaN	
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