AEC Awignment - 2

Criven,
$$V_{C_1}(0^-) = V_0$$

$$V_{C_2}(0^-) = 0.$$

Let us come up with general conditions,

$$i(t) \longrightarrow t = 0^{(t)} \longrightarrow 0$$

$$t = 0^{(t)} \longrightarrow \frac{V_0}{R}$$

$$t = \infty \longrightarrow 0$$

$$V_{C_1}(t) \longrightarrow t = 0^{t}) \longrightarrow V_0$$

$$t = 0^{t}) \longrightarrow V_0$$

$$t = \infty \longrightarrow \frac{C_1}{C_1 + C_2}$$

$$V_{C_2}(t) \longrightarrow t = 0^{t}) \longrightarrow 0$$

$$V_{C_2}(t) \rightarrow t = 0^{(-)} \rightarrow 0$$

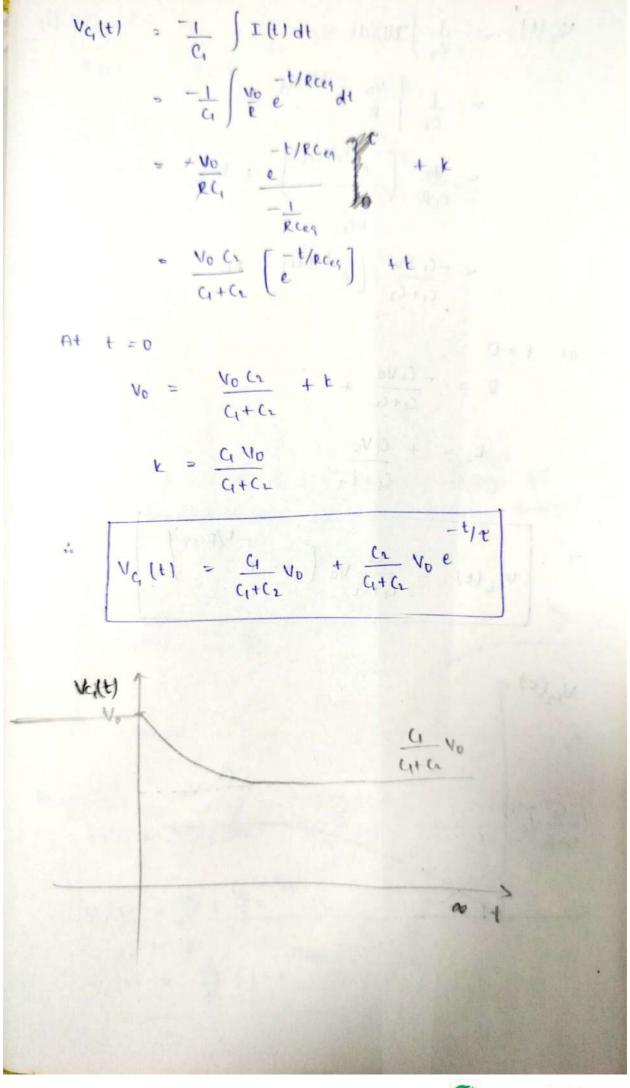
$$t = 0^{(+)} \rightarrow 0$$

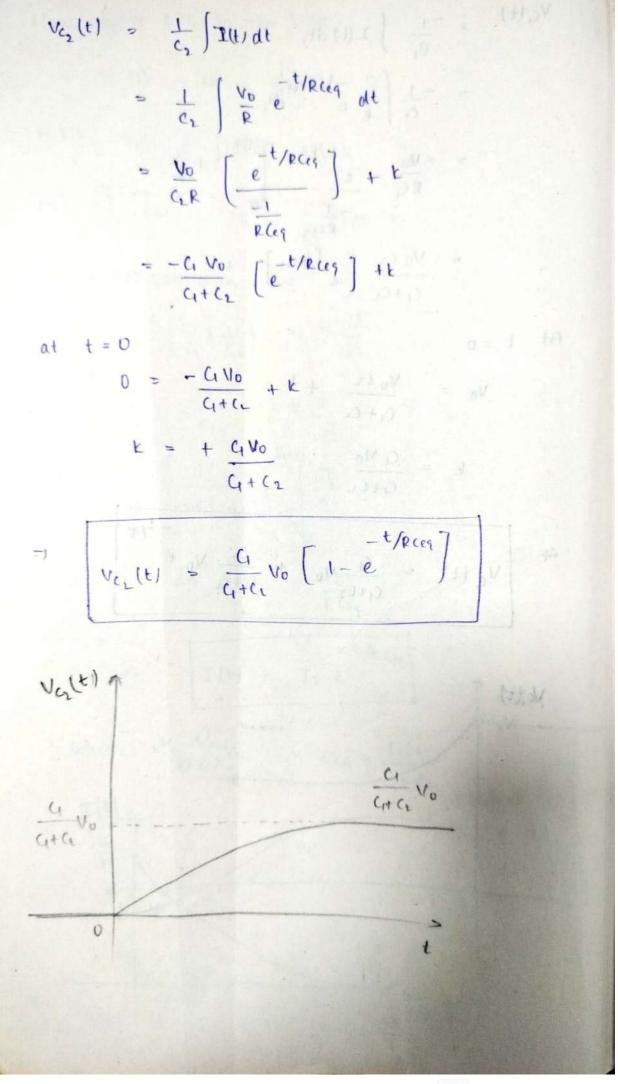
$$t = \infty \rightarrow \frac{C_1}{C_1 + C_2} V_0$$

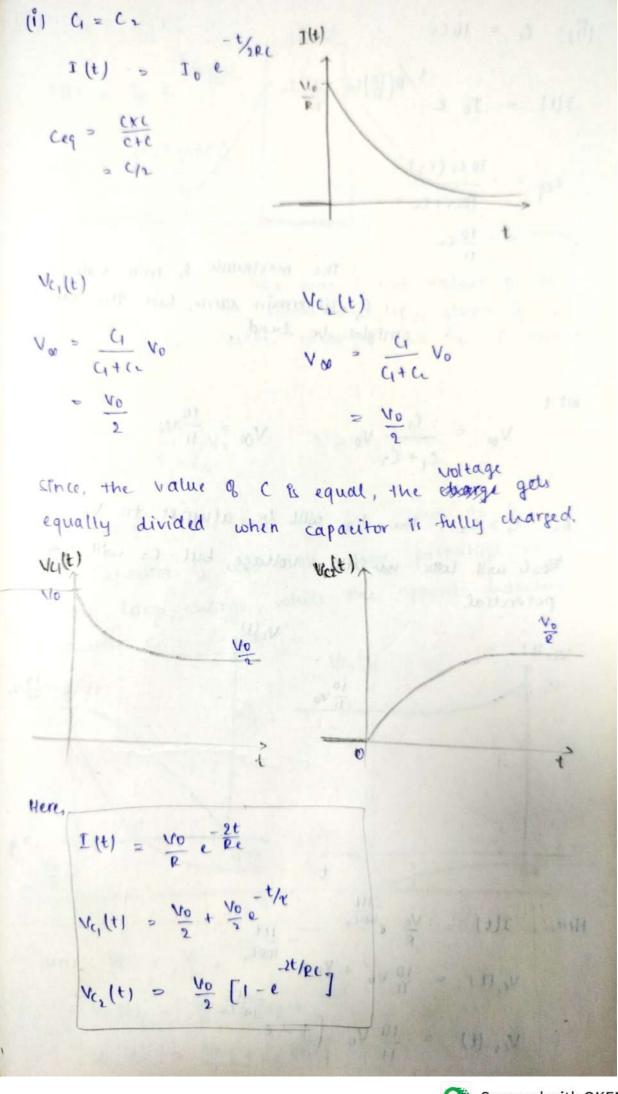
At
$$t = infinity$$

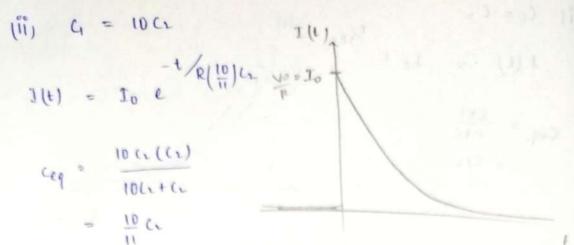
charge must be conserved,
 $c_1V_0 + C_2(0) = (C_1 + C_2)V_0$
 $V_\infty = \frac{C_1}{C_1 + C_2}V_0$

Now, apply KVL in the loop. Vc,(t) - 1R - Vc,(t) = 0 W. Eit Ve = 1 Sidt $= \frac{1}{c_1} \left[\int dt - IR - \frac{1}{c_2} \int dt = 0 \right]$ differentiate on both sides. $- I \left(\frac{1}{C_1} + \frac{1}{C_1} \right) - R \frac{dI}{dt}$ $\frac{1}{c_{eq}} = -R \frac{dI}{dt}$ $\int_{0}^{\infty} \frac{dt}{R \operatorname{Ceq}} = \int_{1}^{\infty} \frac{dI}{I}$ $I(t) = I_0 e^{-t/R ceq}$ where, Io = No; (eq = CICL ILH YOUR





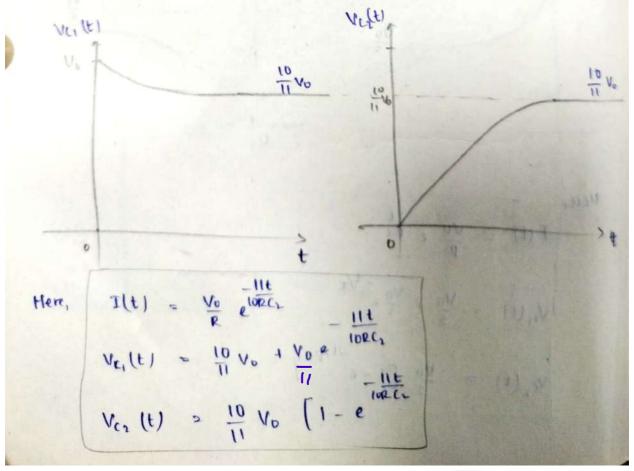


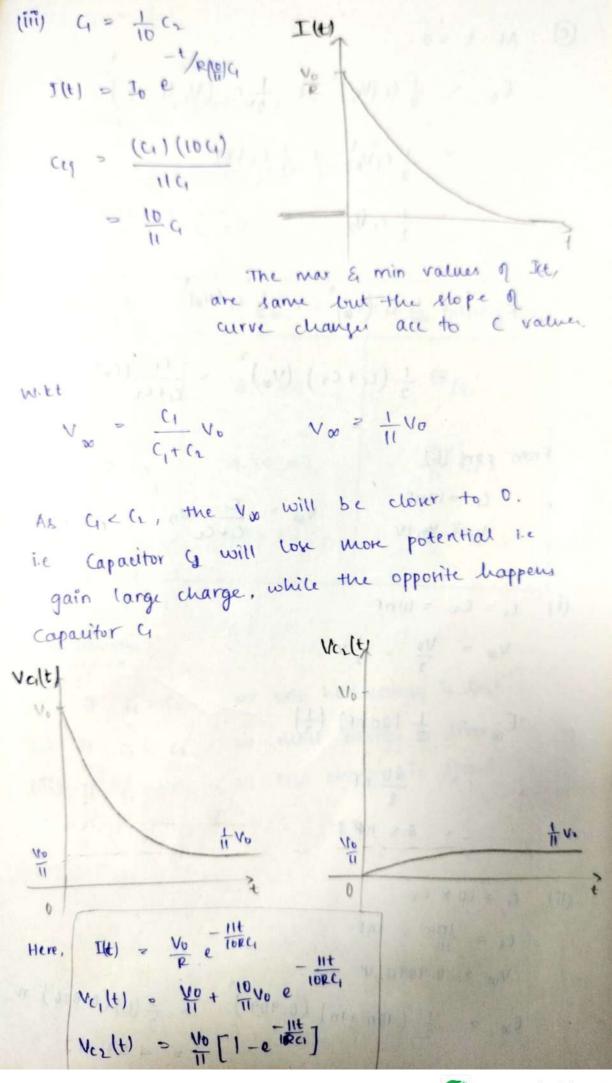


of Ist/remain same, but the curre, might be bend.

$$v_{\infty} = \frac{c_1}{c_1 + c_2} v_0 \qquad v_{\infty} > \frac{t_0}{u} v_0$$

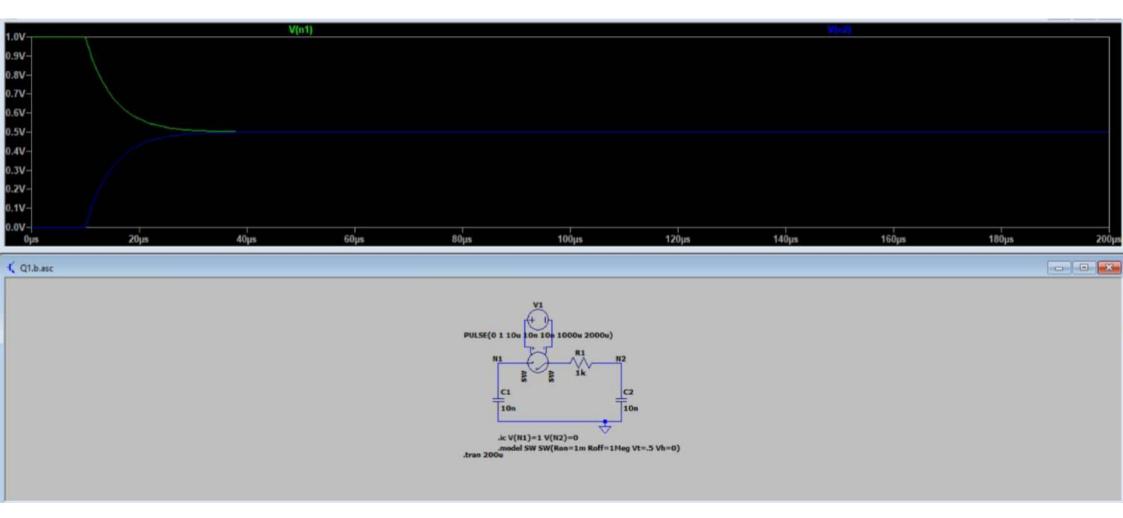
Ac G>Cz; the Vos will be almost to Vo Mes G will look mu little voltage but Cz will going potential

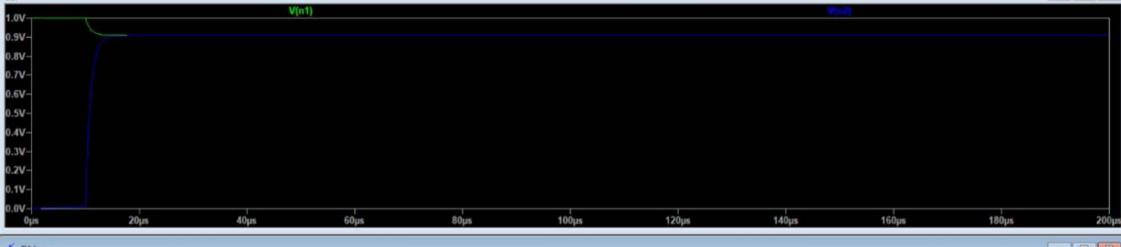


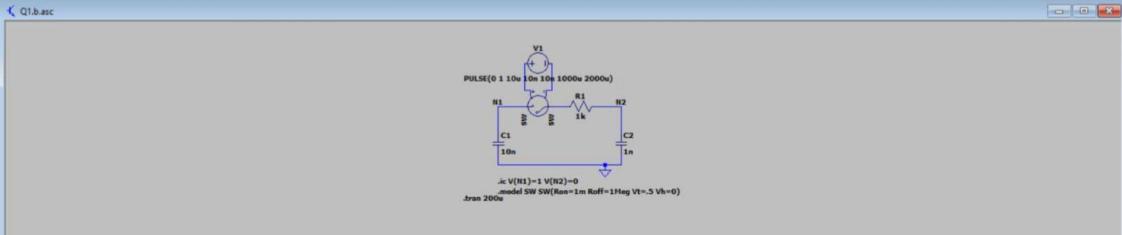


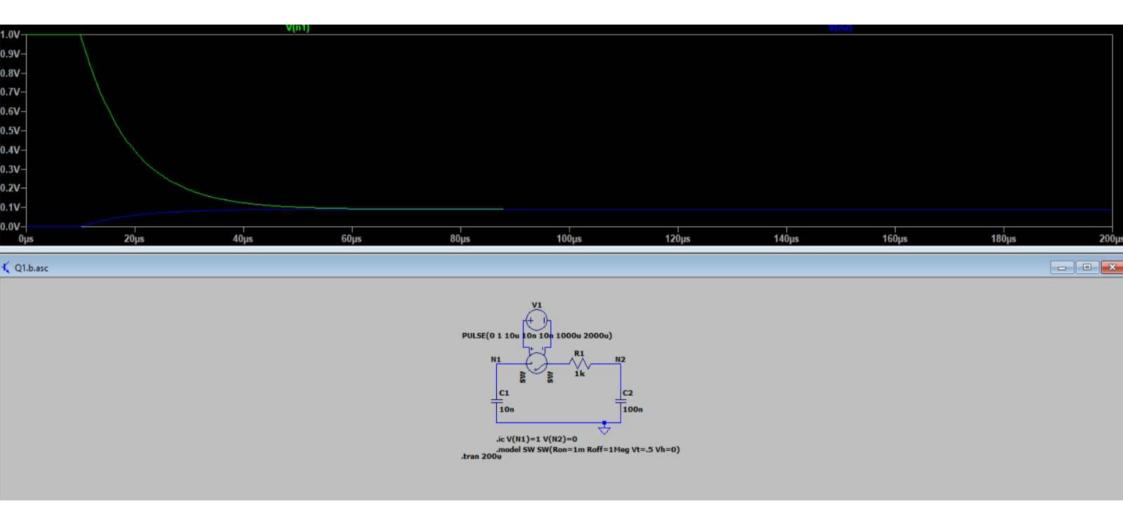
(b) It spice commands . model sw sw (Ron = Im Roff = IMeg Vt = . 5 Vh = 0) here sw - name of the switch. -> this is the resistance when switch is Ron closed, its value, - its value must be very low for current to completely flow. resistance when switch is open, ROM - îts value must be high so no current passes. -> min threshold voltage for switch to -> =0 show purely dependent circuit. there, this explains the mechanism in which witch will be working, basing on the top voltage inflow PULSE (0 1 100 10n 10n 1000 u 2000 u) 1 1 1 fall The period. Venitial initial delay time width a pulse of whath me as rise time AND AND A CO KNOW during this span the pulse will be at A possession and a supply of Villal to the balls and

With these values, we can generate a pulse of voltage input that keeps charging periodically.









$$\begin{array}{lll}
\bullet & & \frac{1}{2} c_1 (V_{c_1})^2 & + & \frac{1}{2} c_2 (V_{c_1}(t \cdot o)) \\
& & = & \frac{1}{2} c_1 V_{o}^2 & + & \frac{1}{2} c_1 (v_{o}) \\
& & = & \frac{1}{2} c_1 (V_{o})^2 & + & \frac{1}{2} c_1 (v_{o}) \\
& & = & \frac{1}{2} (c_1 + c_2) (V_{o})^2 & = & \frac{c_1}{c_1 + c_2} (\epsilon_0)
\end{array}$$
From part (b)
$$\begin{array}{lll}
(1 & = & \text{tonf} \\
V_{o} & = & \text{tonf}
\end{array}$$

$$\begin{array}{lll}
V_{o} & = & \frac{1}{2} (2 \text{onf}) (\frac{1}{2})^2 \\
& = & \frac{20}{8} \text{nf}
\end{array}$$

$$\begin{array}{lll}
\bullet & = & \text{tonf} \\
V_{o} & = & \text{tonf}
\end{array}$$

$$\begin{array}{lll}
(1) & c_1 & = & \text{tonf} \\
v_{o} & = & \frac{1}{2} (2 \text{onf}) (\frac{1}{2})^2
\end{array}$$

$$\begin{array}{lll}
\bullet & = & \text{tonf} \\
V_{o} & = & \text{tonf}
\end{array}$$

$$\begin{array}{lll}
(1) & c_1 & = & \text{tonf} \\
v_{o} & = & \text{tonf}
\end{array}$$

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\end{array}$$

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V_{o} & = & \text{tonf}
\end{array}$$

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\bullet & = & \text{tonf} \\
v_{o} & = & \text{tonf}
\end{array}$$

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v_{o} & = & \text{tonf}
\end{array}$$

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\bullet & = & \text{tonf} \\
v_{o} & = & \text{tonf}
\end{array}$$

$$\begin{array}{lll}
\bullet & = & \text{tonf} \\
v_{o} & = & \text{tonf}
\end{array}$$

$$\begin{array}{lll}
\bullet & = & \text{tonf} \\
v_{o} & = & \text{tonf}
\end{array}$$

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\bullet & = & \text{tonf} \\
v_{o} & = & \text{tonf}
\end{array}$$

$$\begin{array}{lll}
\bullet & = & \text{tonf} \\
v_{o} & = & \text{tonf}
\end{array}$$

$$\begin{array}{lll}
\bullet & = & \text{tonf} \\
\bullet & = & \text{tonf}
\end{array}$$

(iii) G =	10 62
	= 100 nF
Vo	= 0.0909 V
E 10 =	1 (10+100)n x (0.09)
2	Un 28.0

Relation of 4 & Cr	Ew	Es interne Q Es
(i) (1 = (2	2.5 nJ	E0/2
(ii) C1 = 10xC2	4.54 nJ	10 ED
(iii) 4 = Cr	0.45 nJ	1 Eo
	45-16	

Conclusions:

(i) if
$$c_1 = c_2$$
 =) only half energy is lock

(Two capacitor Paradox

Generally, when two capacitors are connected as shown below and when they reach their steady state, the final energy isn't conserved

Velt velty i.e Einitial > Ex

The best possible 101: However, this sa lost energy can be shown to be present by placing a resistor between them,

i.e, the resistor dissipates

that rest energy.

$$E_{initial} = \frac{1}{2} c_1 v_{c_1}^2 + \frac{1}{2} c_2 v_{c_2}^2$$

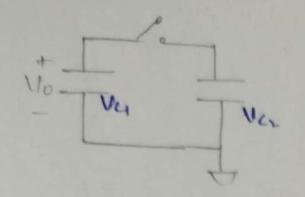
$$= \frac{1}{2} c_1 v_0^2$$

$$E_{\infty} = \frac{1}{2} (4 V_{\infty})^{2} + \frac{1}{2} (2 V_{\infty})^{2}$$

$$= \frac{1}{2} (4 + (2)) V_{\infty}$$

Edinipated by R = Sitirat = C1 Fo.

But for the case women



The initial and final energy is lost similar to as where resistor is placed.

In that care the difference can be explained as the heat dissipated by the resistor

However, in this case,

as the energy isn't conserved, the most probable case is that the resistance in the connecting wires can discipate that beat every in the form of heat I radiation.

(d) Effect & reducing R on settling time.

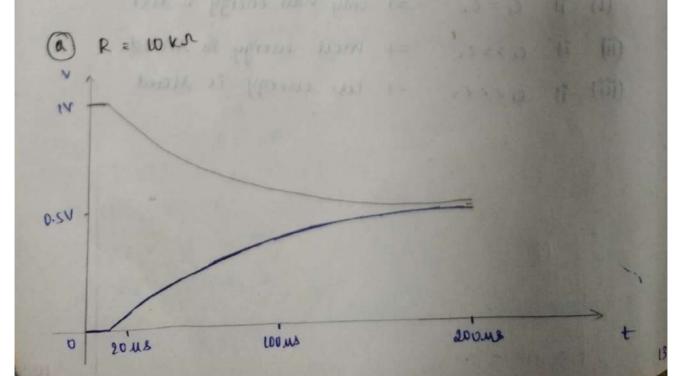
When there is a change in R, the time constant is differed and hence, settling time is altered.

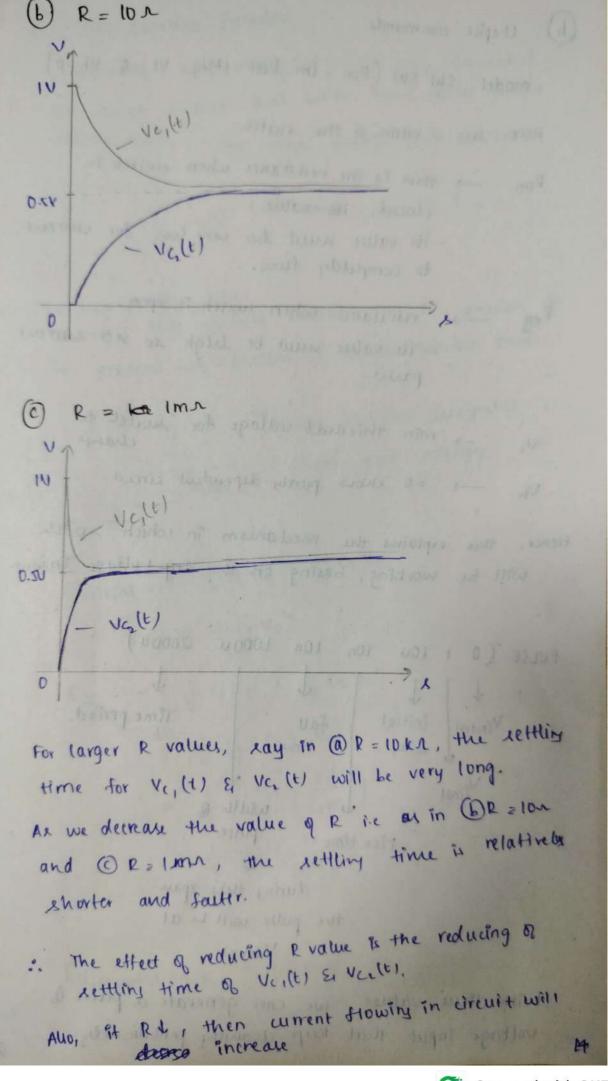
=) If time constant is I, then steady state is achieved faster.

.: Reducing the value q R decreases the settling time and steady state is obtained faster.

Equations
$$V_{c,(t)} = \frac{q}{q \cdot V_0} + \frac{c_1}{q \cdot c_1} \cdot V_0 e$$

$$\frac{c_1}{q \cdot c_1} \cdot \frac{c_2}{q \cdot c_2} \cdot \frac{c_2}{q \cdot c_2} \cdot \frac{c_1}{q \cdot c_2} \cdot \frac{c_2}{q \cdot c_2} \cdot$$





2. RC circuits as fitters (a) Civen 2 = aoms C = LOPF V.(0-) = 0V (9) auo, vin = Vou(t) step dunc Vout (t) - t=0 -> 0 t=04) - 0 - since R is to series t = 00 - July charged * The curve of Yout is expected to rise exponentially (non-line Vint) Vout (t) Vo Vo At-so At-so Here in Vin(t), just after a very small time At, the value of vo immediately went to vo without any delay. - But this transition is not being seen in Vout (1) graph, After small time At, Vout (t) 1. Hill L< Vo . After some visible tibb, it changes to vo.

This is because the Re combination in the circuit acts as a filter and filters out all q the fast transitions and also a delay occurs.

Hence, this Re cercuit acts as as low pass filter.

$$V_{c}(0) = 0$$
 V

 $V_{c}(0^{\dagger}) = 0$ V

 $V_{c}(0^{\dagger}) = 0$ V

 $V_{c}(0) = 0$ V

 $V_$

$$V_0 - iR - V_c(t) = 0.$$

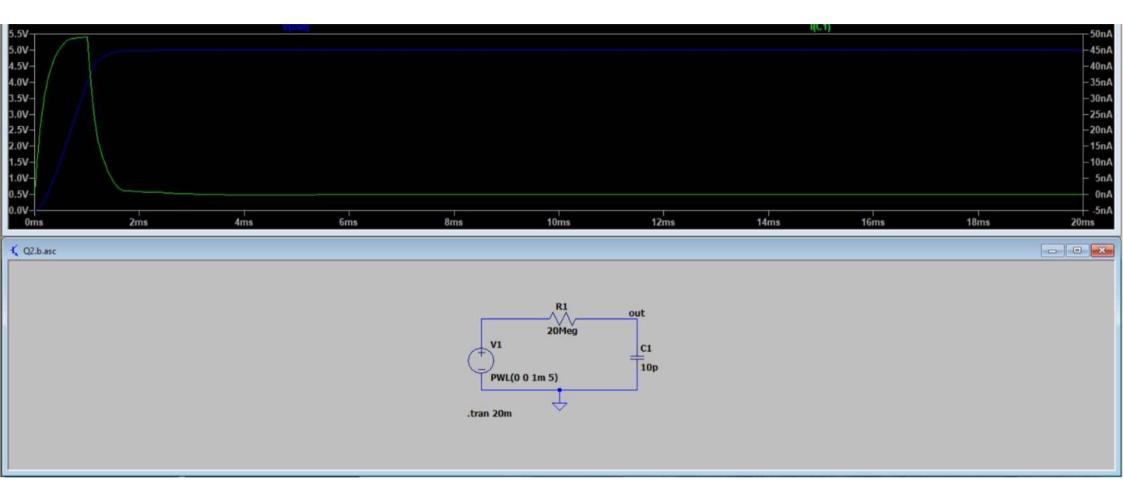
$$\int \frac{dt}{RC} = \int \frac{dV_c}{V_0 - V_c}$$

$$\frac{t}{ec} = -\left[\ln\left(v_0 - v_c\right)\right]_0$$

$$-\frac{t}{ec} = \ln(v_0 - v_c) - \ln v_o$$

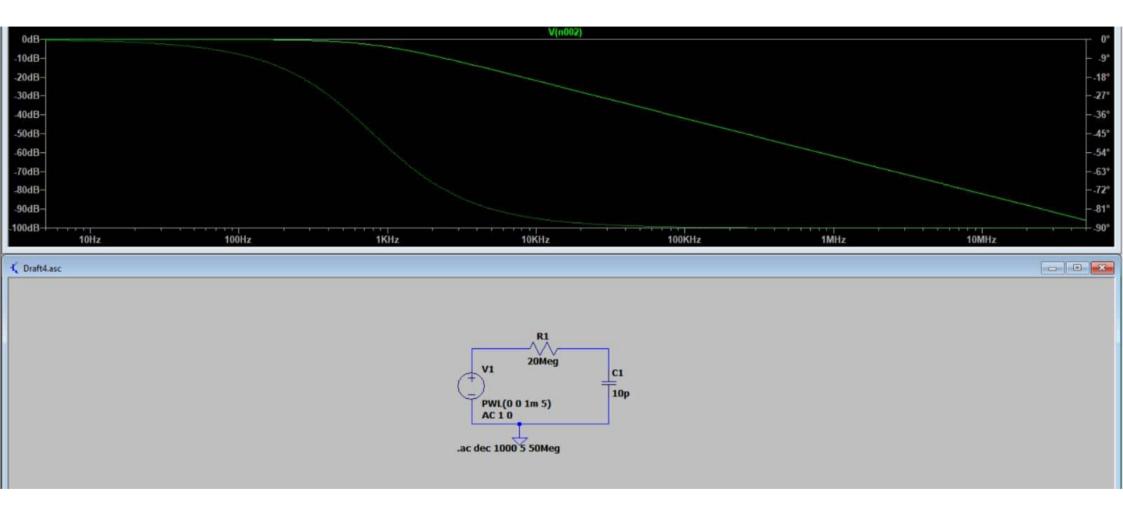
$$-\frac{t}{eL} = ln \left(\frac{V_0 - V_L}{V_0} \right)$$

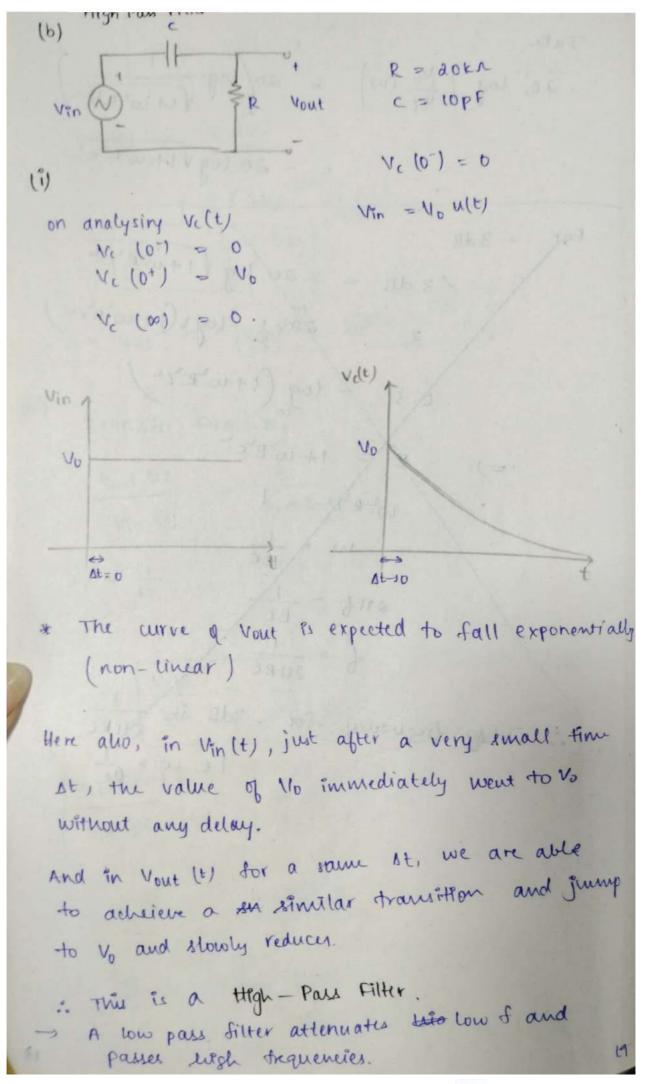
Vector volli-e-t/Rey Vout = Vout 1-e-4

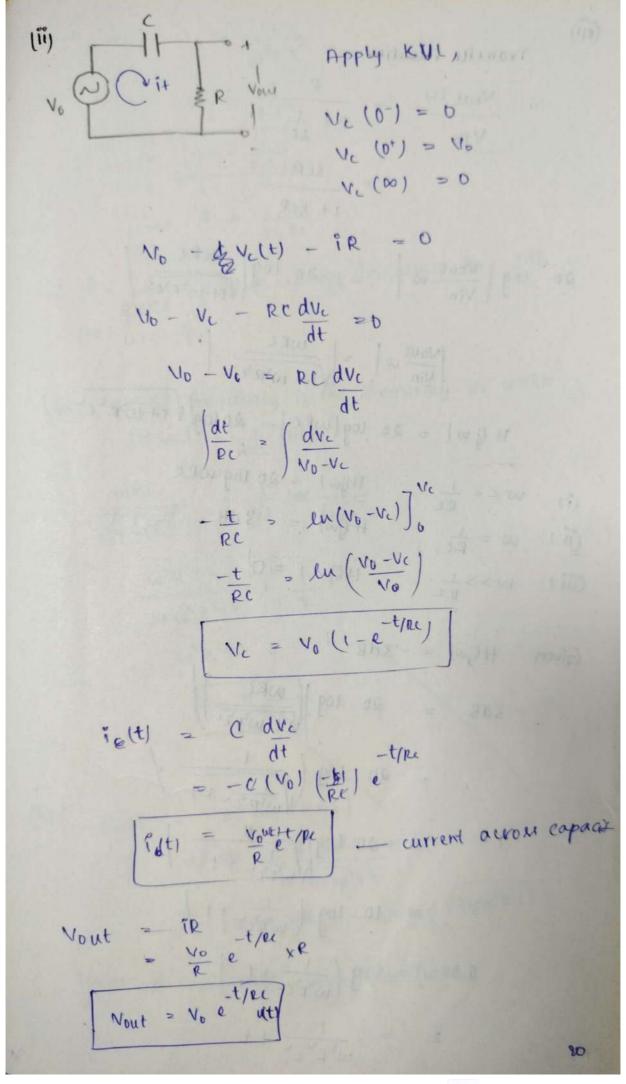


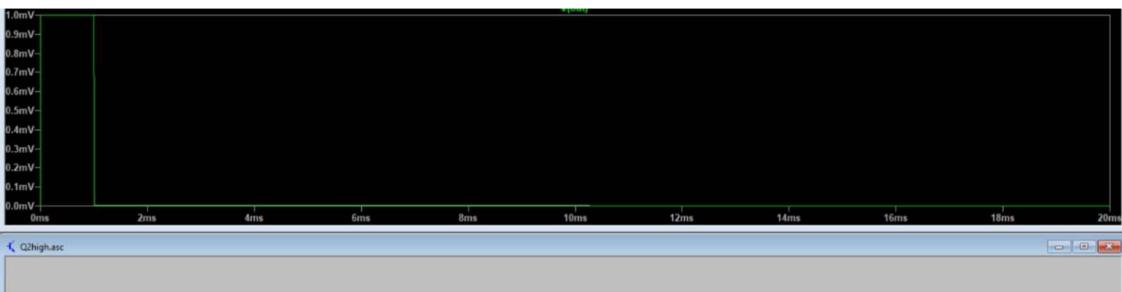
(111) Transfer Function Mout - tan (wec) Take $20 \log \left| \frac{1}{V_1}(w) \right| = 20 \left(\log \frac{1}{\sqrt{1 + w^2 R^2 C^2}} \right)$ $= -20 \log \sqrt{1 + w^2 R^2 C^2}$

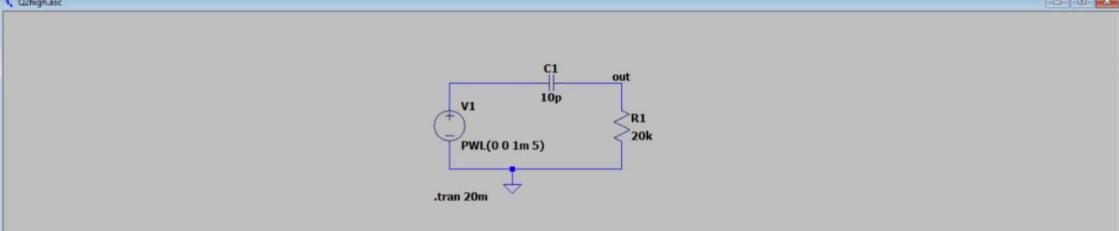
3 dB frequency is the frequency at which H (jw) = 1/2 |H(g) | VI+ W22°C = VI VI+0 VIIW'R'CL = VI Warter = 1 W = I de = 1 3 dB cutoff frequency is w= L 100 Gain = 20 wg (HLS) Vin = 1+RR = 20 cog 1 = -20 log VI+W22°C Phone (Ø) = -tan-1 (wee)

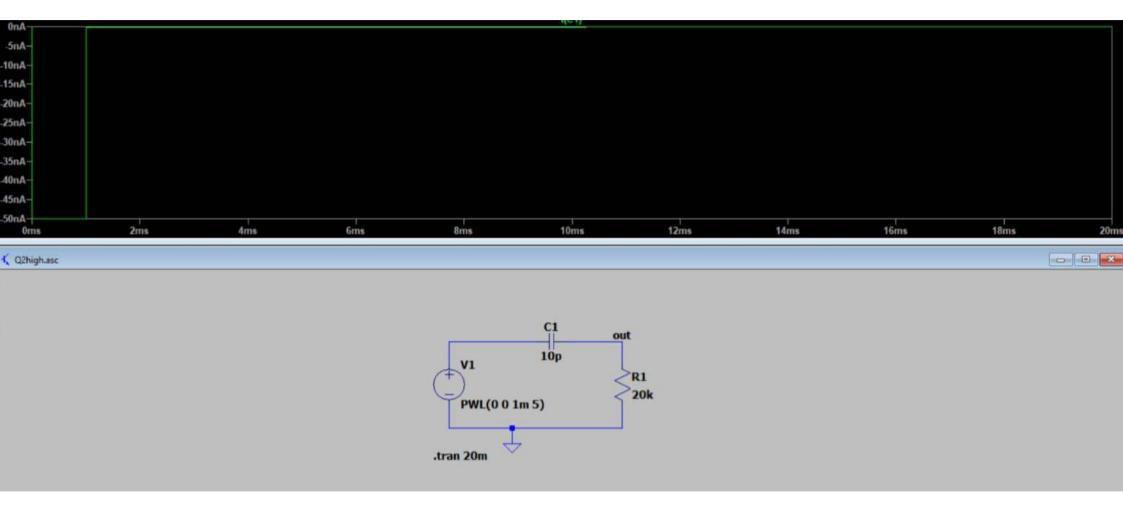












Transfer Function

Volt (1)

Volt (1)

R +
$$\frac{1}{4c}$$

SCR

1+ &cR

20 $\log \left| \frac{Vout}{Vin} w \right| = 20 \log \left| \frac{wRc}{Vt + w^2 e^2 c^2} \right|$
 $\left| \frac{Vout}{Vin} w \right| = \left| \frac{wRc}{V1 + w^2 e^2 c^2} \right|$

H (j w) = 20 $\log \left| wRc \right| - 20 \log \left| V + w^2 e^2 c^2 \right|$

(i) $w < < \frac{1}{2c}$

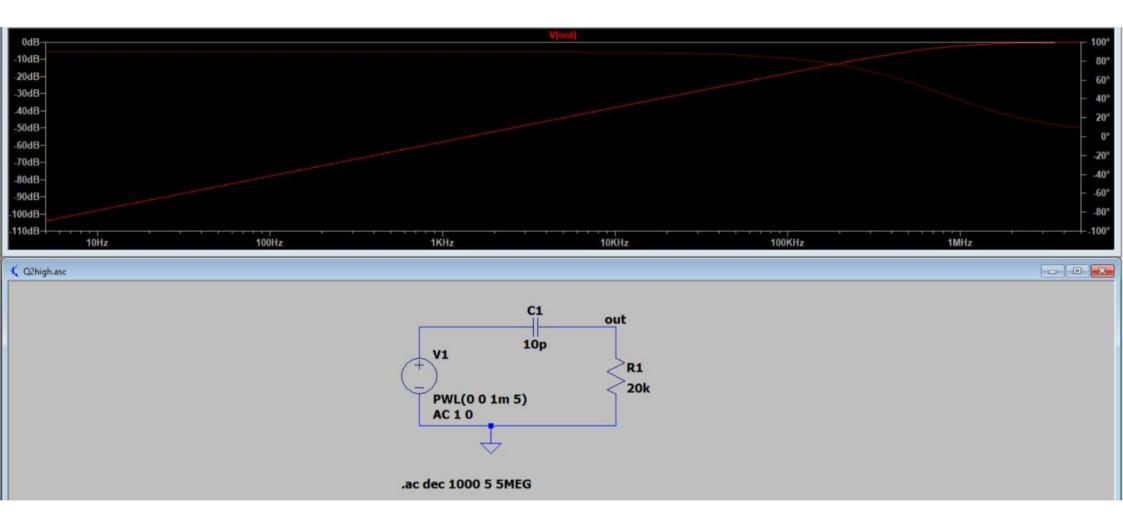
H (jw) = -3 dB.

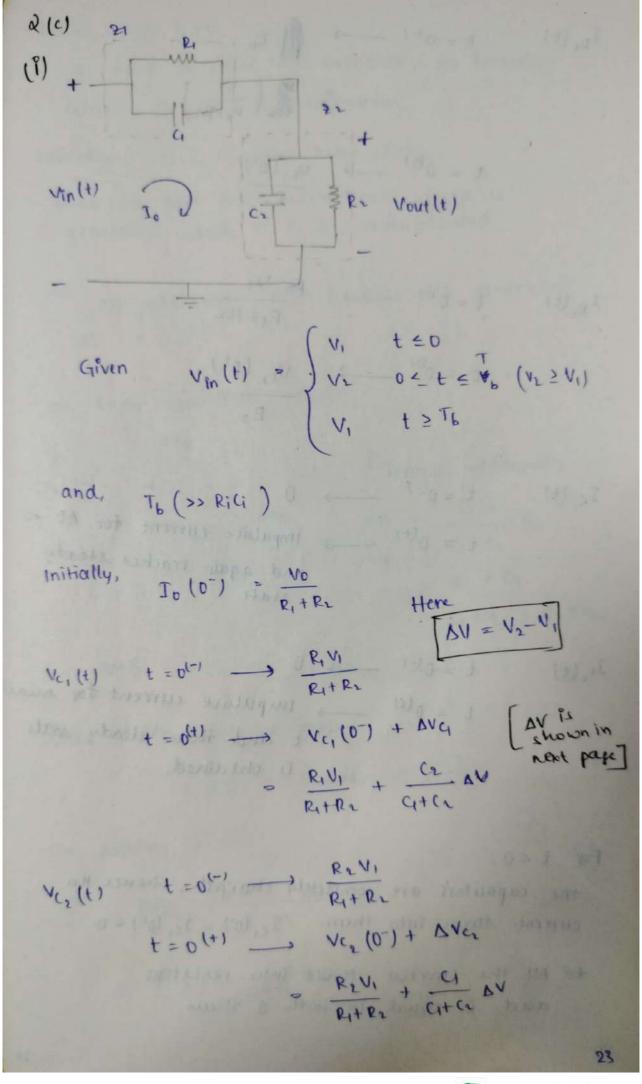
(iii) $w > \frac{1}{2c}$

H (jw) = -3 dB.

3dB ut-off frequency is the frequency at which
$$|H(s)| = \frac{1}{\sqrt{2}} |H(0)|$$

22





$$T_{R_1}(t) \qquad t = 0^{(t)} \longrightarrow M \qquad T_0$$

$$= M_1 \left(\frac{V_1}{R_1 + R_2}\right)$$

$$t = 0^{(t)} \longrightarrow V_{C_1}(0^t)$$

$$R_1$$

$$\begin{array}{cccc}
\mathbf{I}_{R_{2}}(t) & t = 0^{t} & \longrightarrow & \mathbf{E}_{1} \downarrow & \mathbf{E}_{2} \\
t = 0^{t} & \longrightarrow & \mathbf{V}_{C_{2}}(0^{t}) \\
& & & & & & & \\
& & & & & & & \\
\end{array}$$

$$T_{c_{i}}(t)$$
 $t = 0^{(-)}$ \longrightarrow 0
 $t = 0^{(+)}$ \longrightarrow Impulsive current for $\Delta t \rightarrow \infty$
and again reaches steady
state

For t <0, the capacitor's are completely charged, hence no current flows into them. Ic, (0) = Ic, (0+) = 0. to All the current thous into resistors and is equal in both a them

Now, when too

as no P in series with Capacitor, an impulsive current I stowe into capaciton.

Initially, a & a have some charge

Now, we give some additional charge is generated which is to be redistributed.

lo, an additional DV4 & DVcz are generated at += 0+. Drus no mark and a constant

at t=0, equivalent circuit,

$$\Delta V_{c1}$$
 ΔV_{c1}
 ΔV_{c2}
 ΔV_{c2}
 ΔV_{c2}
 ΔV_{c3}
 ΔV_{c4}
 ΔV_{c4}
 ΔV_{c5}
 $\Delta V_$

= 9 AV4 + C2 AV62

Since charge must be conten

On solveng (D, 1)

Alter tso,

Impulsive current goes înto capacitors.

But still small amount of I goes into the resistor

i.e.

IR. (0+) = Ver (0+)

$$I_{R_1}(0^+) - \frac{V_{L_1}(0^+)}{P_{L_1}}$$

$$I_{R_2}(0^+) = \frac{V_{L_1}(0^+)}{P_{L_2}}$$

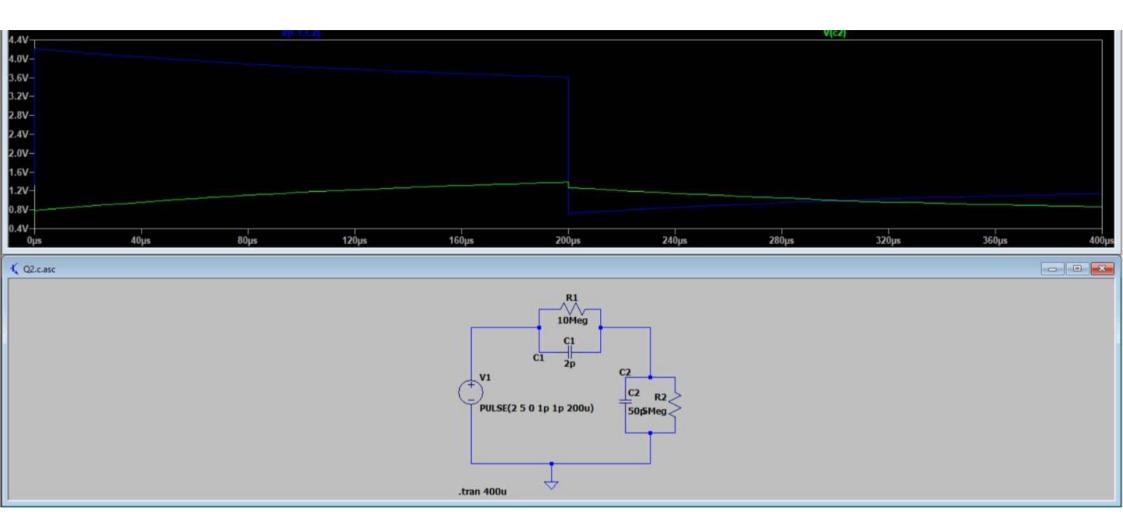
Because of this there are small transient change in capacitor and is not constant.

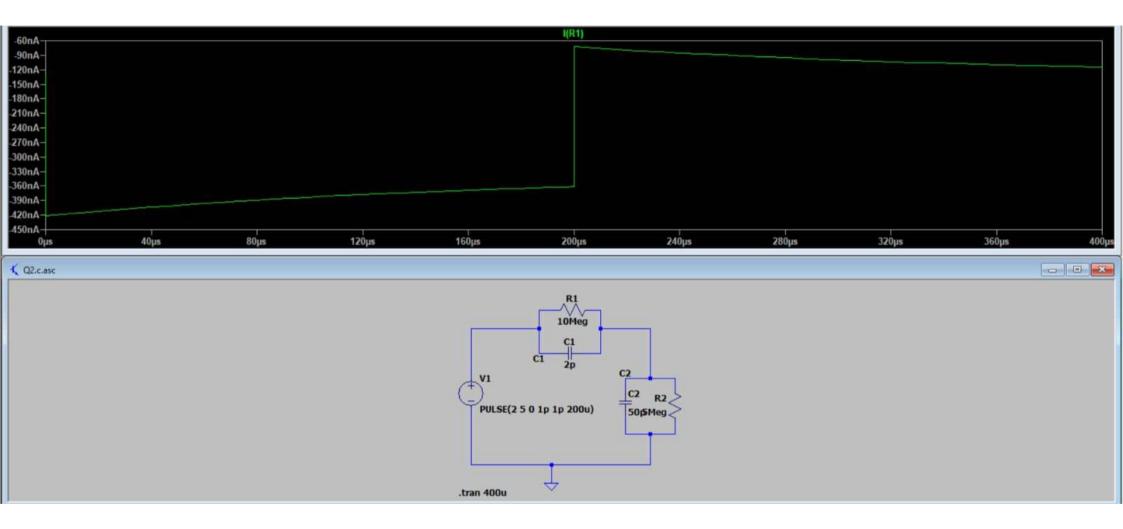
eo, we need

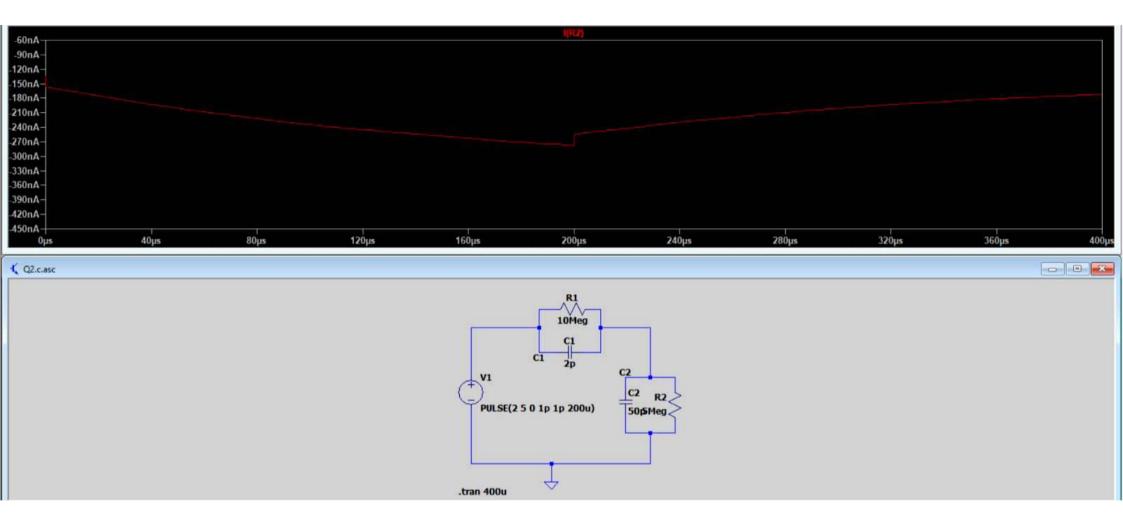
$$I_{\varrho_1}(0^+) = I_{\varrho_2}(0^+)$$

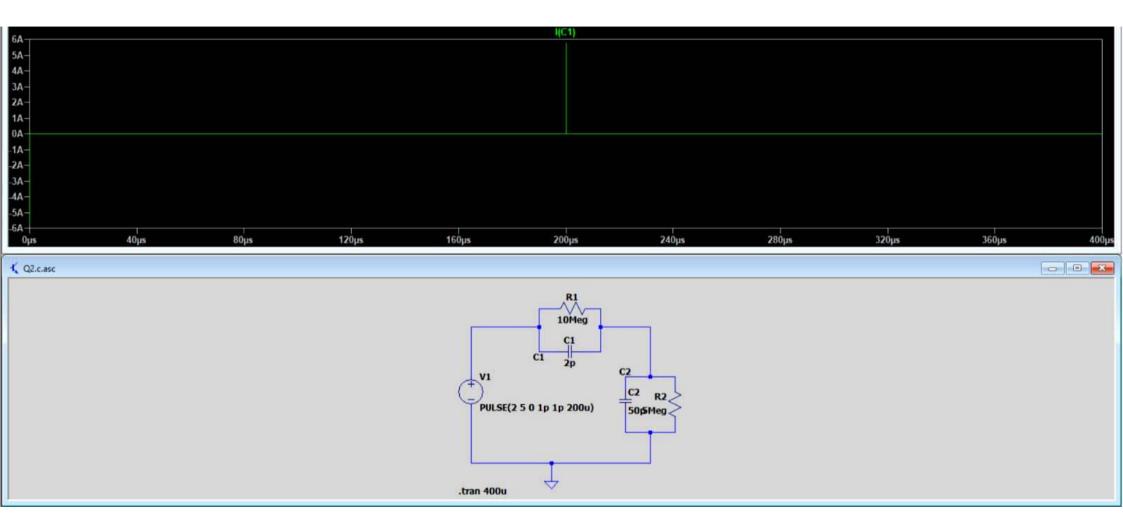
when this is achieved, capacitor are fully charged and reach steady state

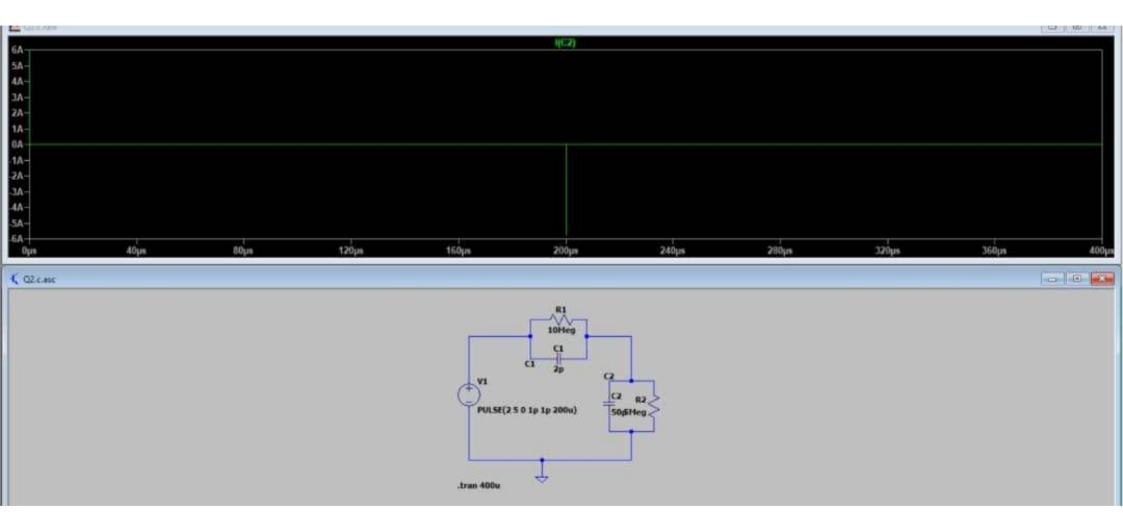
if this is possible, then Vc, (0+) & Vc (0+) will remain fixed without transfert changes.

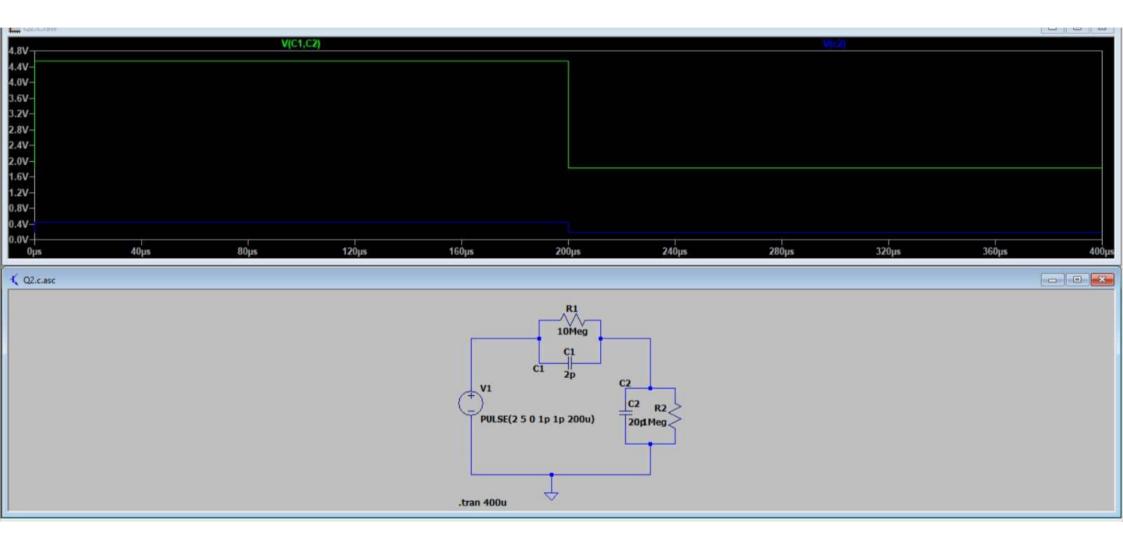


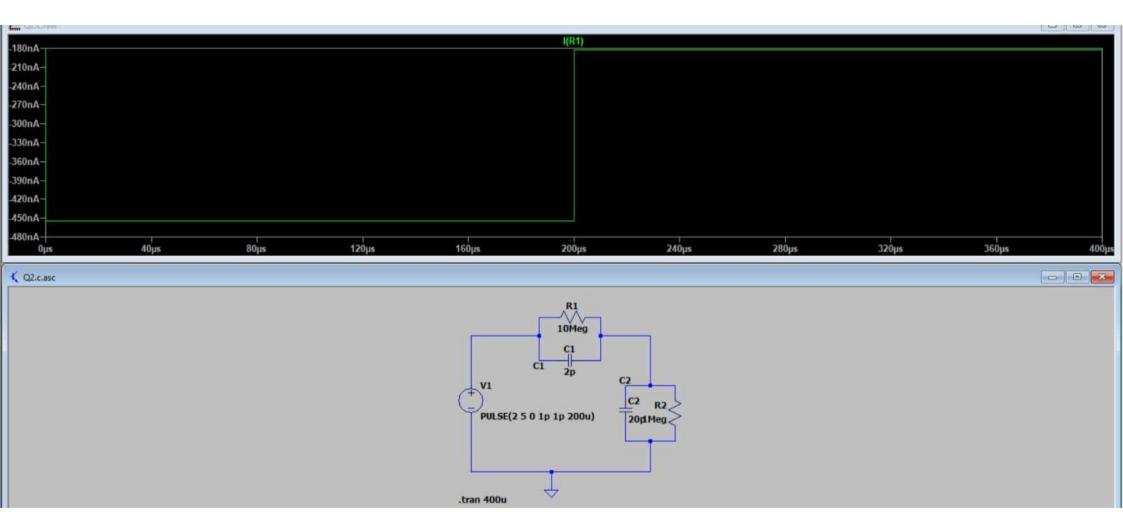


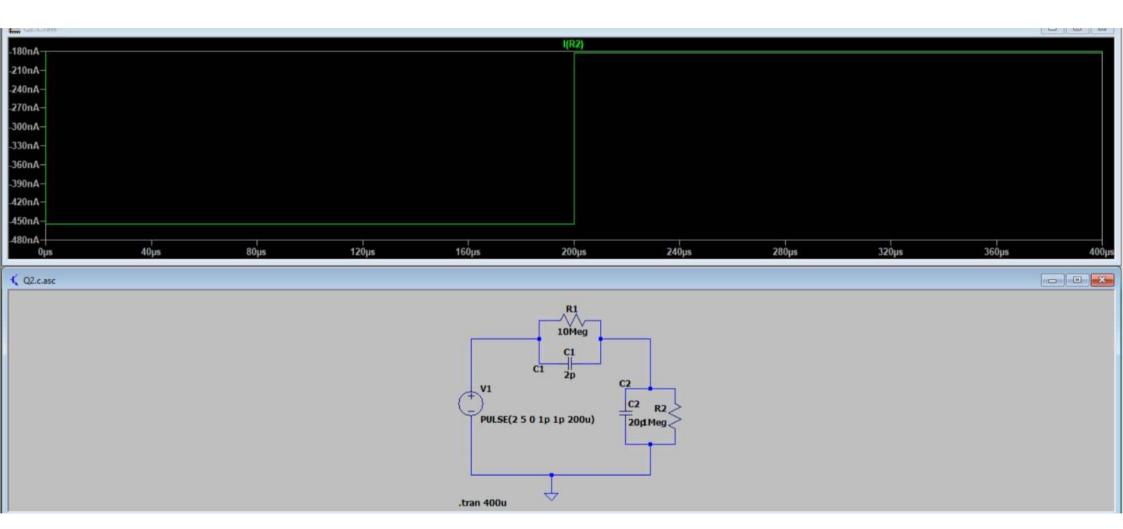


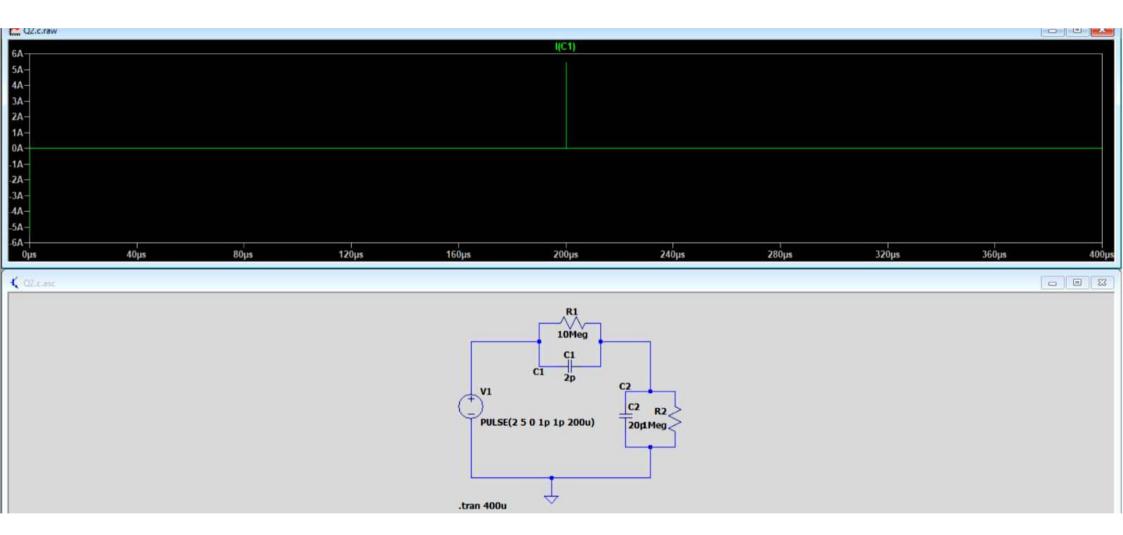


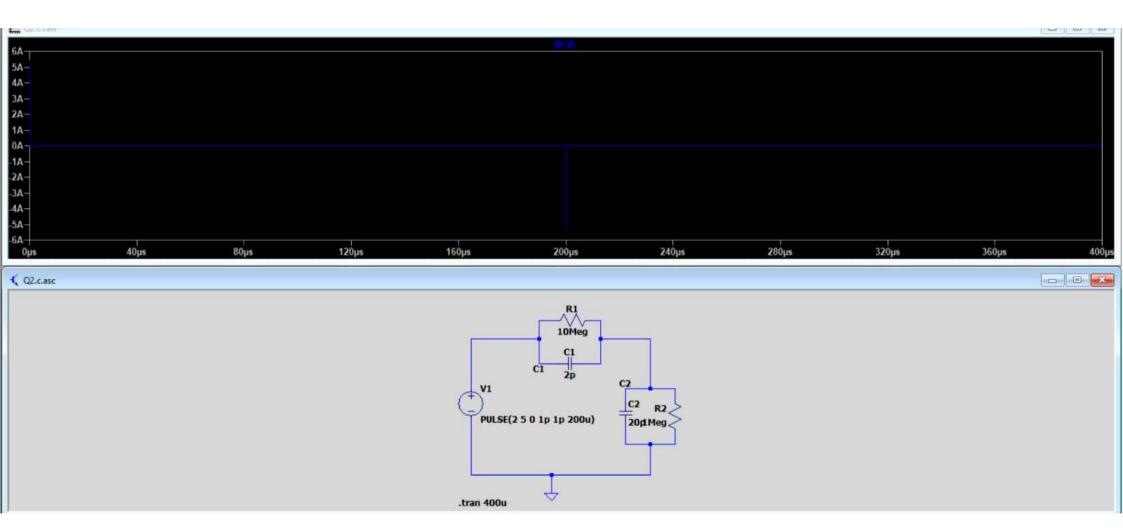












(iv)

Transfer Function:

when,

Transfer Sunction is constant.

Hence, it allows all the frequencies to pass through it.

: It le called an "ALL PASS FILTER".

WOLA

in

NOW .

in this care

if PICI < P2Cz

here, as not => Vout 1 => Low pass filter.

Mature of circuit

R4C1 = R2C2 → All Paus circuit

R4C1 > R2C2 → High paus

R4C1 < R2C2 → Low paus

(ii) Yer, the circuit allows to gupans quick transitions in Enput to the output in the cases where RICI > RICI [High pass]

And, It allows the slow tramitions in input to output when Rige Rice [Low Pan]

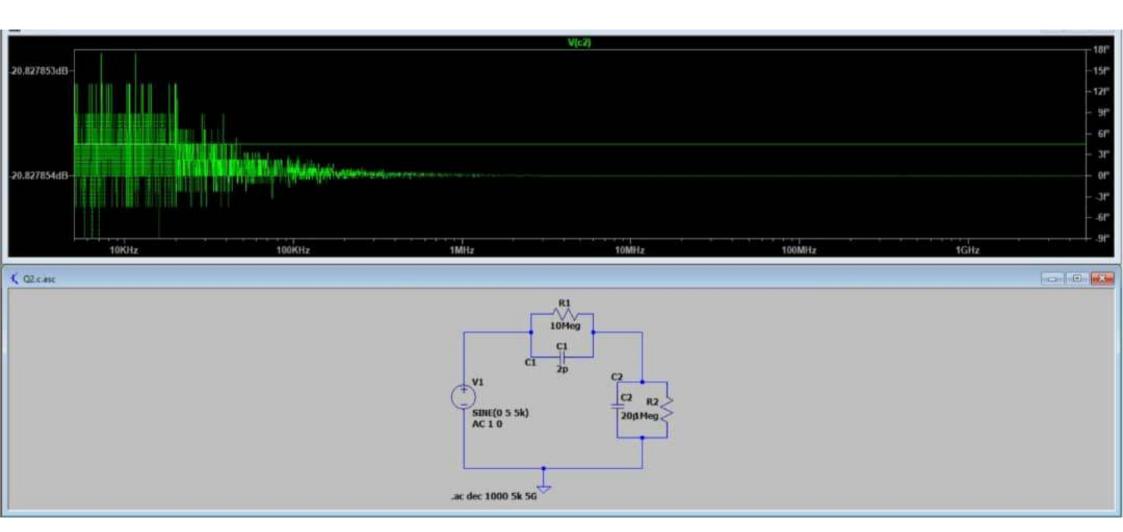
till)

(v) By observing the transfer takeour Bode that plot believe, we can clearly tell that transfer function of this circuit is constant

when Rici = Ricy at - 20 dB (approx).

Hence, there doesn't exist a -3dB bandwidth in the circuit.

state shown below at



(vi) From the three plots shown below at input frequencies f = 1kHz, 5kHz, lokHz, we can see that there is a change in the output frequency but not the amplitude.

