Lesson Plan			
19ECS202: COMPUTER ORGANIZATION AND ARCHITECTURE			
Name of the Faculty: Dr V Jyothi			
Class: II-year CSE			
Sl. No.	**Unit**	**Topic**	**No. of. Classes**
	UNIT I	**Register Transfer and Micro operations**	
1		Register transfer language, register transfer	1
2		Bus and memory transfer	2
3		Arithmetic micro-operations	2
4		Logic micro-operations	1
5		Shift micro-operations	1
6		Arithmetic logic shift unit	1
		Total	***8***
	UNIT II	**Basic Computer Organization and Design**	
7		Instruction codes, computer registers	1
8		Computer instructions	1
9		Timing and control, Instruction cycle	1
10		memory-references instructions, Input-output instructions, and interrupts	2
11		Complete computer description	1
12		Design of the basic computer, Design of accumulator logic	1
		Micro programmed Control	
13		Control memory, Address sequencing	1
14		Micro program Example	1
15		Design of control unit.	1
		Total	***10***
	UNIT III	**Central Processing Unit**	
16		Introduction, general register organization	1
17		Stack organization	1
18		Instruction formats, Addressing modes	1
19		Data transfer, Data manipulation, Program control.	2
		Pipeline and Parallel Processing:	
20		Parallel processing, pipelining, arithmetic pipeline,	1
21		instruction pipeline	1
		Computer Arithmetic:	
22		Introduction, addition, and subtraction,	1
23		decimal arithmetic unit,	1
24		Booth's multiplication algorithm.	1
		Total	***10***
	UNIT IV	**Input-Output Organization**	
25		Peripheral devices, I/O Interface	2
26		Asynchronous Data Transfer, Modes of Transfer	2
27		Priority Interrupt	1

28		DMA	1
29		I/O Processor	1
30		Serial Communication	1
		Total	***8***
	UNIT V	**Memory Organization**	
31		Memory Hierarchy, Main Memory	2
32		Auxiliary Memory	1
33		Associative Memories, Cache Memory	2
34		Virtual Memories	2
35		Memory Management Hardware	1
		Total	**8**
		Total number of classes	**44**