





Signal Name	Signal Values	
IRD1/1	0	; 0,0,IR[15:12]
	1	; 1,1,IR[15:14],0,IR[12]
COND(COND2/1, COND1, COND0)	COND0~COND	
	3	; Original Cond Signal
	COND5	; Protection Exception
	COND6	; Unaligned Access
LD.SSP/1	COND7	; Interrupt
	No, LOAD	
LD.VECTOR/1	No, LOAD	
LD.PSR/1	No, LOAD	
LD.PSR15/1	No, LOAD	
GateVector/1	No, YES	
GatePSR/1	No, YES	
PCMUX1/1	PC	; Select PC
	PC-2	; Select PC-2
REGSRCMUX/1	BUS	; Select value from bus
	SSP	; Select value from SSP register
INCDECMUX/2	0, 1	; Select SR1OUT
	2	; Select SR1OUT - 2
	3	; Select SR1OUT + 2
VECTORMUX/2	X01	; Select timer interrupt vector
	X02	; Select protection exception vector
	X03	; Select unaligned-access exception vector
	X04	; Select unknown-opcode exception vector
SR1OUTMUX	0	; Select data from output of INCDECMUX
	1	; Select data from SEXT&LSHF1ed vector
BASEADDRMUX	0	; Select data from the output of SR2MUX
	1	; Select 0x0200
PSR15MUX	0	; Select value 1
	1	; Select value 0
DRMUX1	0	; Select value from output of DRMUX
	1	; Select 110
SR1MUX1	0	; Select value from output of SR1MUX
	1	; Select 110

