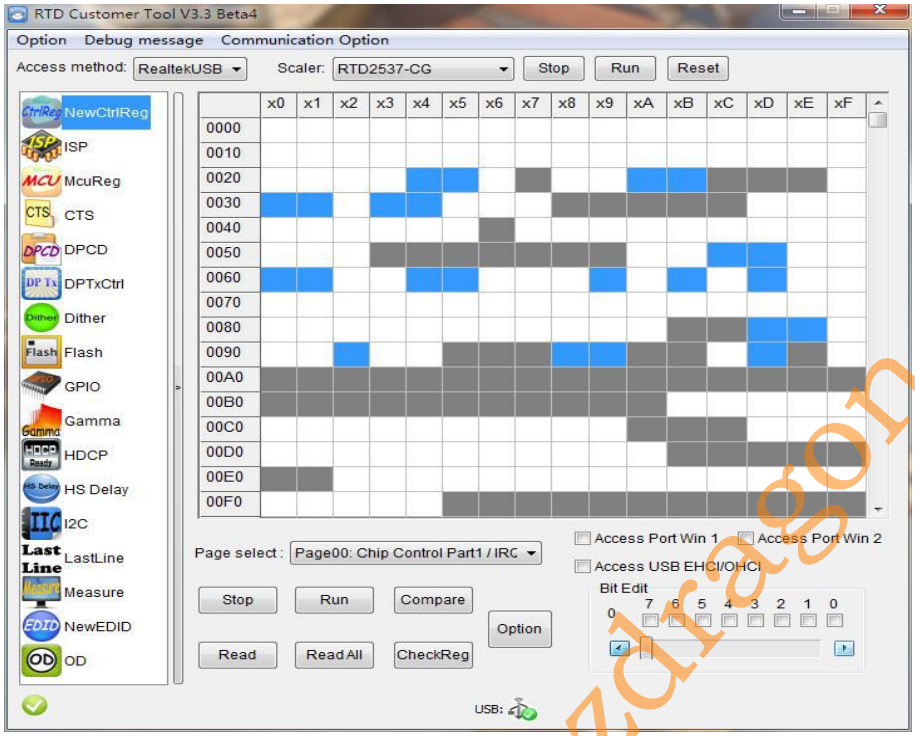
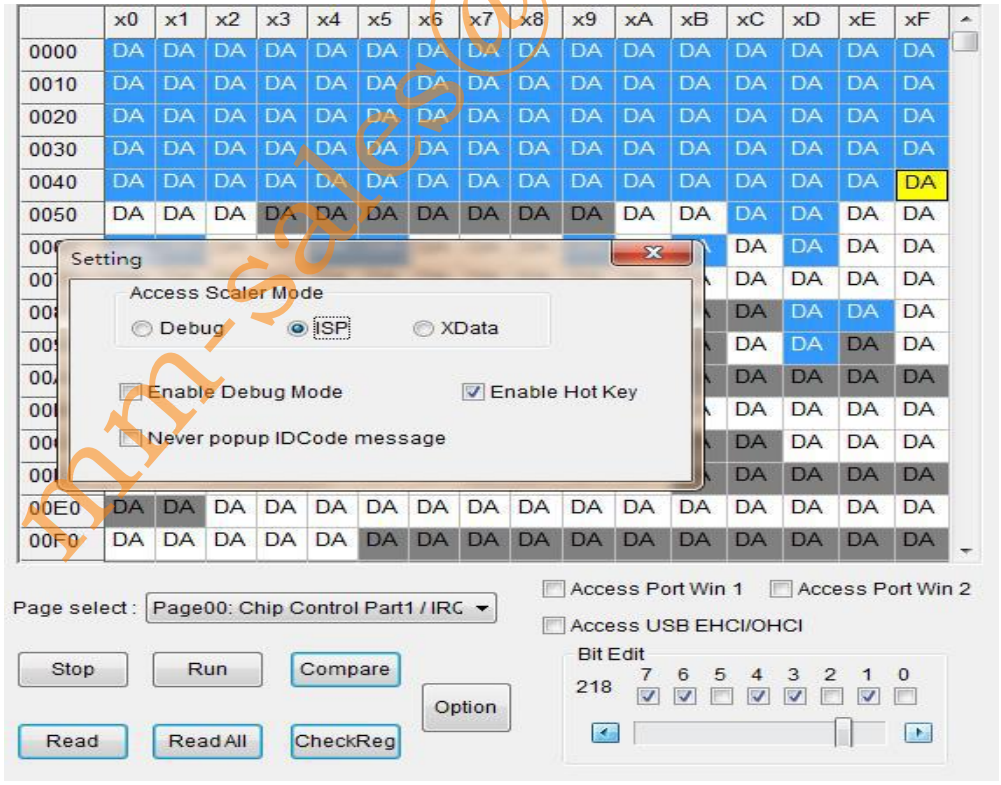
RTDTOOL-NewCtrlReg 使用

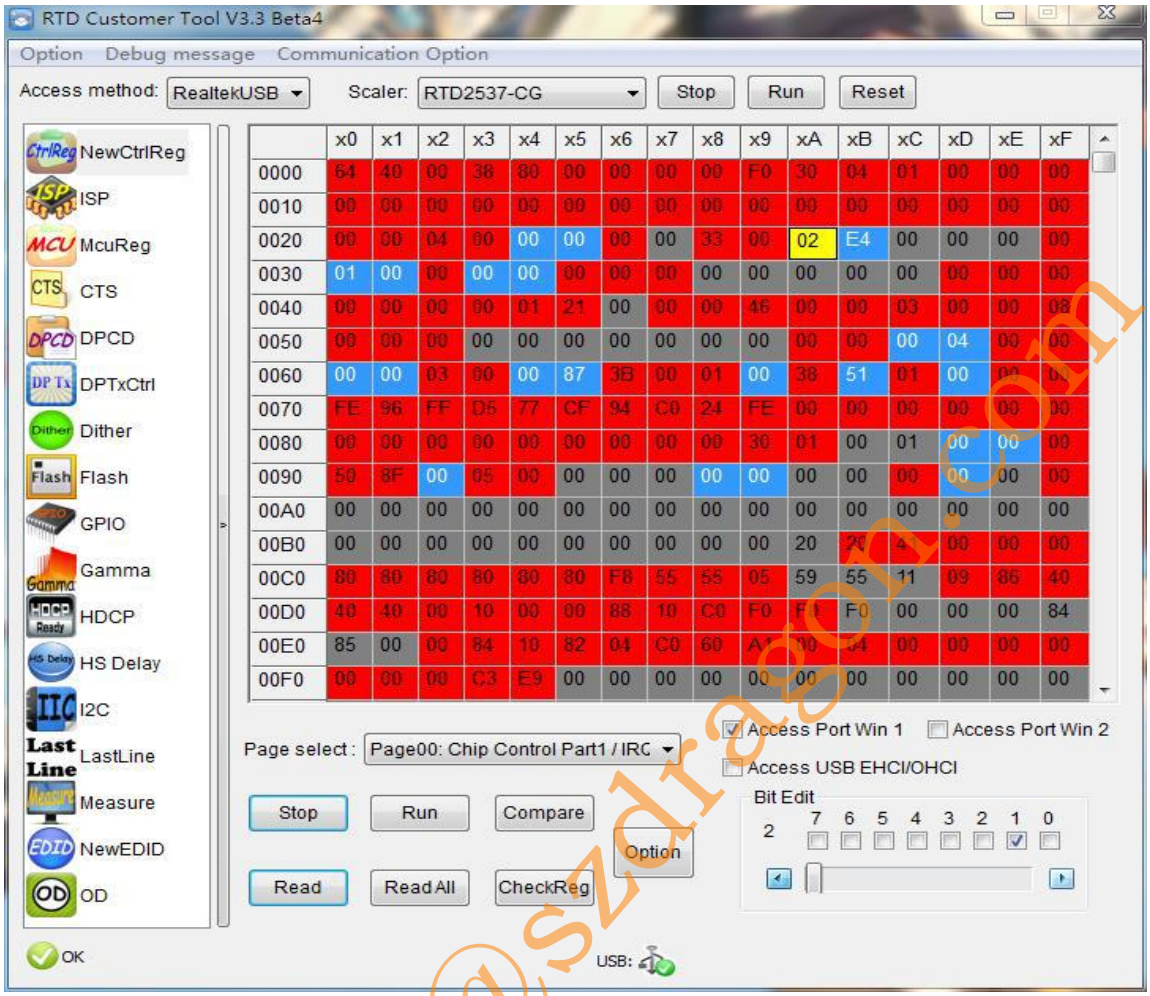
1.打开 RTDTOOL.exe 软件，选择 NewCtrlReg 功能



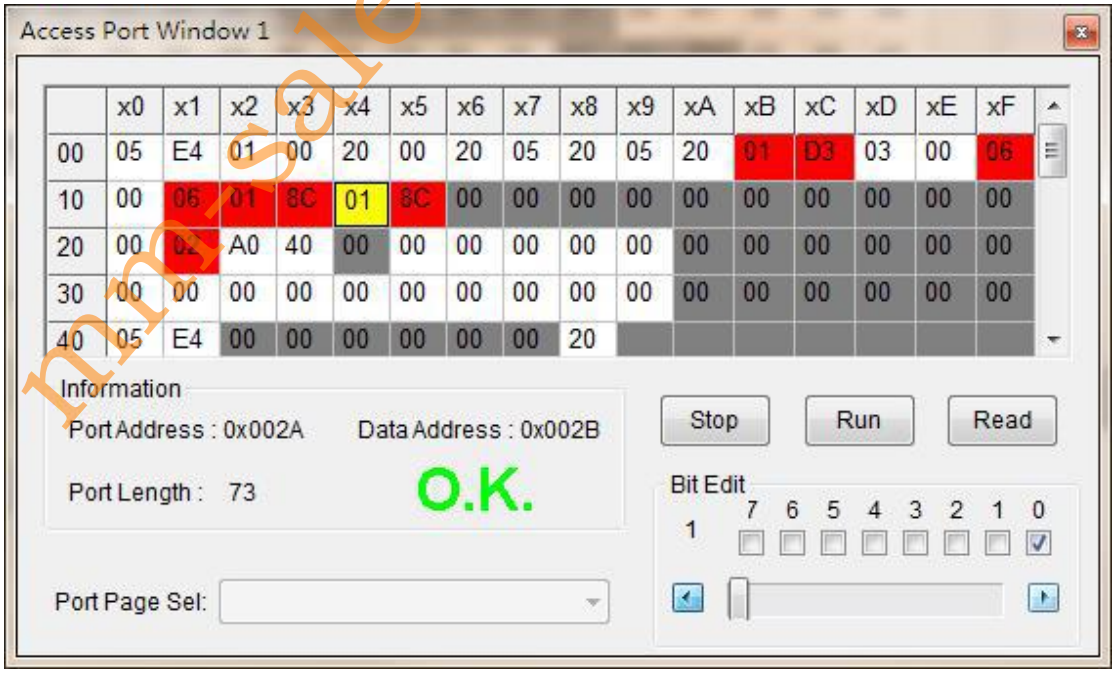
2.点击左上角“Option->Setting”选择 ISP



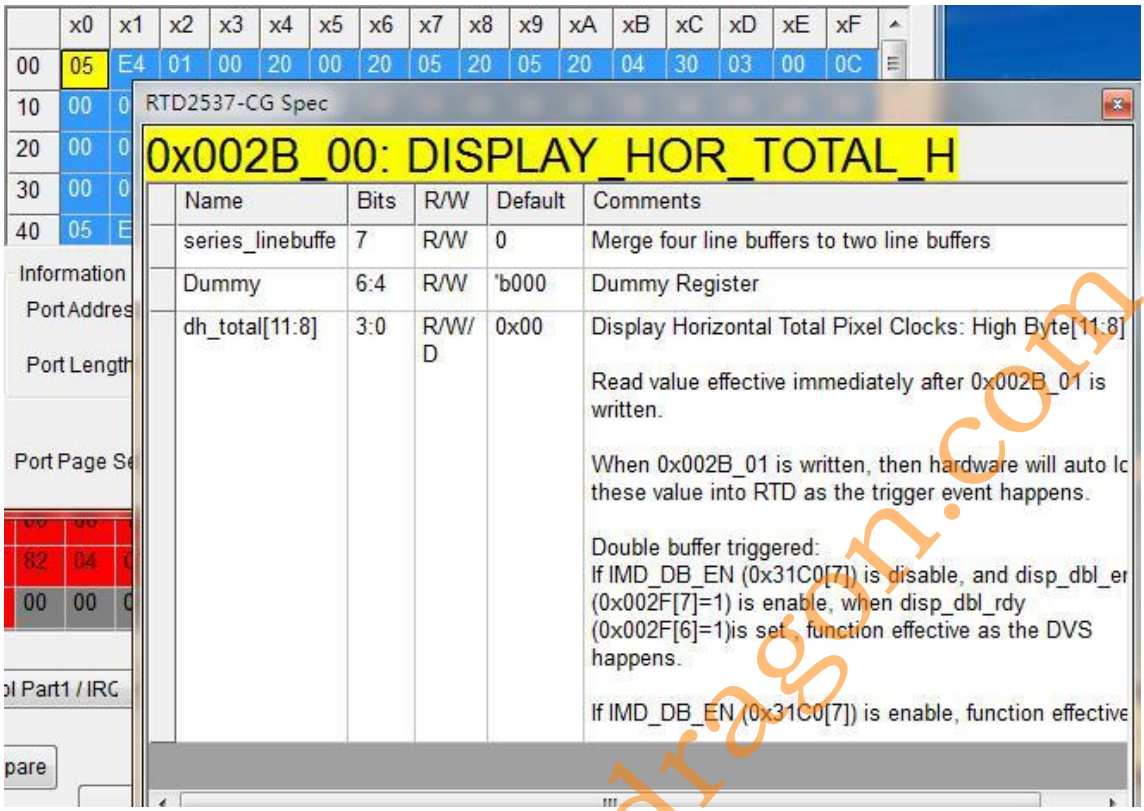
3.VGA 连接板子和电脑，待到测试屏出现图像时，点击“Read”读取数据



4.选择寄存器 A（上图黄色 02 处），点击右下角的“Access Port Win1”接着点击 Read 后读取的数据如下图：



5.单击第一个数据（05，呈黄色），按下 F1，出现寄存器位的值，如下图所示：



6.通过一一读取各个寄存器位，最后汇成的数据如下图：

可以得到：

DH_TOAL:0X05E4-1508

DV_TOAL:0X01D3-467

dh_hs_width:0x0001-1

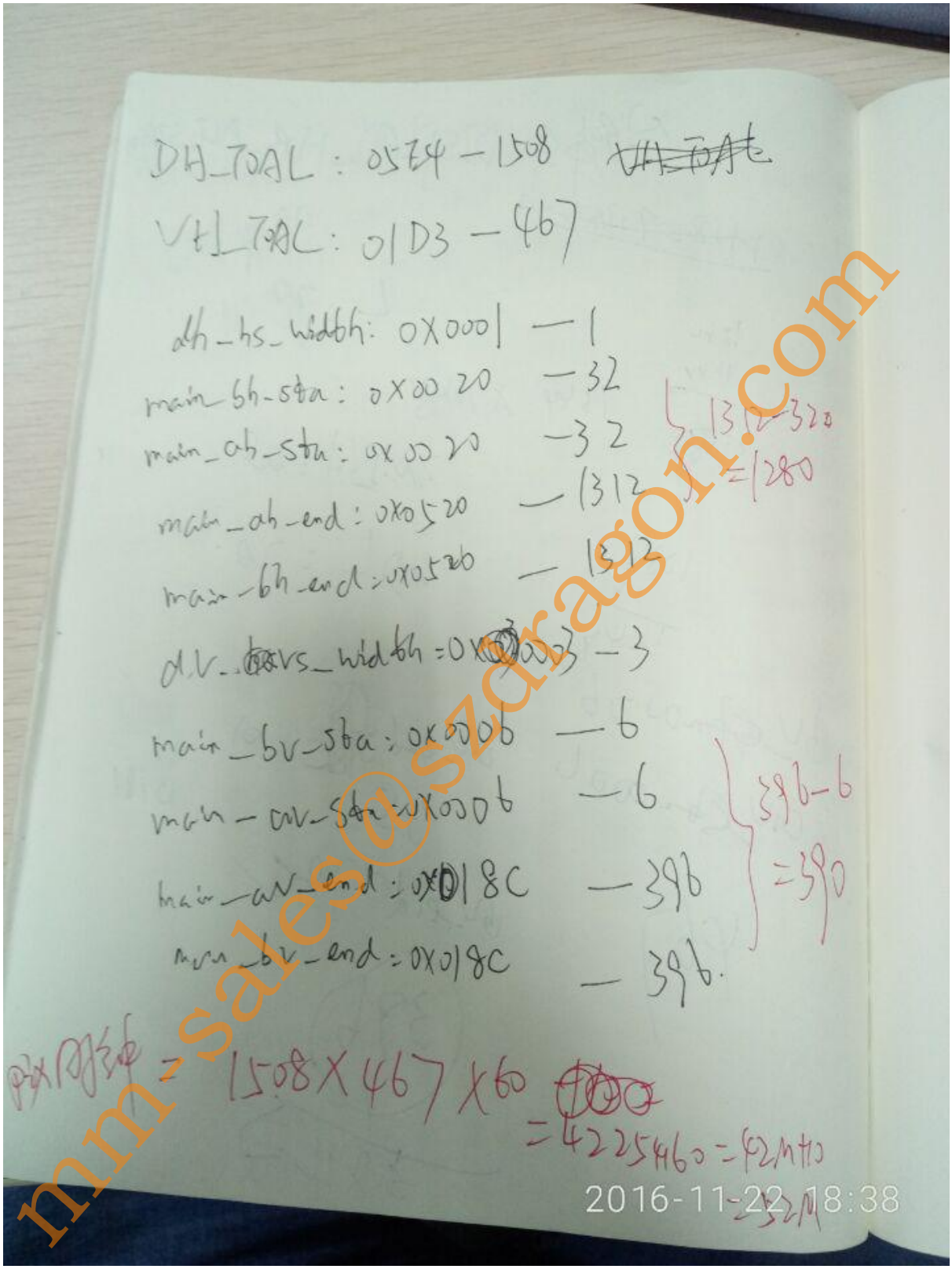
dv_vs_width:0x0003-3

分辨率:

width:1312-32=1280;

height:396-6=390

.....具体见下图



7.修改程序的参数为：

```
//-----------------------------------------------
//1280x800
code PanelType Panel3 =
{
    // Panel Style
。。。。

    32,          // Display Horizontal Start Position
    1280,        // Display Horizontal Width
    3016, //1508, // Display Horizontal Total Clock Number in One Display
Line
    3016, //1508, // Display Horizontal Total Clock Number in One Display
Line for CVBS PAL
    3016, //1508, // Display Horizontal Total Clock Number in One Display
Line for CVBS NTSC

    6,   // Display Vertical Start Position
    390,         // Display Vertical Height
    467,         // Display Vertical Total Line Number in One Frame

    8, //1,      // Display H Sync Width
    3,           // Display V Sync Height

    94, //52,    // Typical Pixel Clock in MHz

    1100,       // H Sync Max Freq Unit in 0.1 kHZ
    100,        // H Sync Min Freq Unit in 0.1 kHZ
    760,        // V Sync Max Freq Unit in 0.1 HZ
    490,        // V Sync Min Freq Unit in 0.1 HZ

    // TTL setting
    //(2 << 4) |  // Delay
    //(1 << 1) |  // DCLK output enable
    0x00,         // DCLK Polarity

};
```

蓝色部分的参数是根据读到的寄存器参数填写进入，后来编译发现没图像，最后将 DH_TOAL,DV_TOAL 翻一倍即红色部分才出图像。