

# Deep Learning Hardware 설계 경진대회 Orientation, code structure

2022.02.07 (Mon)



# Road map

About AIX

Code structure

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# Organizing committee

Representative organizing committee



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## Road map

Code structure

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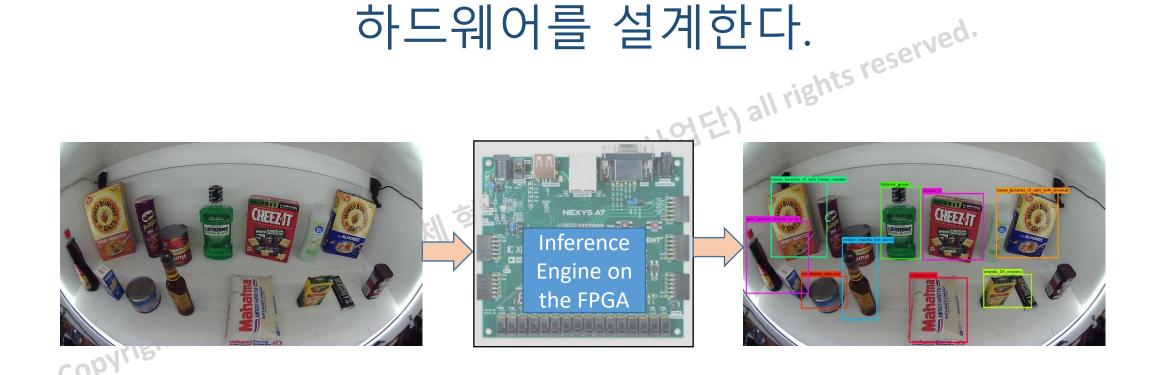
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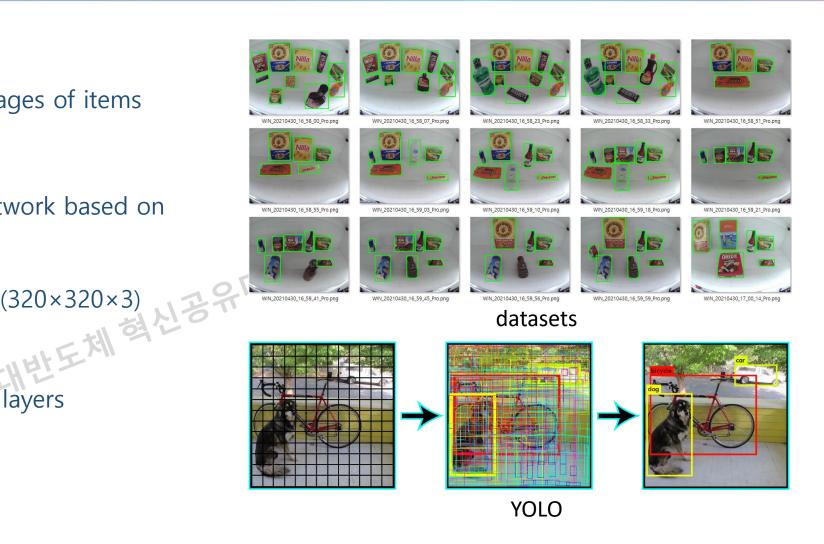
### 설계의 목표

무인판매대에서 상품 인식을 위하는 딥러닝 추론 하드웨어를 설계한다.



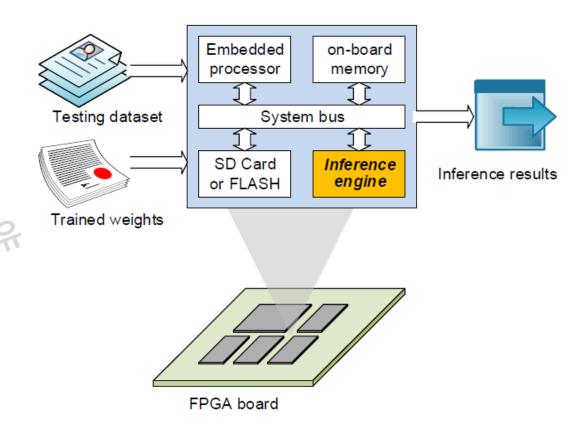
# Object detection (Application-based)

- Training dataset
  - Collected 200,000 images of items
  - Labelled the data
- We developed a deep network based on Tiny-YOLOv3
  - Input: an RGB image (320×320×3)
  - 22 layers:
    - 11 convolutional layers
- Max pooling



# 제공되는 것과 준비할 것 (system-based)

- 추론엔진용 딥러닝 파라미터
  - Pretrained model
- 시험 데이터셋 (상품 이미지 150장)
- 추론엔진(Inference Engine)의 reference code
  - S/W: Evaluation and Host PC (C++)
  - H/W: Components (Verilog HDL)
- Nexys A7 FPGA Board (Xilinx Artix-7 FPGA XC7A100T-1CSG324C)



#### Tutorials<sup>(1)</sup>

- Four-week tutorials are given to cover several fundamental issues in the AIX Design
  - Introduce fundamental components and their usages

#	Title	Content	Date
1	Orientation	Introduction to AIX, code structure	2/07 (Mon)
2	Network	Reference S/W, network architecture, evaluation metrics	2/10 (Thu)
3	Quantization	Model quantization, data preparation	2/14 (Mon)
4	MAC	Hardware description language, computing units	2/17 (Thu)
5	Memory	On-chip buffer, block RAM, IP generator, MIG	2/21 (Mon)
6	Bus	AXI interconnect, DMA	2/24 (Thu)
7	Integration	System integration and verification (one layer)	2/28 (Mon)
8	CPU-FPGA	PC-FPGA communication	3/03 (Thu)

<sup>(1)</sup> Tutorials do NOT aim to replace courses at school. Students are highly recommended to take relevant courses.

### 경진대회 개최 일정

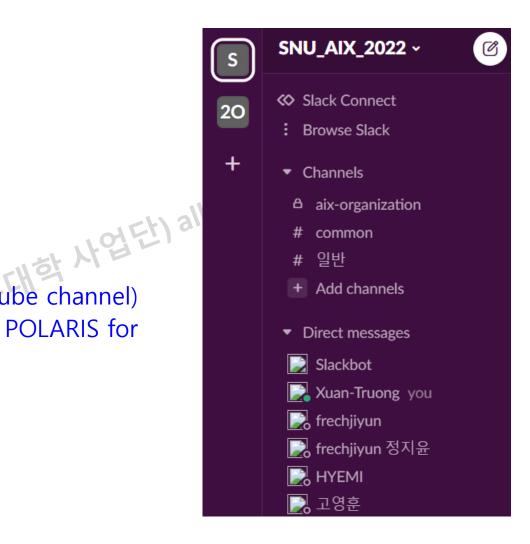
- <del>공지 및 접수: 1.10 (월)~2.6 (일)</del>
  - 사전 온라인 설명회 : 1.26(수) 오후 3시, 온라인(Zoom)으로 진행
- 본선: 2.7 (월) ~ 5.20 (금, 최종 설계 제출 마감): Code submission (5.20) 중간 평가: 4.1 (금)
- • 참가자 발표 및 최종 심사: 5.23 (월)~5.27 (금) Presentation
- 최우수팀 선정 및 시상식: 2022년 6월 중

#### Team list

- 대상 및 자격: 전국의 대학생 2~5인 구성의 팀으로 딥러닝과 하드웨어에 관심있는 누구나 참여 가능
- 접수 방법: <a href="https://forms.gle/dq44a1cjYeZcj5jH8">https://forms.gle/dq44a1cjYeZcj5jH8</a>
- 공유대학 사업단) all rights reserved. o 387 responses, some of them are duplicated
  - ~30 universities
  - A new registration is no longer accepted
  - o 374 students from 111 teams are approved.
  - o Four students or one-member teams are pending
    - Matching

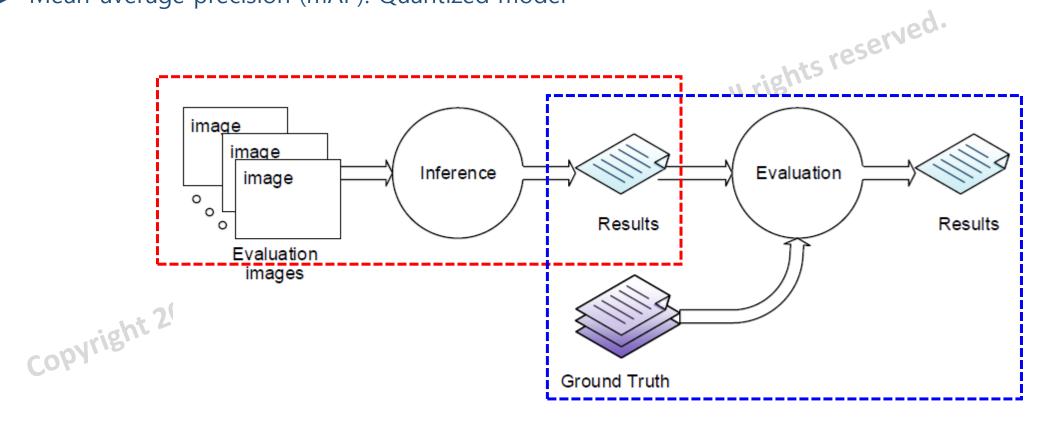
#### AIX2022 Communication

- Open discussion platform (slack)
  - Invite all participants
  - FAQ
    - List up all common questions
  - QA
  - Share materials
    - Codes
    - Documents (PPT files)
    - Link to video tutorials (POLARIS's youtube channel)
  - Note: we can not use servers from SNU or POLARIS for students from 30 universities.
- Two options for board delivery (in Feb)
  - 1. At Seoul National University
  - 2. Mail



# 결과평가: Accuracy

- 평가 데이터 셋 (참가자에게 공개하지 않음)
- ► Images of product items (150개+ 150개)
- ► Mean-average precision (mAP): Quantized model



# 결과평가: Accuracy

- Intersection of Union (IoU)
  - Intersection: Overlapped area between two bounding boxes
  - Union: Combined area of two bounding boxes
- Mean average precision (mAP)
  - Calculate the AP at IoU threshold 0.5.
  - INT8 quantized accuracy.
- The code for evaluation is given



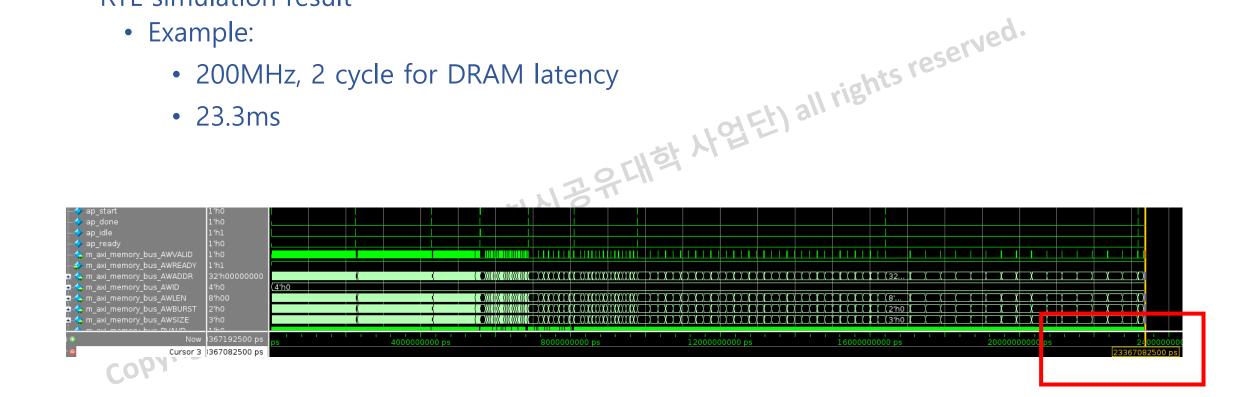
```
(TP = 60, FP = 24)
(TP = 24, FP = 0)
(TP = 56, FP = 82)
```

```
for conf thresh = 0.25, precision = 0.82, recall = 0.72, F1-score = 0.77
for conf thresh = 0.25, TP = 3165, FP = 688, FN = 1239, average IoU = 63.17 %
IoU threshold = 50 %, used Area-Under-Curve for each unique Recall
mean average precision (mAP@0.50) = 0.835103, or 83.51 %
Total Detection Time: 68 Seconds
```

```
conf_thresh = 0.25, precision = 0.82, recall = 0.72, F1-score = 0.77 conf_thresh = 0.25, TP = 3165, FP = 688, FN = 1239, average IoU = 63.17 %
COU threshold = 50 %, used Area-Under-Curve for each unique Recall
mean average precision (mAP@0.50) = 0.835103, or 83.51 %
```

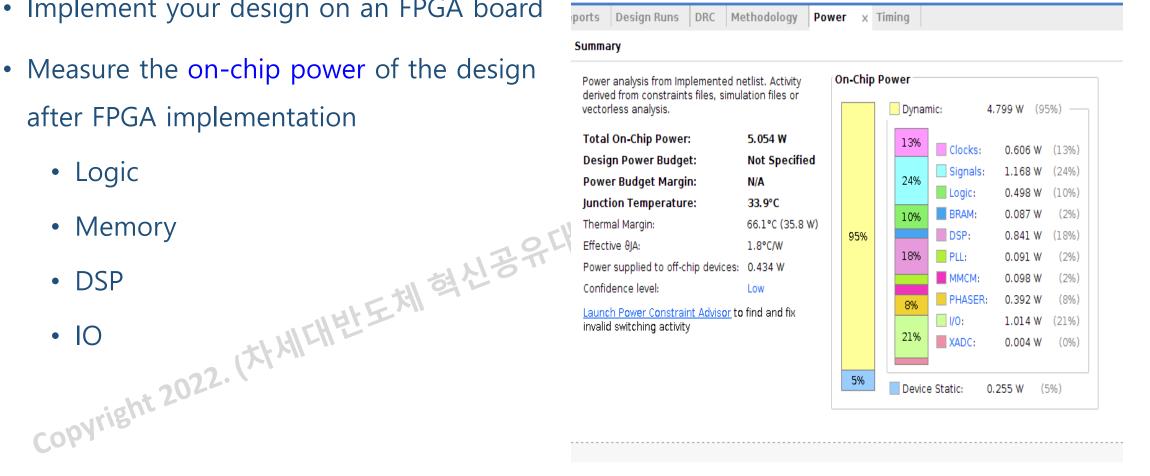
# 결과평가: Inference speed

- Inference speed measures the number of frames your CNN accelerator IP processes in a second
- RTL simulation result
  - Example:
    - 200MHz, 2 cycle for DRAM latency
    - 23.3ms



# 결과평가: Energy

- Implement your design on an FPGA board
- Measure the on-chip power of the design after FPGA implementation
  - Logic



#### Evaluation

- 중간 평가: 4.1 (금)
  - Quantized model
- Copyright 2022. (차세대반도체 혁신공유대학 사업단) all rights reserved. RTL simulation for the three-first CONV layers.

### 대회 최우수팀 수상 및 상품

• 평가 점수 기준 최우수팀 및 우수팀 선정 (3등까지)

• 1등 : 5백 만원

• 2등 : 2백 만원

• 3등: 1백 만원

• \* 최종 1,2,3등 수상팀 인공지능반도체

국제학술대회 (IEEE AICAS) 참석

\* 중간 평가 통과 팀 전원 장려상 수여 및 부상 증정



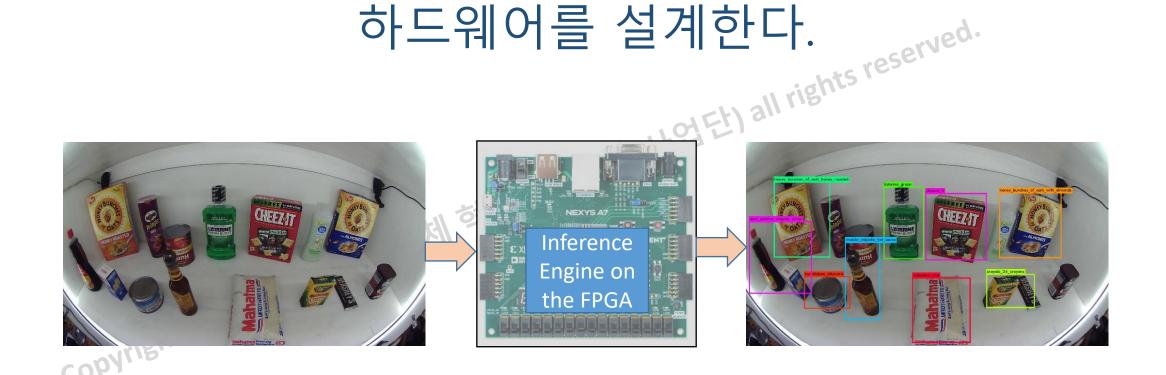
### Road map

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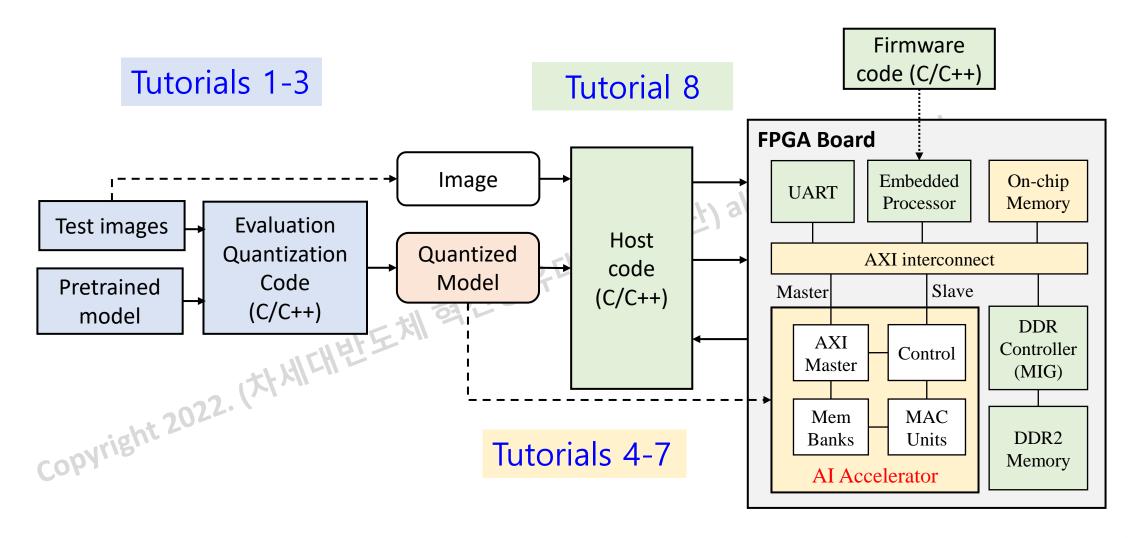
# Object detection

- Object detection = Proposal + Classification
  - Proposal: Location of an object
    - Bounding boxes: a rectangle bounding an object
  - Classification: an object's class
    - Indicate by its box's color and name tag



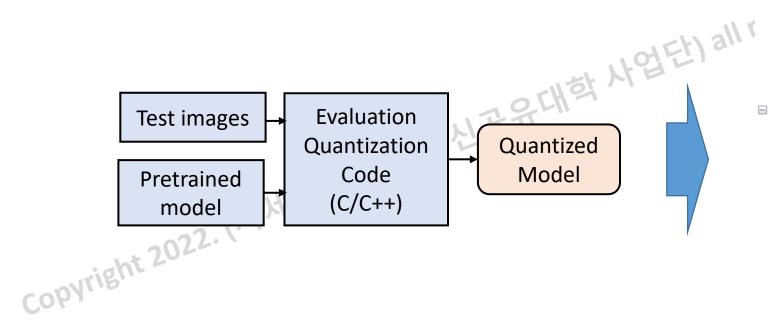


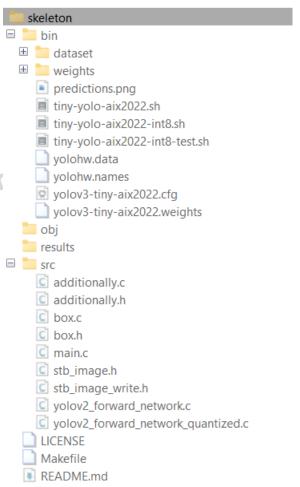
# Top structure and tutorials



### Tutorials 1-3: Reference S/W

- Skeleton
  - Test images, object classes, and ground truth
  - Pretrained model
  - Reference code for evaluation and quantization
  - The S/W code is based on the darknet code

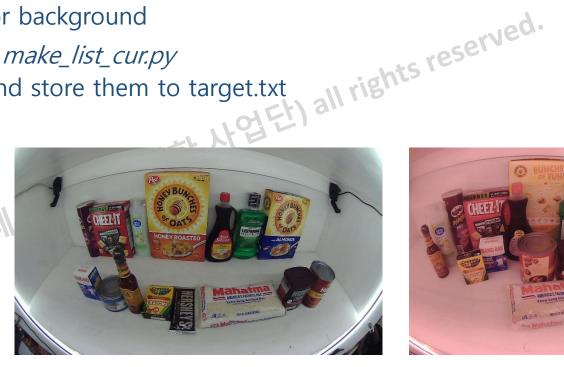




# Test images (skeleton/bin/dataset)

- 150 test images in three categories
  - Long: distances among objects are far
  - Close: items are close
  - Color: apply different colors for background
- Execute a UNIX command: *python make\_list\_cur.py* 
  - Get directories of all images and store them to target.txt







close color long

# Test images (skeleton/bin/dataset)

- Ground truth: \*.txt files
  - Each line represents a labelled object
    - [class\_index x\_pos y\_pos width height]
- yolohw.names: names of all 60 classes of products

```
bts reserved.
aunt jemima original syrup
band aid clear strips
bumblebee albacore
cholula chipotle hot sauce
crayola 24 crayons
hersheys cocoa
honey bunches of oats honey roasted
honey bunches of oats with almonds
hunts sauce
listerine green
mahatma rice
white rain body wash
pringles_bbq
cheeze it
hersheys bar
redbull
```

CAPP\_testset\_close\_10000.txt

yolohw.names

### Pretrained model (skeleton/bin)

- A pretrained model is defined by two files
  - yolov3-tiny-aix2022.cfg: Network's configuration
    - Input size
    - Training/testing options
    - Layer's settings

```
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```

```
[net]
batch=1
subdivisions=1
#batch=64
#subdivisions=2
width=320
height=320
momentum=0.9
decay=0.0005
angle=0
saturation = 1.5
exposure = 1.5
hue=.1
learning rate=0.001
burn in=1000
max batches = 50200
policy=steps
steps=40000,45000
scales=.1..1
[convolutional]
batch normalize=1
filters=16
size=3
stride=1
pad=1
activation=leaky
[maxpool]
```

### Source files

```
    additionally.c // Definitions of darknet functions used

                                                       -Hot Halet) all rights reserved.
• additionally.h // Declaration of darknet functions + additional functions for forward pass of yolo model
              // For bounding boxes
                rite.h

// For loading/writing images
stb_image_write.h
• stb_image.h
• yolov2_forward_network.c // Functions for forward pass of yolo network
  yolov2_forward_network_quantized.c // Functions for quantization, saving of the quantized model, and the
  forward pass of quantized yolo model

    main.c // The main functions

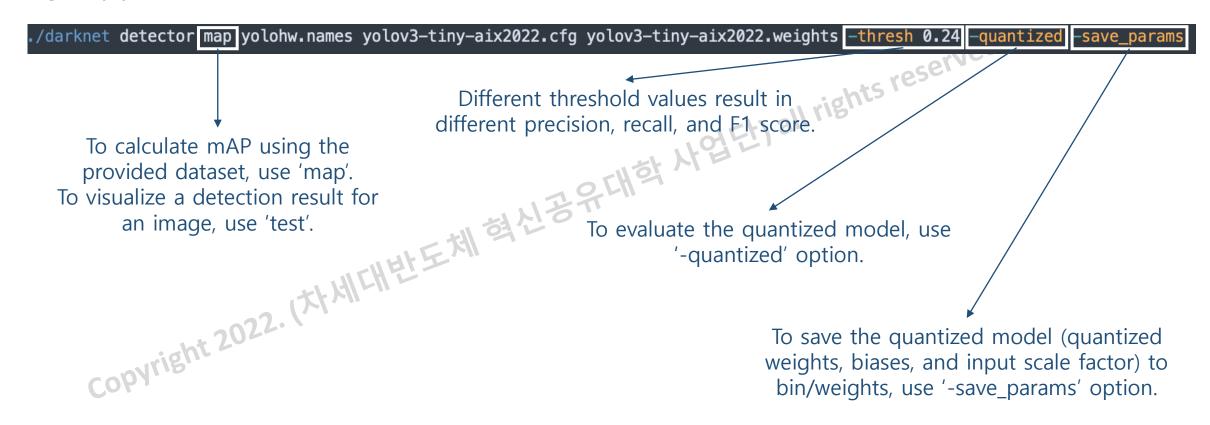
                                            You should mainly edit this file for quantization!
```

# How to Compile & Run?

- Use the provided Makefile to compile
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# How to Compile & Run?

e.g.) tiny-yolo-aix2022-int8.sh



# Incoming tutorials ...

- Tutorial 2: Network
  - Network architecture
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