

UIC

BANK 33

IO_0_VRN_33	I9		PL_DDR_DM_B032
IO_L1P_T0_33	G4		PL_DDR_D033
IO_L1N_T0_33	F4		PL_DDR_D034
IO_L2P_T0_33	D4		PL_DDR_D035
IO_L2N_T0_33	D3		PL_DDR_D035
IO_L3P_T0_DQS_33	G2		PL_DDR_DQS_Byt4_N
IO_L3N_T0_DQS_33	F2		PL_DDR_DQS_Byt4_P
IO_L4P_T0_33	D1		PL_DDR_D036
IO_L4N_T0_33	C1		PL_DDR_D037
IO_L5P_T0_33	E2		PL_DDR_D038
IO_L5N_T0_33	E1		PL_DDR_D039
IO_L6P_T0_33	F3		PL_DDR_DM_Byt4
IO_L6N_T0_VREF_33	E3	X	
IO_L7P_T1_33	H1		PL_DDR_D040
IO_L7N_T1_33	H4		PL_DDR_D041
IO_L8P_T1_33	H3		PL_DDR_D042
IO_L8N_T1_33	K2		PL_DDR_D043
IO_L9P_T1_DQS_33	K1		PL_DDR_DQS_Byt5_N
IO_L9N_T1_DQS_33	H2		PL_DDR_DQS_Byt5_P
IO_L10P_T1_33	G1		PL_DDR_D044
IO_L10N_T1_33	L3		PL_DDR_D045
IO_L11P_T1_SRCC_33	K3		PL_DDR_D046
IO_L11N_T1_SRCC_33	J4		PL_DDR_D047
IO_L12P_T1_MRCC_33	J3		SYS_CLK_P
IO_L12N_T1_MRCC_33	J4		SYS_CLK_N
IO_L13P_T2_MRCC_33	M6		PL_DDR_DM_Byt5
IO_L13N_T2_MRCC_33	M5		PL_DDR_DM_Byt6
IO_L14P_T2_SRCC_33	J5		PL_DDR_DM_Byt8
IO_L14N_T2_SRCC_33	J4		PL_DDR_DM_Byt9
IO_L15P_T2_SRCC_33	N3		PL_DDR_DQS_Byt6_N
IO_L15N_T2_DQS_33	N2		PL_DDR_DQS_Byt6_P
IO_L16P_T2_33	M2		PL_DDR_D050
IO_L16N_T2_33	J2		PL_DDR_D051
IO_L17P_T2_33	K4		PL_DDR_D052
IO_L17N_T2_33	M4		PL_DDR_D053
IO_L18P_T2_33	Z1		PL_DDR_D054
IO_L18N_T2_33	M1		PL_DDR_D055
IO_L19P_T2_33	M7		PL_DDR_DM_Byt7
IO_L19N_T3_VREF_33	L7	X	
IO_L20P_T3_33	J5		PL_DDR_D056
IO_L20N_T3_33	B4		PL_DDR_D057
IO_L21P_T3_DQS_33	M8		PL_DDR_DQS_Byt7_N
IO_L21N_T3_DQS_33	L8		PL_DDR_DQS_Byt7_P
IO_L22P_T3_33	K6		PL_DDR_D058
IO_L22N_T3_33	J6		PL_DDR_D059
IO_L23P_T3_33	N7		PL_DDR_D060
IO_L23N_T3_33	N6		PL_DDR_D061
IO_L24P_T3_33	K8		PL_DDR_D062
IO_L24N_T3_33	K7		PL_DDR_DQ63
IO_25_VRP_33	N8		

XC7Z035-2FFG676I

PL_DDR_Data_Byt0

PL_DDR_Data_Byt3

PL_DDR_Data_Byt6

PL_DDR_Data_Byt9

PL_DDR_Data_Byt12

PL_DDR_Data_Byt15

PL_DDR_Data_Byt18

PL_DDR_Data_Byt21

PL_DDR_Data_Byt24

PL_DDR_Data_Byt27

PL_DDR_Data_Byt30

PL_DDR_Data_Byt33

PL_DDR_Data_Byt36

PL_DDR_Data_Byt39

PL_DDR_Data_Byt42

PL_DDR_Data_Byt45

PL_DDR_Data_Byt48

PL_DDR_Data_Byt51

PL_DDR_Data_Byt54

PL_DDR_Data_Byt57

PL_DDR_Data_Byt60

PL_DDR_Data_Byt63

PL_DDR_Data_Byt66

PL_DDR_Data_Byt69

PL_DDR_Data_Byt72

PL_DDR_Data_Byt75

PL_DDR_Data_Byt78

PL_DDR_Data_Byt81

PL_DDR_Data_Byt84

PL_DDR_Data_Byt87

PL_DDR_Data_Byt90

PL_DDR_Data_Byt93

PL_DDR_Data_Byt96

PL_DDR_Data_Byt99

PL_DDR_Data_Byt102

PL_DDR_Data_Byt105

PL_DDR_Data_Byt108

PL_DDR_Data_Byt111

PL_DDR_Data_Byt114

PL_DDR_Data_Byt117

PL_DDR_Data_Byt120

PL_DDR_Data_Byt123

PL_DDR_Data_Byt126

PL_DDR_Data_Byt129

PL_DDR_Data_Byt132

PL_DDR_Data_Byt135

PL_DDR_Data_Byt138

PL_DDR_Data_Byt141

PL_DDR_Data_Byt144

PL_DDR_Data_Byt147

PL_DDR_Data_Byt150

PL_DDR_Data_Byt153

PL_DDR_Data_Byt156

PL_DDR_Data_Byt159

PL_DDR_Data_Byt162

PL_DDR_Data_Byt165

PL_DDR_Data_Byt168

PL_DDR_Data_Byt171

PL_DDR_Data_Byt174

PL_DDR_Data_Byt177

PL_DDR_Data_Byt180

PL_DDR_Data_Byt183

PL_DDR_Data_Byt186

PL_DDR_Data_Byt189

PL_DDR_Data_Byt192

PL_DDR_Data_Byt195

PL_DDR_Data_Byt198

PL_DDR_Data_Byt201

PL_DDR_Data_Byt204

PL_DDR_Data_Byt207

PL_DDR_Data_Byt210

PL_DDR_Data_Byt213

PL_DDR_Data_Byt216

PL_DDR_Data_Byt219

PL_DDR_Data_Byt222

PL_DDR_Data_Byt225

PL_DDR_Data_Byt228

PL_DDR_Data_Byt231

PL_DDR_Data_Byt234

PL_DDR_Data_Byt237

PL_DDR_Data_Byt240

PL_DDR_Data_Byt243

PL_DDR_Data_Byt246

PL_DDR_Data_Byt249

PL_DDR_Data_Byt252

PL_DDR_Data_Byt255

PL_DDR_Data_Byt258

PL_DDR_Data_Byt261

PL_DDR_Data_Byt264

PL_DDR_Data_Byt267

PL_DDR_Data_Byt270

PL_DDR_Data_Byt273

PL_DDR_Data_Byt276

PL_DDR_Data_Byt279

PL_DDR_Data_Byt282

PL_DDR_Data_Byt285

PL_DDR_Data_Byt288

PL_DDR_Data_Byt291

PL_DDR_Data_Byt294

PL_DDR_Data_Byt297

PL_DDR_Data_Byt200

PL_DDR_Data_Byt203

PL_DDR_Data_Byt206

PL_DDR_Data_Byt209

PL_DDR_Data_Byt212

PL_DDR_Data_Byt215

PL_DDR_Data_Byt218

PL_DDR_Data_Byt221

PL_DDR_Data_Byt224

PL_DDR_Data_Byt227

PL_DDR_Data_Byt230

PL_DDR_Data_Byt231

PL_DDR_Data_Byt232

PL_DDR_Data_Byt233

PL_DDR_Data_Byt234

PL_DDR_Data_Byt235

PL_DDR_Data_Byt236

PL_DDR_Data_Byt237

PL_DDR_Data_Byt238

PL_DDR_Data_Byt239

PL_DDR_Data_Byt240

PL_DDR_Data_Byt241

PL_DDR_Data_Byt242

PL_DDR_Data_Byt243

PL_DDR_Data_Byt244

PL_DDR_Data_Byt245

PL_DDR_Data_Byt246

PL_DDR_Data_Byt247

PL_DDR_Data_Byt248

PL_DDR_Data_Byt249

PL_DDR_Data_Byt250

PL_DDR_Data_Byt251

PL_DDR_Data_Byt252

PL_DDR_Data_Byt253

PL_DDR_Data_Byt254

PL_DDR_Data_Byt255

PL_DDR_Data_Byt256

PL_DDR_Data_Byt257

PL_DDR_Data_Byt258

PL_DDR_Data_Byt259

PL_DDR_Data_Byt260

PL_DDR_Data_Byt261

PL_DDR_Data_Byt262

PL_DDR_Data_Byt263

PL_DDR_Data_Byt264

PL_DDR_Data_Byt265

PL_DDR_Data_Byt266

PL_DDR_Data_Byt267

PL_DDR_Data_Byt268

PL_DDR_Data_Byt269

PL_DDR_Data_Byt270

PL_DDR_Data_Byt271

PL_DDR_Data_Byt272

PL_DDR_Data_Byt273

PL_DDR_Data_Byt274

PL_DDR_Data_Byt275

PL_DDR_Data_Byt276

PL_DDR_Data_Byt277

PL_DDR_Data_Byt278

PL_DDR_Data_Byt279

PL_DDR_Data_Byt280

PL_DDR_Data_Byt281

PL_DDR_Data_Byt282

PL_DDR_Data_Byt283

PL_DDR_Data_Byt284

PL_DDR_Data_Byt285

PL_DDR_Data_Byt286

PL_DDR_Data_Byt287

PL_DDR_Data_Byt288

PL_DDR_Data_Byt289

PL_DDR_Data_Byt290

PL_DDR_Data_Byt291

PL_DDR_Data_Byt292

PL_DDR_Data_Byt293

PL_DDR_Data_Byt294

PL_DDR_Data_Byt295

PL_DDR_Data_Byt296

PL_DDR_Data_Byt297

PL_DDR_Data_Byt298

PL_DDR_Data_Byt299

PL_DDR_Data_Byt300

PL_DDR_Data_Byt301

PL_DDR_Data_Byt302

PL_DDR_Data_Byt303

PL_DDR_Data_Byt304

PL_DDR_Data_Byt305

PL_DDR_Data_Byt306

PL_DDR_Data_Byt307

PL_DDR_Data_Byt308

PL_DDR_Data_Byt309

PL_DDR_Data_Byt310

PL_DDR_Data_Byt311

PL_DDR_Data_Byt312

PL_DDR_Data_Byt313

PL_DDR_Data_Byt314

PL_DDR_Data_Byt315

PL_DDR_Data_Byt316

PL_DDR_Data_Byt317

PL_DDR_Data_Byt318

PL_DDR_Data_Byt319

PL_DDR_Data_Byt320

PL_DDR_Data_Byt321

PL_DDR_Data_Byt322

PL_DDR_Data_Byt323

PL_DDR_Data_Byt324

PL_DDR_Data_Byt325

PL_DDR_Data_Byt326

PL_DDR_Data_Byt327

PL_DDR_Data_Byt328

PL_DDR_Data_Byt329

PL_DDR_Data_Byt330

PL_DDR_Data_Byt331

PL_DDR_Data_Byt332

PL_DDR_Data_Byt333

PL_DDR_Data_Byt334

PL_DDR_Data_Byt335

PL_DDR_Data_Byt336

PL_DDR_Data_Byt337

PL_DDR_Data_Byt338

PL_DDR_Data_Byt339

PL_DDR_Data_Byt340

PL_DDR_Data_Byt341

PL_DDR_Data_Byt342

PL_DDR_Data_Byt343

PL

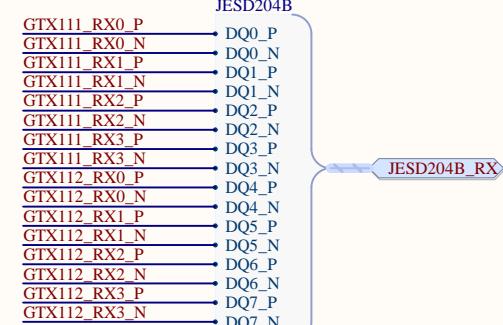
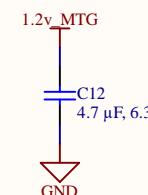
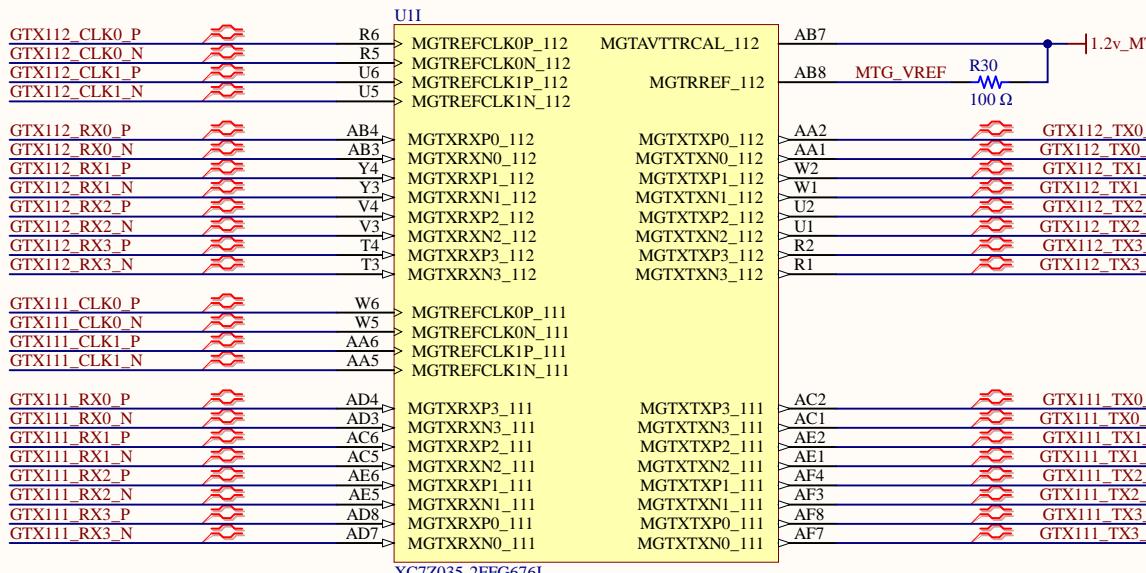
1

2

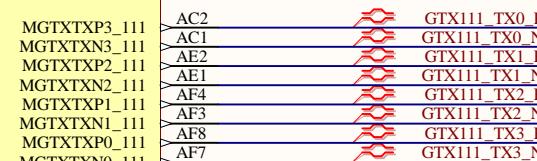
3

4

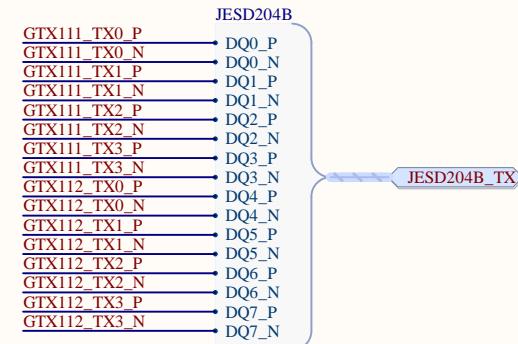
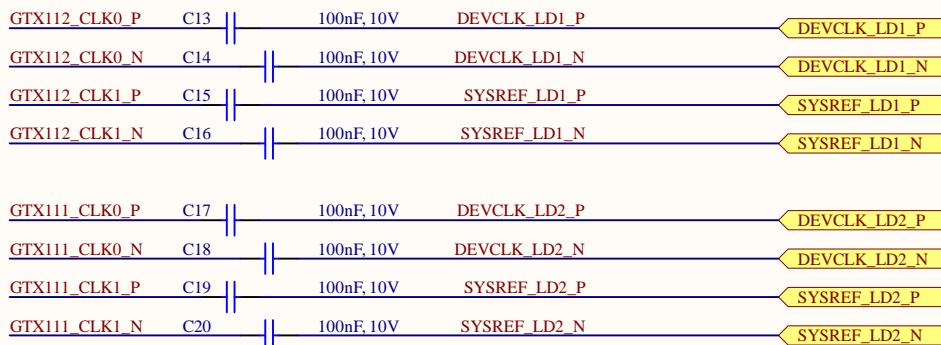
A



B



C



D

Project:	SpaceVNXBaseBoard.PjPcb	
Title:	ZynqPL_GTXE2	
Designer:	Carlos Barreiro	
Date:	21/05/2022	

Revision:A

Sheet 7 of 16

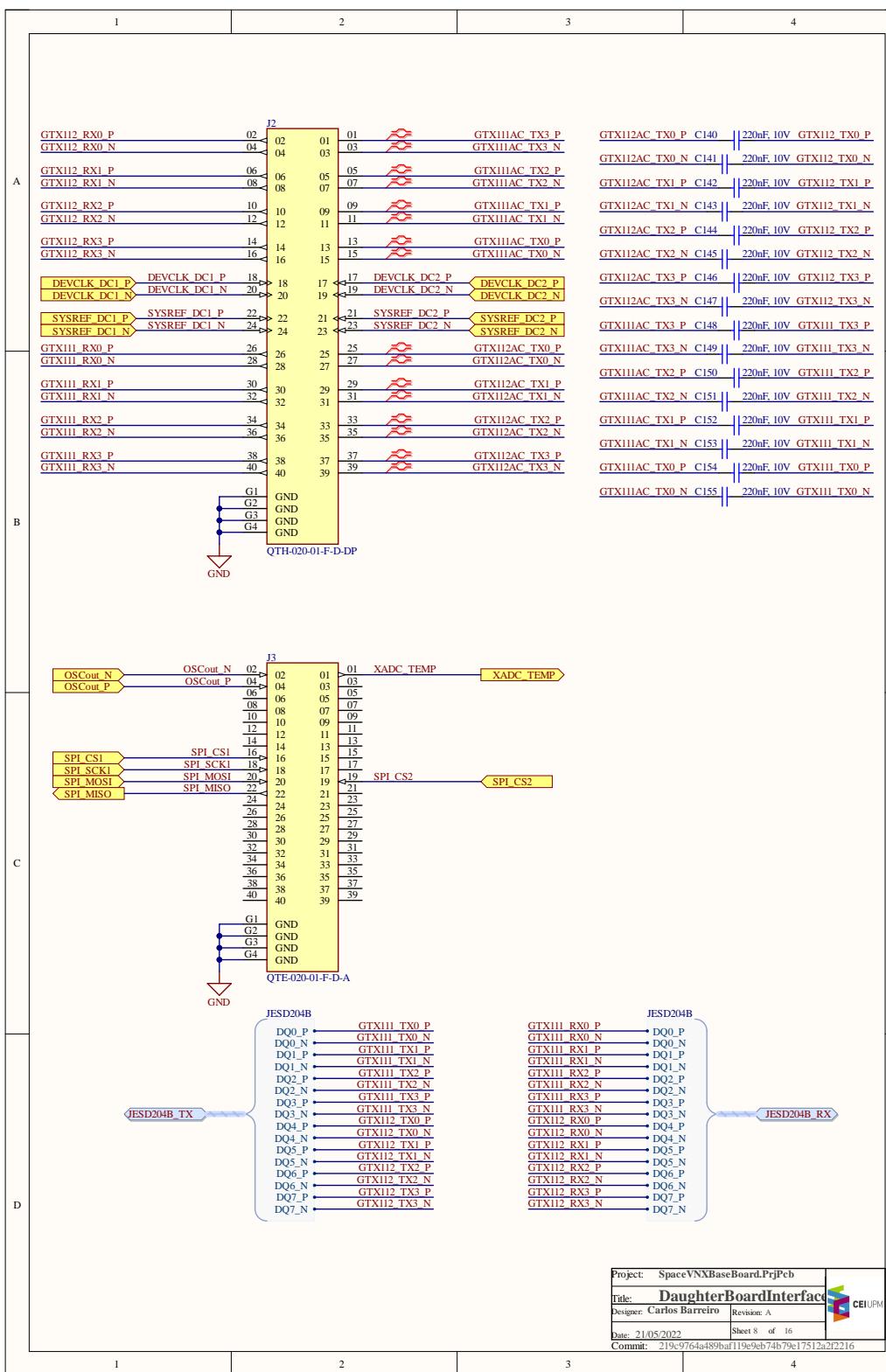
Commit: 219c9764a489baf119e9eb74b79e17512a2f2216

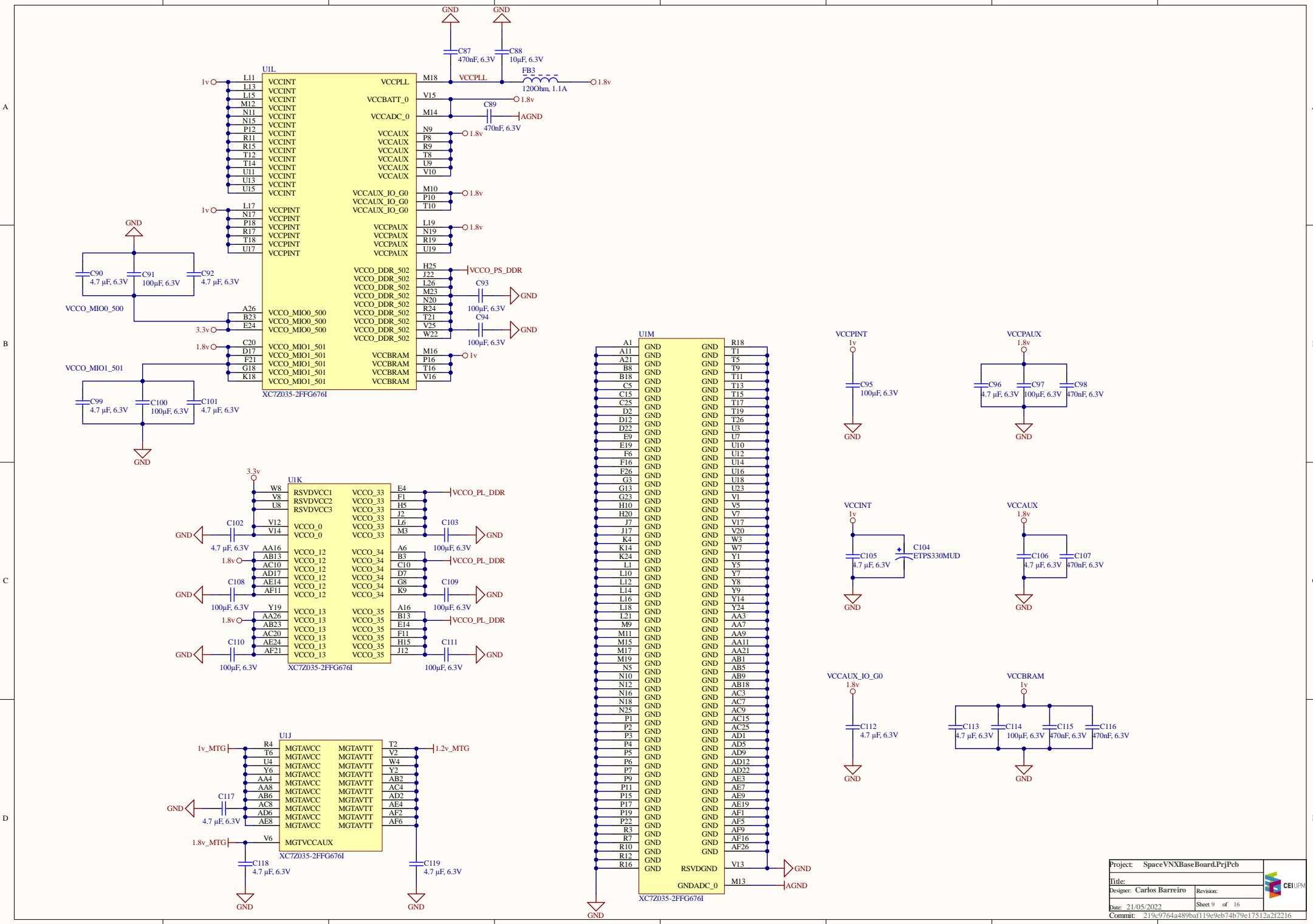
1

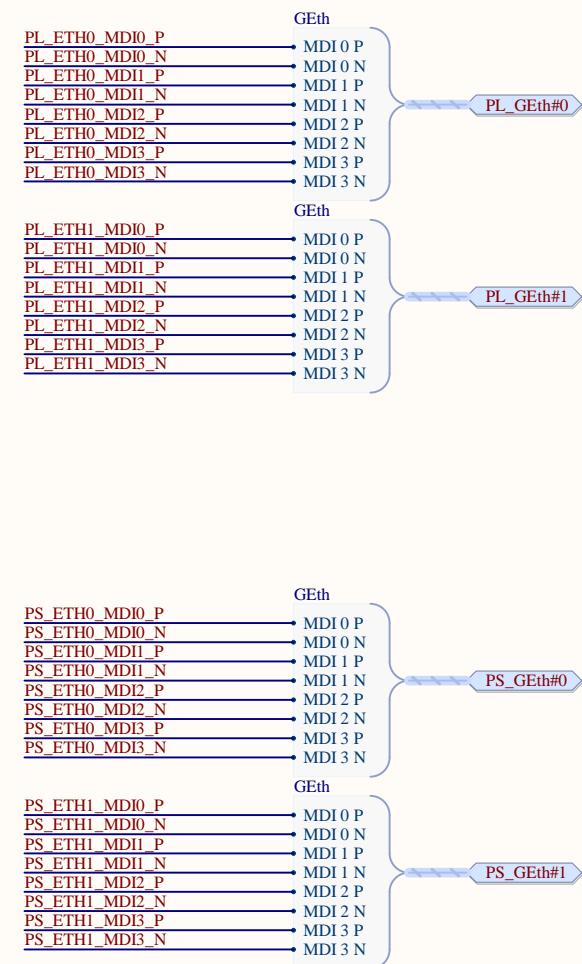
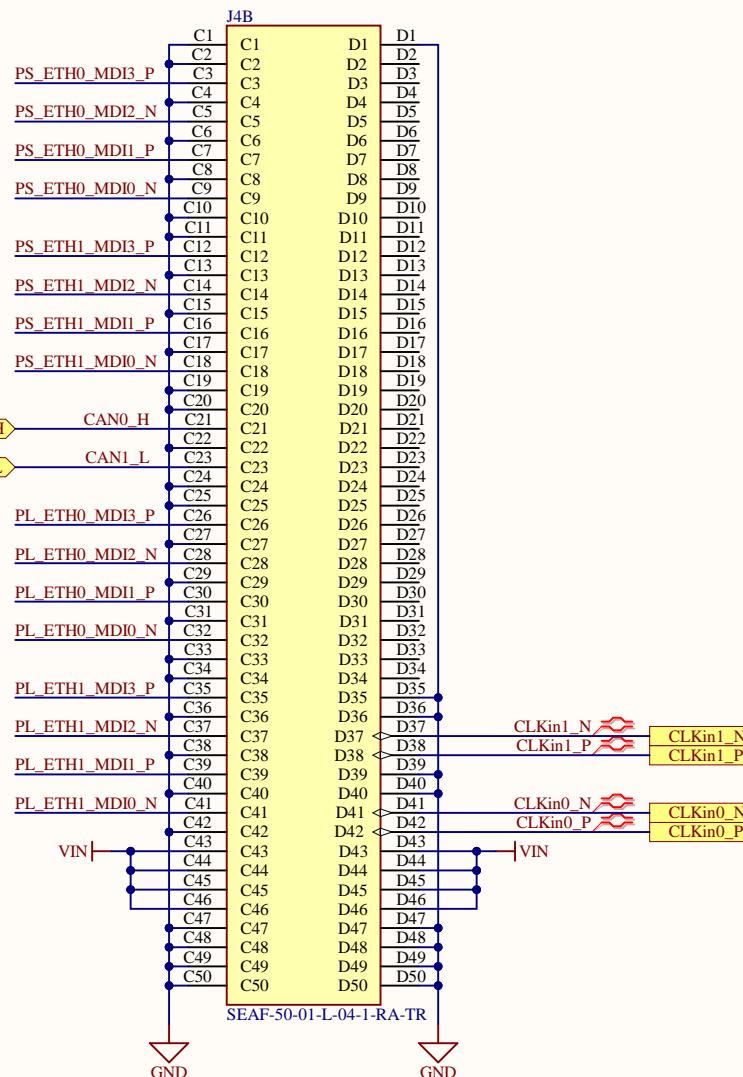
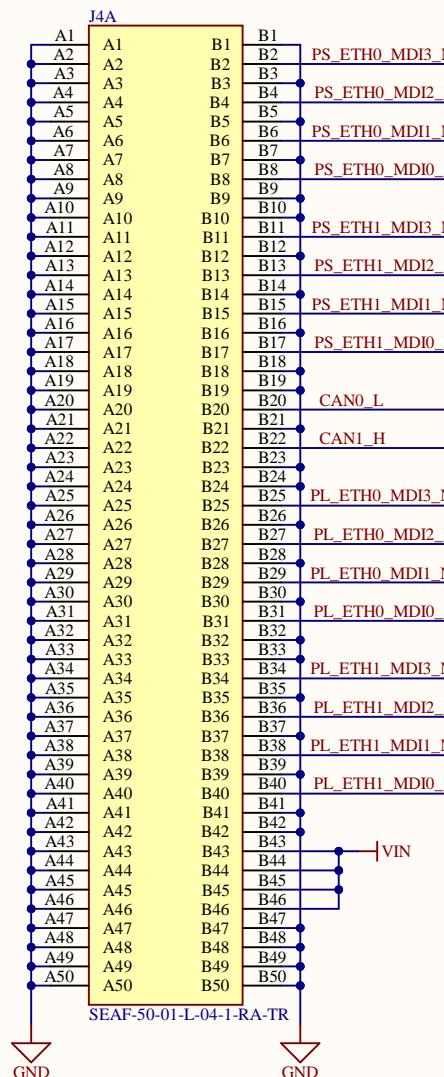
2

3

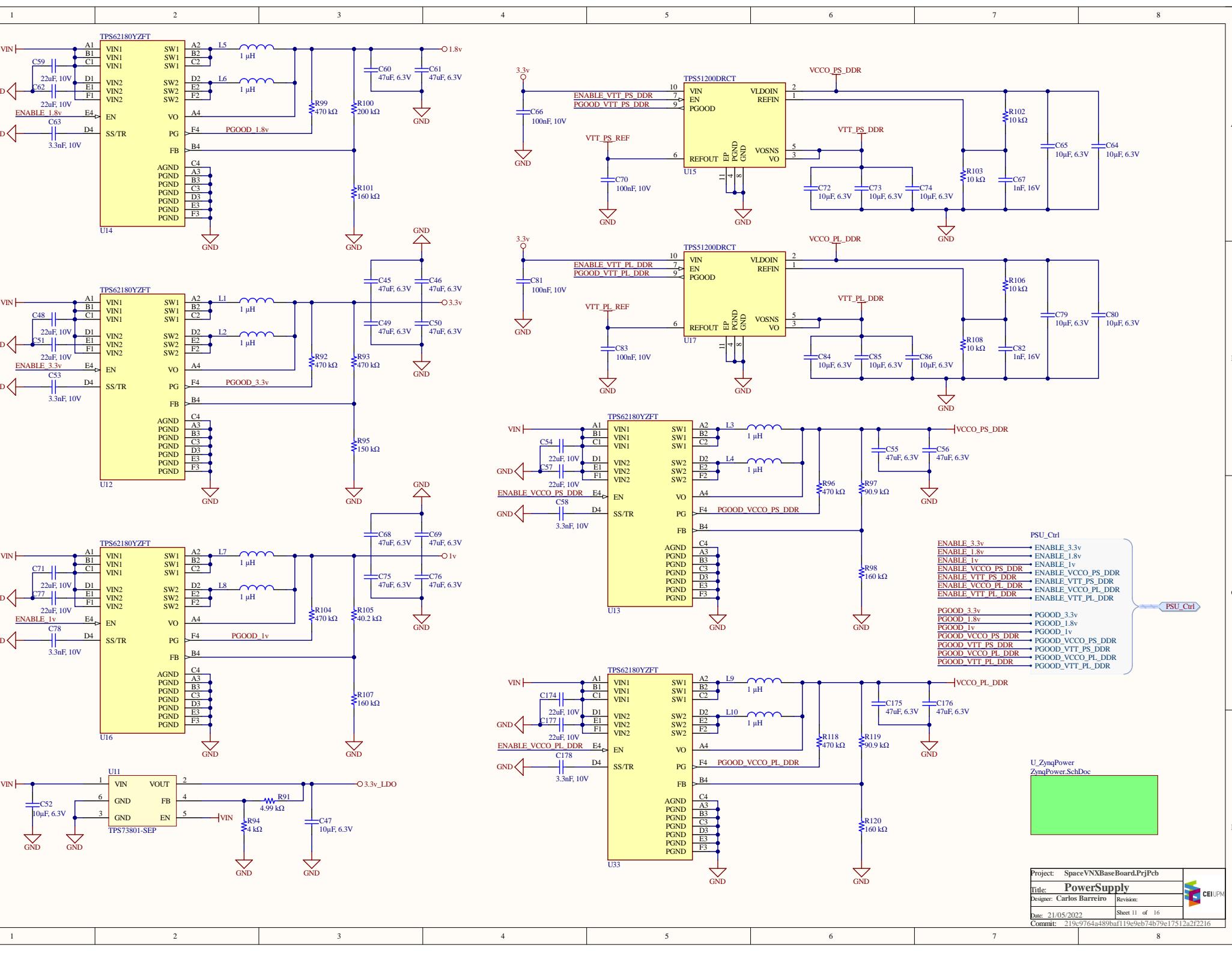
4







Project:	SpaceVNXBaseBoard.PrvPcb
Title:	SpaceVNXInterface
Designer:	Carlos Barreiro
Revision:	A
Date:	21/05/2022
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Commit:	219c9764a489baf119e9eb74b79e17512a2f2216



1 2 3 4

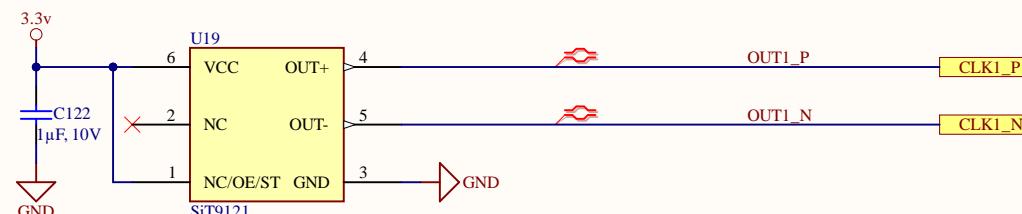
A



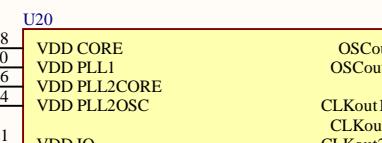
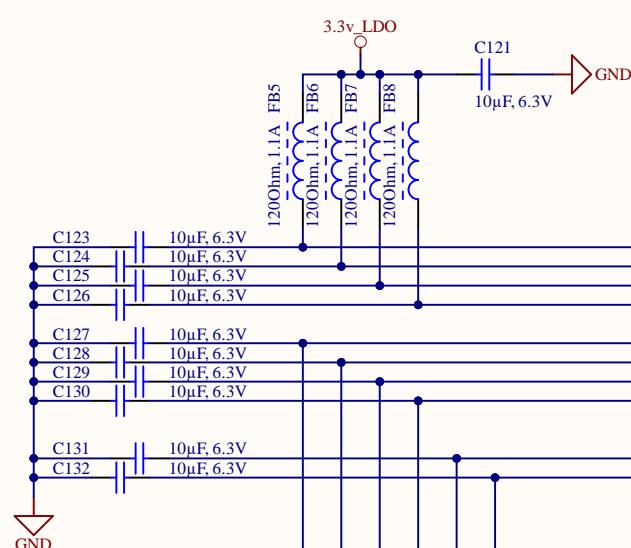
3

4

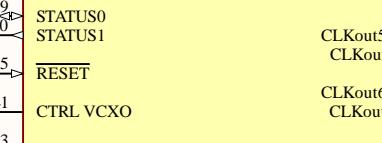
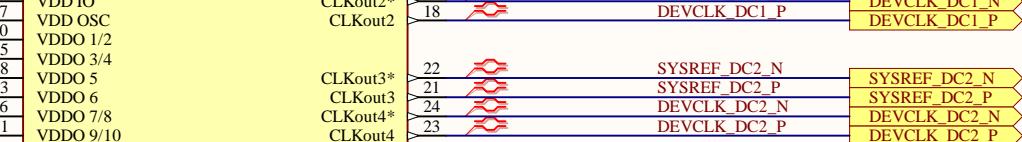
A



B

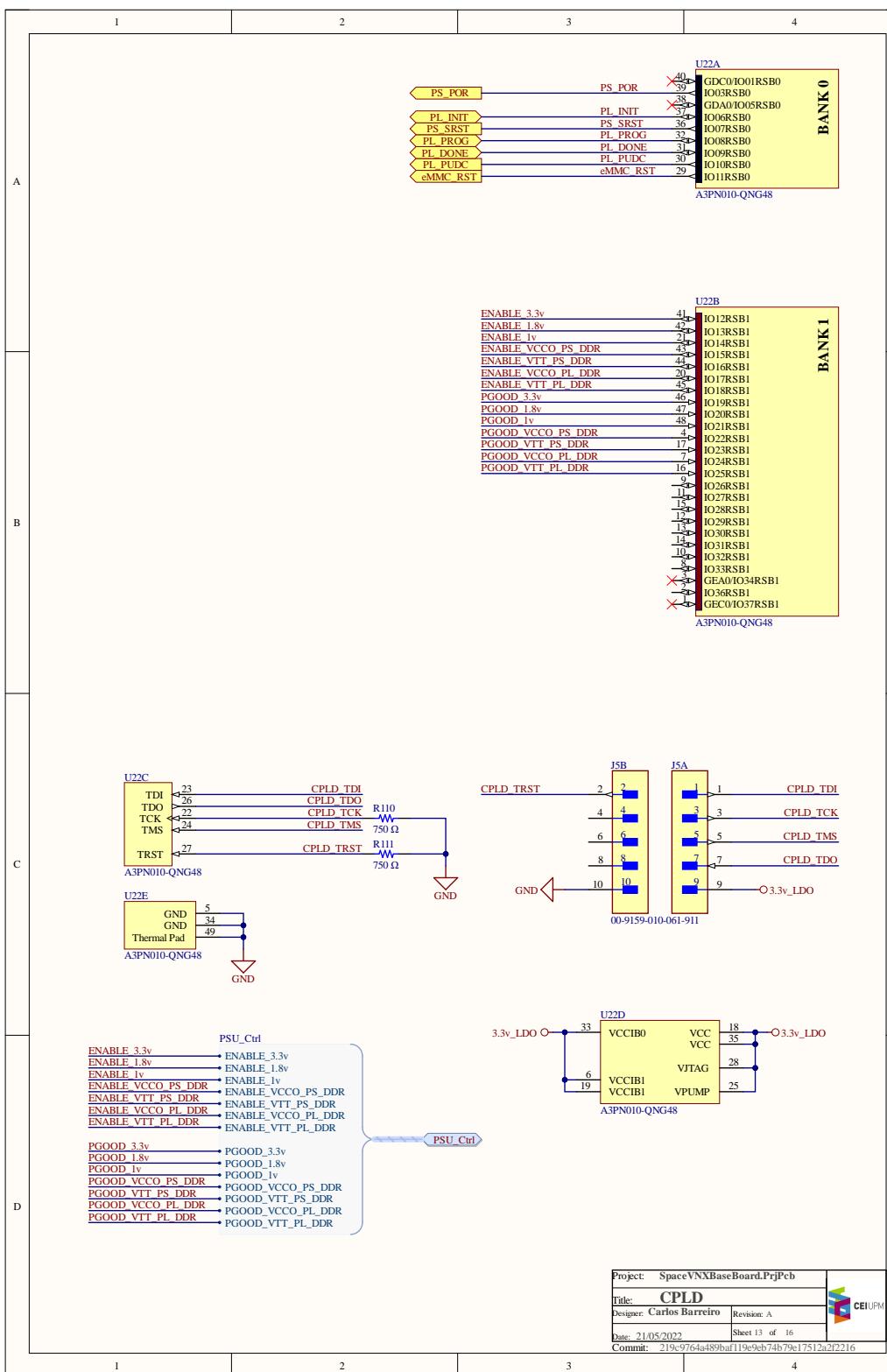


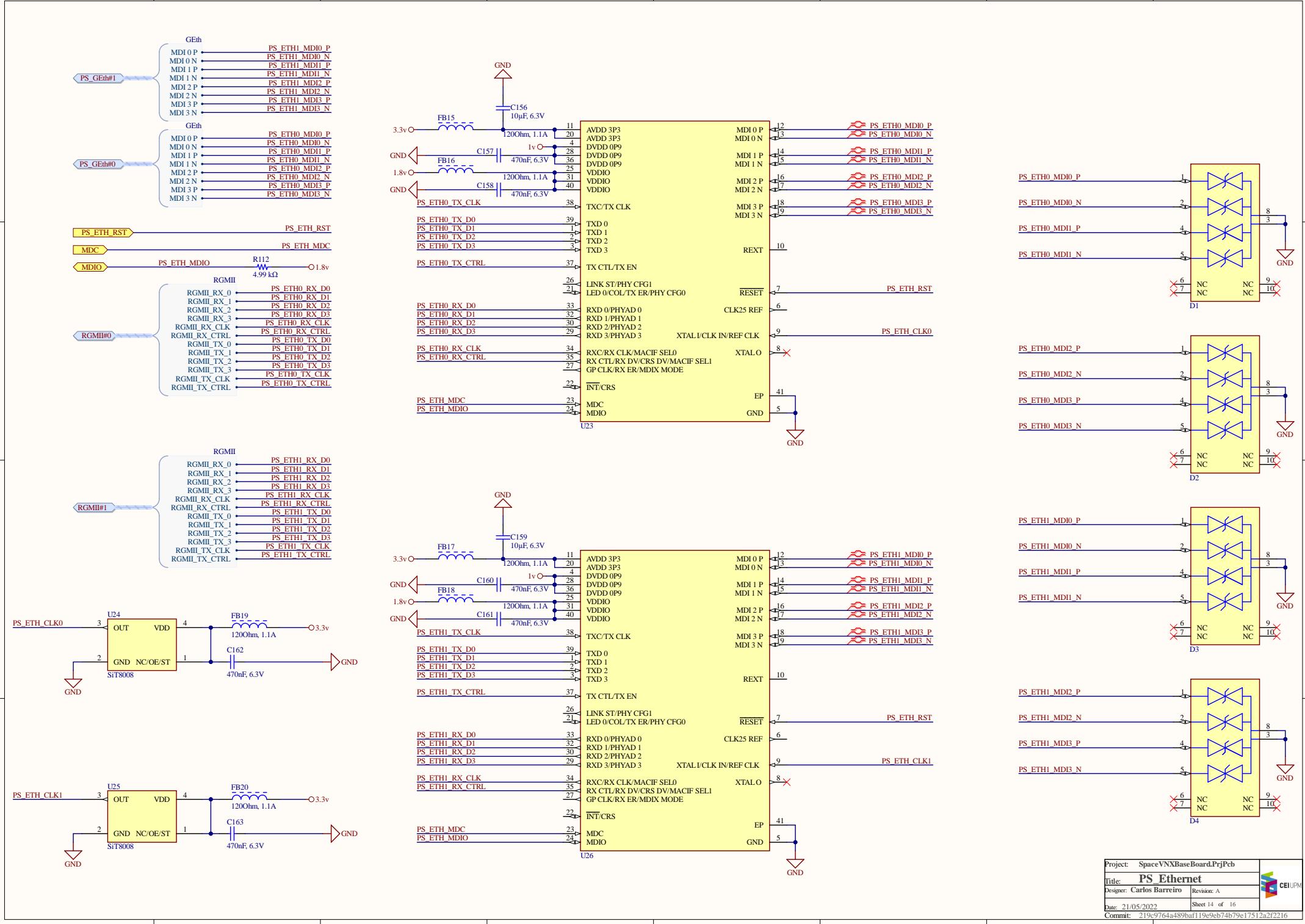
B

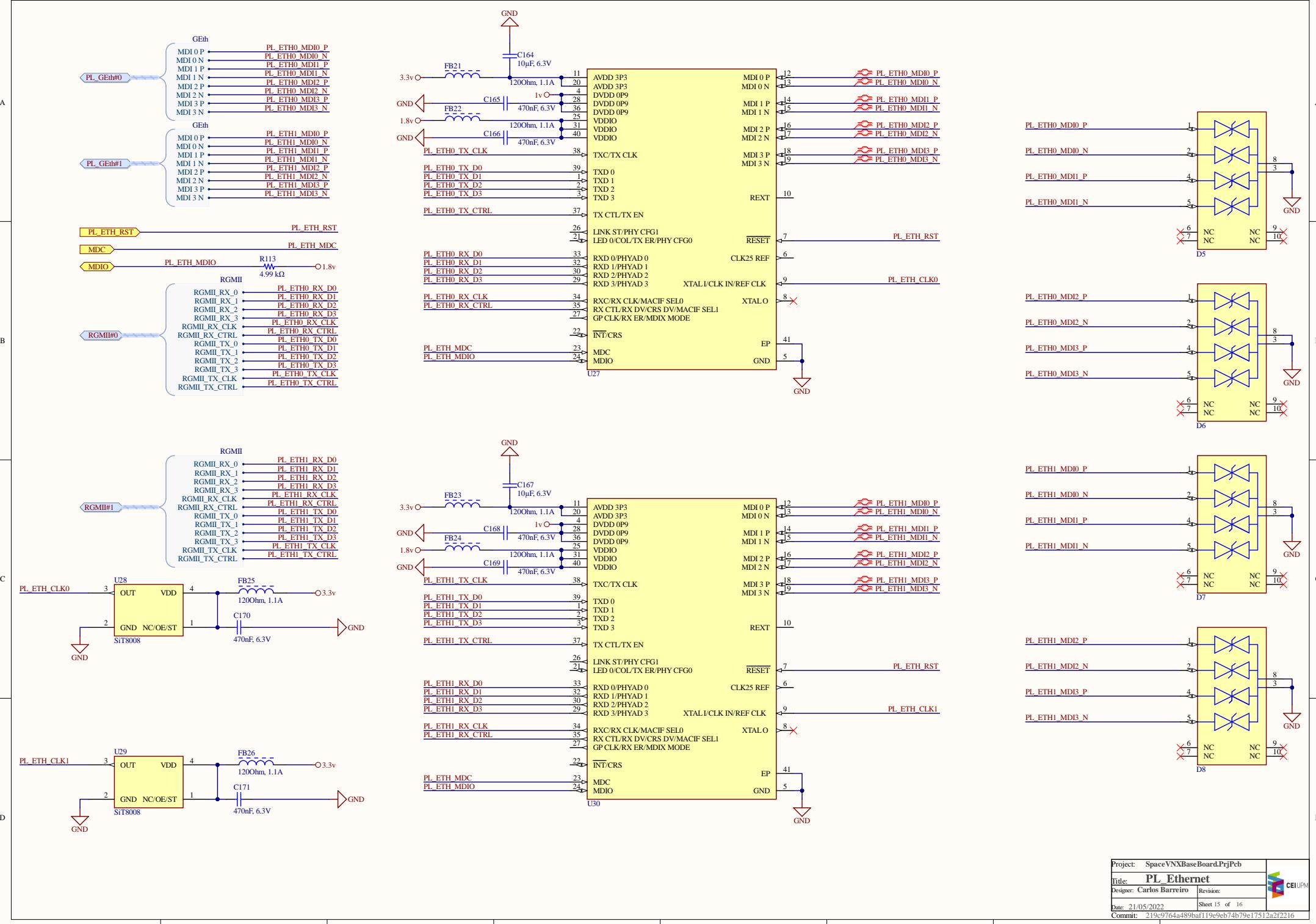


Project: SpaceVNXBaseBoard.PrbPcb	
Title: Clocks	
Designer: Carlos Barreiro	Revision:A
Date: 21/05/2022	Sheet 12 of 16
Commit: 219c9764a489ba119e9eb74b79e17512a2f2216	CEIUPM

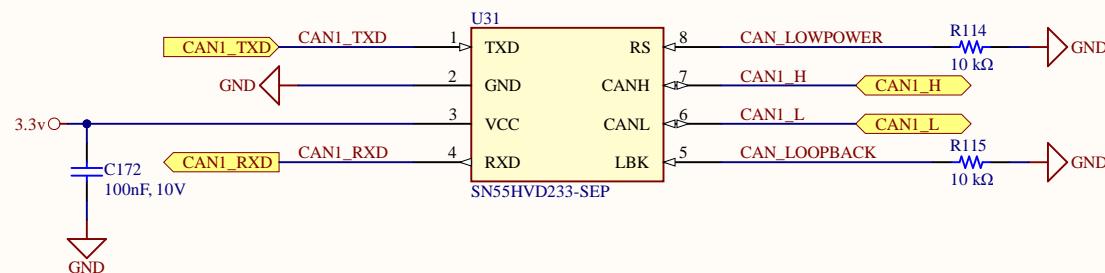
1 2 3 4



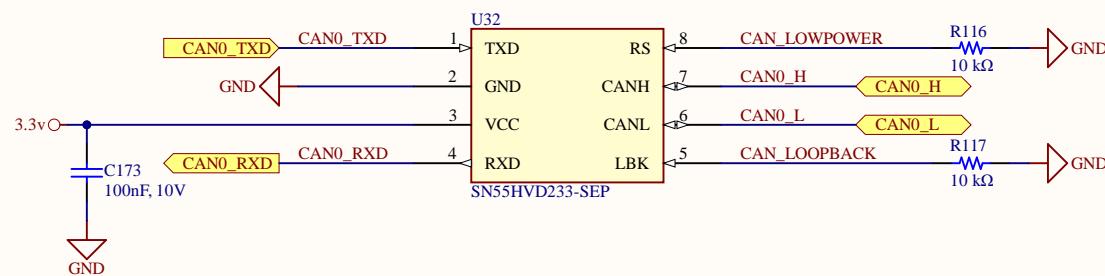




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CAN_LOWPOWER → CAN_LOWPOWER
CAN_LOOPBACK → CAN_LOOPBACK

D

Project:	SpaceVNXBaseBoard.PnjPcb	
Title:	CANTransceiver	
Designer:	Carlos Barreiro	
Date:	21/05/2022	

Revision:A
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Commit: 219c9764a489baf119e9eb74b79e17512a2f2216

