Wafer Production Line - Optimization

# TPK4186 - Advanced Tools for Performance Engineering Assingment 3: Wafer Production Line

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# 2.3 Optimization

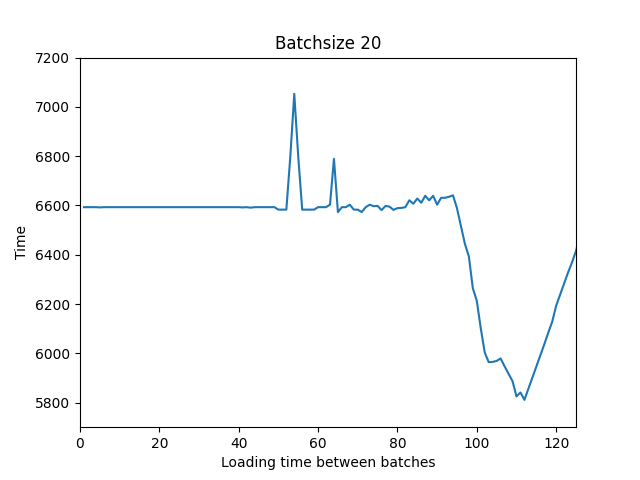
# Task 5.

## Simulation with Worst Case

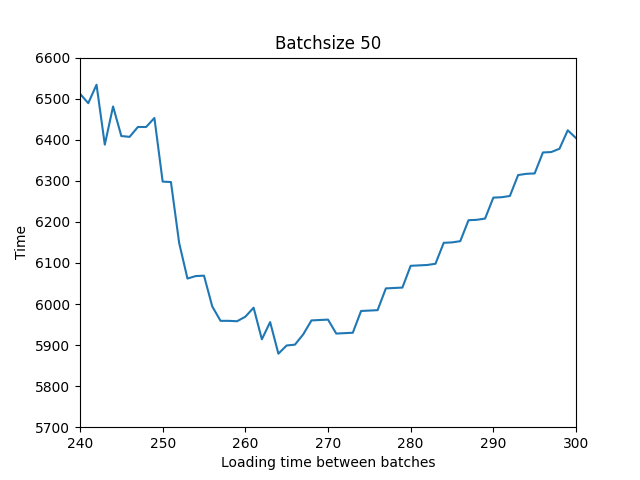
This is an example of runtime with the worst case solution. One batch is loaded into the simulation, and the next one is not loaded until the first one is finished. This is repeated until all 1000 wafers is produced. The batch sizes is random from 20 to 50 wafers per batch, and therefore some variation in time between each run. The total runtime is: 13258.0 minutes for this simulation.

## Simulation with reduced loadtime between batches

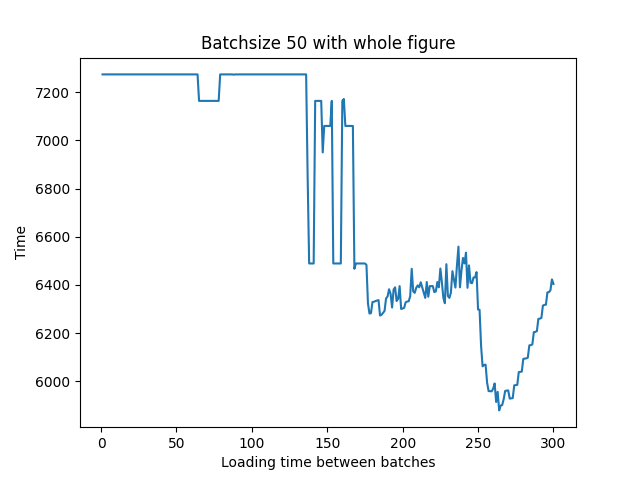
Now we will try to reduce the loading time between the batches. We will simulate 3 simulations, with batch sizes of 20, 50 and random batch size. The graphs below will show the loading times, and the finish time for all 1000 Wafers. The loading time between each batch is increased with a constant number, and the loading time between each batch is the same as the number of simulations. The loading time between each batch is increased from 1 to 300 minutes.



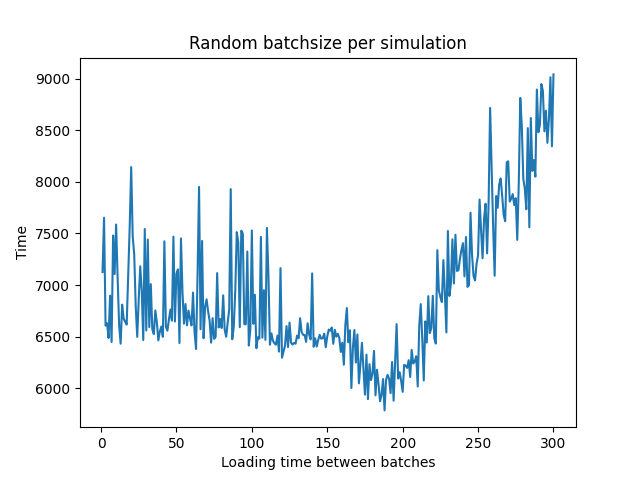
Batchsize 20: The optimal solution is with total time: 5811.0 and loading time between batches 112 minutes.



Batchsize 50: The optimal solution is with total time: 5879.0 and loading time between batches 264 minutes.



Observe that the optimal loading time between each task from batchsize 20 and batchsize 50 is proportional to the batchsize.



Batchsize 20: The optimal solution is with total time: 5784.0 and loading time between batches 188 minutes. NOTE: With random batchsize, the optimal solution is likely to change from run to run.

# Task 6.

## Simulation with changed ordering heuristic

The previous results show the optimal time with 20, 50 and random batch sizes with standard task prioritization, with Unit 1 : [1, 3, 6, 9], Unit 2 : [2, 5, 7] and Unit 3 : [4, 8]. Now we will change the order tasks are prioritized. It is 288 possible permutations of the tasks, and we will simulate the 3 same simulations. That means 300 simulations, with different load time, times 288 permutations. The total number of simulations is 86 400 per batch size. The results are shown below.

The best solution for batchsize 20 is with total time: 5721.0 and loading time between batches 71 minutes. and the best ordering heuristic is: Unit 1 : [1, 3, 6, 9] Unit 2: [5, 7, 2] Unit 3: [8, 4]

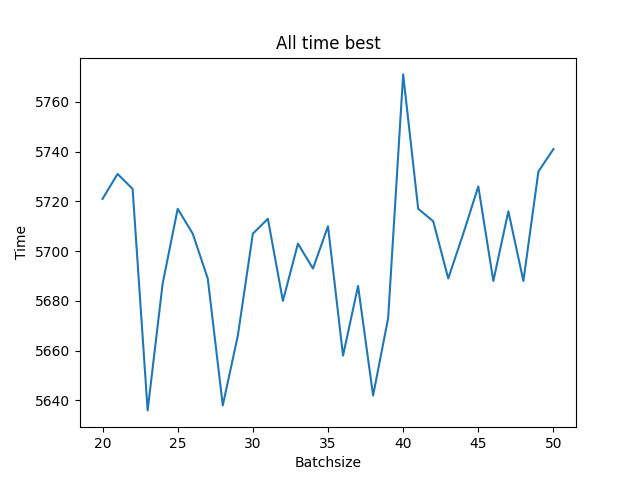
# Task 7.

## Simulation with different loading time, ordering heuristic and batchsizes

Now we will simulate the same as in task 6, but with different batch sizes for each simulation. That means that we will run 288 permutations \* (50-20) batch sizes \* 300 loading times = 2 592 000 simulations! We will only show the best times for each batch size, but the best loading time and ordering heuristic for the best time overall with all optimization. This is probably an overkill solution, but will give us the best results and overview into the problem at hand.

The all time best solution is with total time: 5636.0 and loading time between batches 81 minutes. The optimal batch size is then 23 wafers per batch

The figure below shows the optimal time for each batch size, with the most optimal loading time and ordering heuristic. NOTE: The optimal solution is likely to change from run to run since the simulations is run with random batchsizes each time.



Execution time for the whole optimization process: 1441.49 seconds, ran with a good computer