# 基于有限状态机的自动售票系统设计

# 一、输入输出端口信号分析

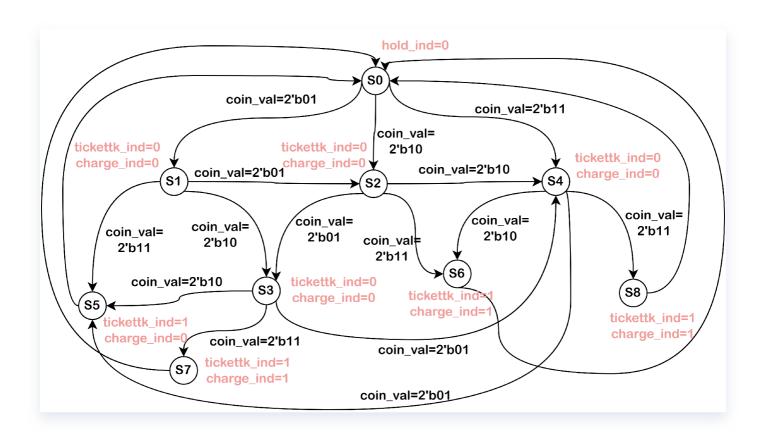
- 输入信号: clk, rst;
- 输入信号: 操作开始: op\_start; // op\_start = 1 开始操作
- 输入信号: 投币币值: coin\_val; // 2'b00 表示 0 元, 2'b01 表示 5 元, 2'b10 表示 10 元, 2'b11 表示 20 元;
- 输入信号:取消操作指示: cancel\_flag; // cancel\_flag = 1 表示取消操作
- 输出信号: 机器是否可用: hold\_ind; // hold\_ind = 0表示可以使用
- 输出信号: 取票信号: tickettk\_ind; // tickettk\_ind = 1 表示取票
- 输出信号: 找零与退币标志: charge\_ind; // charge\_ind = 1 表示找零
- 输出信号: 找零与退币币值: charge\_val; // 3'b000 表示找 5 元, 3'b001 表示找 10 元, 3'b010 表示找 15 元, 3'b011 表示找 20 元, 3'b100 表示找 25 元, 3'b101 表示找 30 元, 3'b110 表示找 35 元, 3'b111 表示找 40 元;

## 二、系统状态分析

### 2.1 状态说明

- S0: 初始状态;
- S1: 已投币 5 元;
- S2: 已投币 10 元;
- S3: 已投币 15 元;
- S4: 已投币 20 元;
- S5: 已投币 25 元;
- S6: 已投币 30 元;
- S7: 已投币 35 元;
- S8: 已投币 40 元;

#### 2.2 状态转移图



### 2.3 状态转移关系

- 1. 始态 S0 (coin=0元), 根据状态转移条件 coin\_val 分别转移到 S1(coin=5元)/S2(coin=10元)/S3(coin=15元);
- 2. 状态 S1(coin=5元), 根据状态转移条件 coin\_val 分别转移到 S2(coin=10元)/S3(coin=15元)/S5(coin=25元);
- 3. 状态 S2(coin=10元), 根据状态转移条件 coin\_val 分别转移到 S3(coin=15元)/S4(coin=20元)/S6(coin=30元);
- 4. 状态 S3(coin=15元), 根据状态转移条件 coin\_val 分别转移到 S4(coin=20元)/S5(coin=25元)/S7(coin=35元);
- 5. 状态 S4(coin=20元), 根据状态转移条件 coin\_val 分别转移到 S5(coin=25元)/S6(coin=30元)/S8(coin=40元);
- 6. 状态 S5(coin=25元), 无条件转移到始态 S0;
- 7. 状态 S6(coin=30元), 无条件转移到始态 S0;
- 8. 状态 S7(coin=35元), 无条件转移到始态 S0;
- 9. 状态 S8(coin=40元), 无条件转移到始态 S0;

### 2.4 系统工作流程

1. 在 S0 状态下,如果检测到 op\_start = 1,开始检测是否有投币,如果有,一次新的售票操作开始;

- 2. 在状态 S1/S2/S3/S4 下,如果检测到 cancel\_flag = 1,则取消操作,状态退回到 S0,并退回相应的币值,否则进行投币;
- 3. 在状态 S5 下, 售票不找零; 在状态 S6/S7/S8 下, 售票并找零;
- 4. 在状态 S5/S6/S7/S8 操作完后,都返回状态 S0,等待下一轮新的操作开始;
- 5. 只有在 S0 状态下, hold\_ind = 0, 可以发起新一轮操作, 其他状态都为 1.

## 三、VHDL语言描述

```
library IEEE;
1
 2
 3
     entity saler is
         Port ( clk : in bit;
 4
                rst : in bit;
 5
                op_start : in bit;
 6
7
                coin_val : in bit_vector(1 downto 0);
                cancel_flag : in bit;
8
9
                hold_ind : buffer bit;
                tickettk_ind : buffer bit;
10
11
                refund_ind : buffer bit;
                refund_val : buffer bit_vector(3 downto 0);
12
13
                ccur_state : buffer bit_vector(3 downto 0));
     end saler;
14
15
     architecture Behavioral of saler is
16
17
     type states is (s0, s1, s2, s3, s4, s5, s6, s7, s8);
18
19
     signal state: states;
20
21
     begin
         saling_system: process(clk, rst, op_start, cancel_flag)
22
23
         begin
             -- start the saling system and initialize
24
             if(op_start = '1' or rst = '1') then
25
                 state ≤ s0;
26
                 tickettk_ind ≤ '0';
27
                 refund_ind ≤ '0';
28
                 refund_val < "0000";
29
                 hold_ind ≤ '0';
30
                 ccur_state < "0000";
31
32
             -- refund for canceling the operation
33
34
                 case state is
35
                          when s1 \Rightarrow refund_val \leq "0001";
36
                                     refund_ind ≤ '1';
37
                          when s2 \Rightarrow refund_val \leq "0010";
38
                                     refund_ind ≤ '1';
```

```
when s3 \Rightarrow refund_val \leq "0011";
39
                                          refund_ind ≤ '1';
40
                              when s4 \Rightarrow refund_val \leq "0100";
41
                                          refund_ind ≤ '1';
42
43
                              when s5 \Rightarrow refund_val \leq "0101";
                                          refund_ind ≤ '1';
44
                              when s6 \Rightarrow refund_val \leq "0110";
45
                                          refund_ind ≤ '1';
46
                              when s7 \Rightarrow refund_val \leq "0111";
47
                                          refund_ind ≤ '1';
48
49
                              when s8 \Rightarrow refund_val \leq "1000";
                                          refund_ind ≤ '1';
50
                              when others \Rightarrow refund_val \leq "0000";
51
52
                                          refund_ind ≤ '0';
53
                    end case;
               -- states transfer
54
               55
56
                   case state is
57
                        when s0 \Rightarrow case coin_val is
                                         when "01" \Rightarrow state \leq s1;
58
59
                                             ccur_state ≤ "0001";
                                         when "10" \Rightarrow state \leq s2;
60
61
                                             ccur_state < "0010";
                                         when "11" \Rightarrow state \leq s4;
62
63
                                              ccur_state ≤ "0100";
                                         when others \Rightarrow state \leq s0;
64
                                               ccur_state < "0000";
65
                                      end case;
66
                        when s1 \Rightarrow case coin_val is
67
                                         when "01" \Rightarrow state \leq s2:
68
                                             ccur_state < "0010";
69
70
                                         when "10" \Rightarrow state \leq s3;
71
                                               ccur_state < "0011";
                                         when "11" \Rightarrow state \leq s5;
72
73
                                               ccur_state < "0101";
                                         when others \Rightarrow state \leq s1;
74
                                               ccur_state < "0001";
75
76
                                      end case;
77
                        when s2 \Rightarrow case coin_val is
78
                                         when "01" \Rightarrow state \leq s3;
                                               ccur_state < "0011";
79
80
                                         when "10" \Rightarrow state \leq s4;
                                               ccur_state < "0100";
81
82
                                         when "11" \Rightarrow state \leq s6;
                                               ccur_state < "0110";
83
84
                                         when others \Rightarrow state \leq s2;
85
                                               ccur_state < "0010";
```

```
86
                                       end case;
 87
                          when s3 \Rightarrow case coin_val is
                                            when "01" \Rightarrow state \leq s4;
 88
 89
                                            ccur_state < "0100";
 90
                                            when "10" \Rightarrow state \leq s5;
                                            ccur_state ≤ "0101";
 91
                                            when "11" \Rightarrow state \leq s7;
 92
                                            ccur_state ≤ "0111";
 93
 94
                                            when others \Rightarrow state \leq s3;
                                            ccur_state ≤ "0011";
 95
 96
                                        end case;
 97
                          when s4 \Rightarrow case coin_val is
 98
                                            when "01" \Rightarrow state \leq s5;
 99
                                            ccur_state ≤ "0101";
100
                                            when "10" \Rightarrow state \leq s6;
101
                                            ccur_state < "0110";
102
                                            when "11" \Rightarrow state \leq s8;
                                            ccur_state < "1000";
103
104
                                            when others \Rightarrow state \leq s1;
105
                                            ccur_state ≤ "0001";
106
                                        end case;
107
                          when s5 \Rightarrow tickettk_ind \leq '1';
                                        hold_ind ≤ '1';
108
109
                          when s6 \Rightarrow tickettk_ind \leq '1';
110
                                        hold_ind ≤ '1';
111
                          when s7 \Rightarrow tickettk_ind \leq '1';
112
                                        hold_ind ≤ '1';
113
                          when s8 \Rightarrow tickettk_ind \leq '1';
                                       hold_ind ≤ '1';
114
                          when others ⇒ refund_ind ≤ '1'; -- uncommon state to refund
115
       and exit
116
                                        hold_ind ≤ '1';
117
                     end case;
118
                 -- get the last states
                 elsif(clk = '1' and tickettk_ind ='1') then
119
                 -- refund for extra model
120
121
                     case state is
                           when s6 \Rightarrow refund_val \leq "0001";
122
123
                                        refund_ind ≤ '1';
                           when s7 \Rightarrow refund_val \leq "0010";
124
125
                                        refund_ind ≤ '1';
                           when s8 \Rightarrow refund_val \leq "0011";
126
127
                                        refund_ind ≤ '1';
                           when others \Rightarrow refund_val \leq "0000";
128
129
                                        refund_ind ≤ '0';
130
                      end case;
131
                 end if;
132
            end process;
```

```
133
134 end Behavioral;
```

# 四、仿真配置

#### 仿真配置 Pipline: 系统启动 $\rightarrow$ 购票不找零 $\rightarrow$ 购票找零 $\rightarrow$ 取消操作

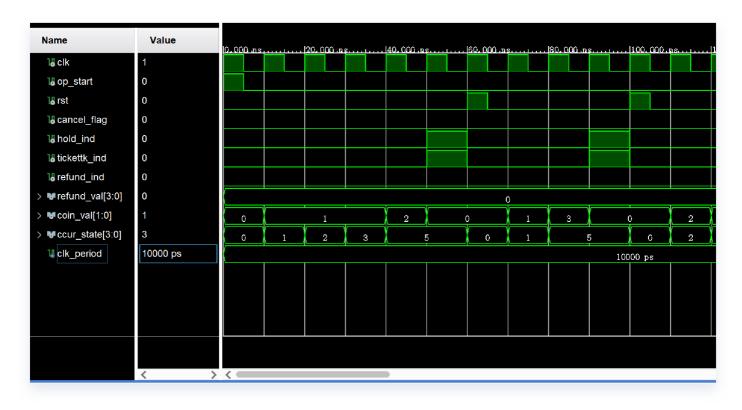
```
library IEEE;
1
 2
     use IEEE.STD_LOGIC_1164.ALL;
 3
 4
     entity saler_sim is
     end saler_sim;
 5
 6
7
     architecture Behavioral of saler_sim is
8
9
      component saler is
          Port ( clk : in bit;
10
11
                 rst : in bit;
                 op_start : in bit;
12
13
                 coin_val : in bit_vector(1 downto 0);
                 cancel_flag : in bit;
14
                 hold_ind : buffer bit;
15
                 tickettk_ind : buffer bit;
16
                 refund_ind : buffer bit;
17
                 refund_val : buffer bit_vector(3 downto 0);
18
                 ccur_state : buffer bit_vector(3 downto 0));
19
      end component;
20
21
22
     signal clk, rst, op_start, cancel_flag, hold_ind, tickettk_ind, refund_ind : bit
      := '0';
      signal coin_val : bit_vector(1 downto 0) := "00";
23
      signal refund_val, ccur_state : bit_vector(3 downto 0) := "0000";
24
      constant clk_period : time := 10 ns;
25
26
      begin
27
28
          UUT: saler port map(
                  clk \Rightarrow clk
29
30
                  rst \Rightarrow rst,
                  op_start ⇒ op_start,
31
                  coin_val ⇒ coin_val,
32
                  cancel_flag \Rightarrow cancel_flag,
33
34
                  hold_ind ⇒ hold_ind,
35
                  tickettk_ind ⇒ tickettk_ind,
                  refund_ind ⇒ refund_ind,
36
37
                  refund_val ⇒ refund_val,
                  ccur_state ⇒ ccur_state );
38
```

```
39
40
          -- clk production
          process
41
42
              begin
43
                  clk ≤ '1';
                  wait for clk_period / 2;
44
                 clk ≤ '0';
45
                 wait for clk_period / 2;
46
47
          end process;
48
49
50
51
          process
52
              begin
53
54
                  -- start the saling system
55
                  op_start ≤ '1';
56
                  wait for clk_period / 2;
57
                 op_start ≤ '0';
                 wait for clk_period / 2;
58
59
                  -- check coining
60
                  coin_val ≤ "01";
61
                 wait for clk_period;
62
                 coin_val ≤ "01";
63
                 wait for clk_period;
64
                 coin_val ≤ "01";
65
66
                 wait for clk_period;
                 coin_val ≤ "10";
67
                 wait for clk_period;
68
                 coin_val ≤ "00";
69
                  wait for clk_period;
70
71
72
                  -- reset
73
                  rst ≤ '1';
                  wait for clk_period / 2;
74
                  rst ≤ '0';
75
76
                 wait for clk_period / 2;
77
78
                  -- check coining
79
                  coin_val ≤ "01";
80
                  wait for clk_period;
81
                  coin_val ≤ "11";
82
                 wait for clk_period;
                  coin_val ≤ "00";
83
84
                  wait for clk_period;
85
86
                  -- reset
```

```
87
                   rst ≤ '1';
 88
                   wait for clk_period / 2;
                   rst ≤ '0';
 89
 90
                   wait for clk_period / 2;
 91
 92
                   -- check refund
                   coin_val ≤ "10";
 93
                   wait for clk_period;
 94
                   coin_val ≤ "11";
 95
                   wait for clk_period;
 96
                   coin_val ≤ "00";
 97
 98
                   wait for clk_period;
 99
                   coin_val ≤ "00";
                   wait for clk_period;
100
101
102
                   -- reset
                   rst ≤ '1';
103
104
                   wait for clk_period / 2;
                   rst ≤ '0';
105
                   wait for clk_period / 2;
106
107
                   -- check refund
108
109
                   coin_val ≤ "11";
110
                   wait for clk_period;
                   coin_val ≤ "11";
111
112
                   wait for clk_period;
                   coin_val ≤ "00";
113
114
                   wait for clk_period;
                   coin_val ≤ "00";
115
                   wait for clk_period;
116
117
118
                   -- reset
                   rst ≤ '1';
119
120
                   wait for clk_period / 2;
                   rst < '0';
121
122
                   wait for clk_period / 2;
123
124
                   -- check cancel
                   coin_val ≤ "10";
125
                   wait for clk_period;
126
127
                   coin_val ≤ "01";
                   wait for clk_period;
128
129
                   cancel_flag ≤ '1';
                   wait for clk_period;
130
131
                   cancel_flag ≤ '0';
                   wait for clk_period;
132
133
134
                   -- rst
```

### 五、测试代码编写、仿真与结果分析

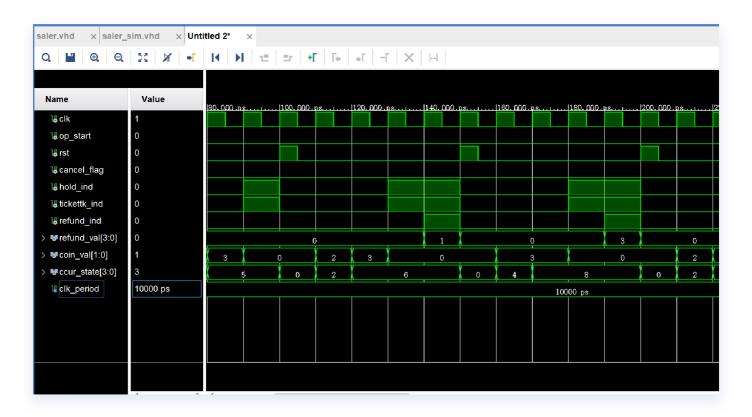
## 5.1 买票不找零: 5元 + 5元 + 5元 + 10元 / 5元 + 20元



#### • 结果分析:

- 5元 + 5元 + 5元 + 10元: 从仿真结果可以看出,在 op\_start 产生一个上升沿信号时,系统 初始化并开始启动工作;coin\_val = 1 (5元) 持续三个 CLK,使得状态从 S0 迁移到 S3,再次 coin\_val = 2 (10元) 使得状态迁移到 S5 (已投币25元);下一个时钟周期检测到可以 取票,此时设置系统被占用,并计算不找零;一轮操作结束后,系统异步复位初始化所有 状态;
- 5元 + 20元: 系统复位后, coin\_val = 1 (5元)、coin\_val = 3 (20元) 使得状态从 S0 迁移到 S1再迁移到 S5; 下一个时钟周期检测到可以取票,此时设置系统被占用,并计算不找零;一轮操作结束后,系统异步复位初始化所有状态;

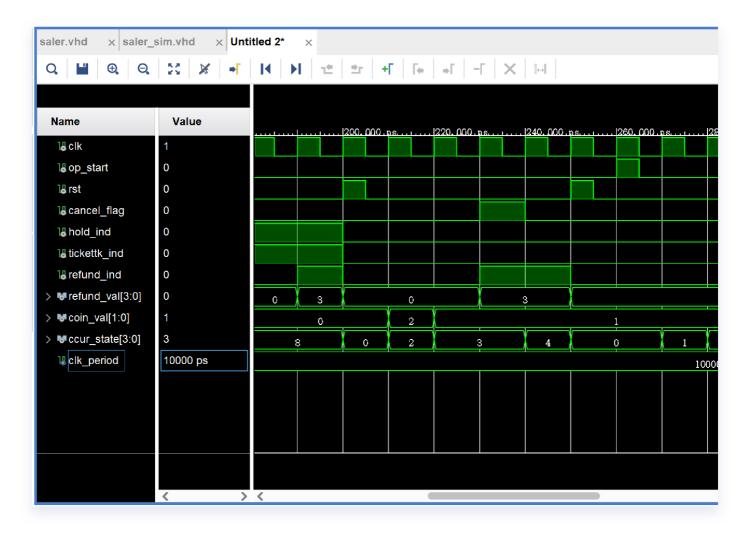
### 5.2 买票找零: 10元 + 20元 / 20元 + 20元



#### • 结果分析:

- 10元 + 20元: 从仿真结果可以看出,在系统复位后,coin\_val = 2 (10元)、coin\_val = 3 (20元)使得状态从 S0 迁移到 S2 再迁移到 S6;下一个时钟周期检测到可以取票,并计算需要找零,找零 refund\_val=1 (5元);一轮操作结束后,系统异步复位初始化所有状态;
- 20元 + 20元: 从仿真结果可以看出,在系统复位后,coin\_val = 3 (20元)持续两个CLK,使得状态从S0迁移到S4再迁移到S8;下一个时钟周期检测到可以取票,并计算需要找零,找零 refund\_val=3 (15元);一轮操作结束后,系统异步复位初始化所有状态;

### 5.3 取消操作并退款: 10元+5元



#### • 结果分析:

■ 10元 + 5元:从仿真结果可以看出,在系统复位后,coin\_val = 2 (10元)、coin\_val = 1 (5元)使得状态从 S0 迁移到 S2 再迁移到 S3;此时用户取消操作,使得cancel\_flag = 1,则系统进行自动退款,退款refund\_val = 3(15元);下一个时钟周期对系统异步复位并初始化所有状态;