

# SAT Solvers

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***Abstract —***

## **Context/Background**

The purpose of SAT Solvers is to solve the satisfiability problem, they are in use in modern day technologies for many practical reasons including: model checking, hardware verification, automatic test pattern generation, planning, scheduling, and even challenging problems from algebra. Over the years, we have seen new implementations of these SAT solvers which more importantly are becoming more efficient.

## **Aims**

The aim of this project is to research modern day SAT solvers and understand the reasons to their efficiency, discovering different implementations and understanding their uses is therefore vital. Furthermore, another aim of this project and one in which concrete results can be produced is the implementation of a SAT Solver capable in solving more difficult satisfiability problems.

## **Method**

Various SAT solvers should be researched, comparisons in their run times should be made and recorded. Implementation on different variations of SAT Solvers ranging from their complexity, understanding the underlying concepts of these implementations, taking into consideration the algorithm and data structures involved.

## **Proposed Solution.**

Research variations of the SAT Competition solvers as a initial look into the various implementations, compare running time of these variations, taking into account search problem implementations and other solutions.

Using Python to create test implementations without consideration of advanced optimisations and then moving on to produce more optimised versions either using optimised frameworks or a more low level programming language such as C++.

Research modern technologies such as machine learning and how they may be used to produce more efficient solvers.

***Keywords —*** SAT Solver, search problem, machine learning.

## I INTRODUCTION

SAT Solvers come in many variations, this project is to understand the practical purpose of SAT Solvers, how SAT Solvers have developed over the years and how some variations are implemented. Concrete results can be gathered from comparing SAT solvers through their running times and use cases. Moreover, implementation of SAT Solvers of different variations including the use of more modern techniques will be made. Overall, this project will consider SAT Solvers from a theoretical view and implementations will be made from a theoretical approach on ideas from search problem and machine learning concepts.

### Project Domain

The boolean satisfiability problem is defined as: "does there exist a assignment of variables that satisfies the given boolean formula?"

In our consideration of this problem we will focus on Boolean Formulas in Conjunctive Normal Form: A conjunction of clause where a clause is the disjunctions of literals.

- A literal is a variable or the negation of the variable:  $x$  or  $\neg x$ .
- A conjunction  $(X \wedge Y)$  or  $(X_1 \wedge X_2 \wedge \dots \wedge X_n)$  is True only when all variables are True.
- A disjunction  $(X \vee Y)$  or  $(X_1 \vee X_2 \vee \dots \vee X_n)$  is True only when at least one variable is True.

This problem is NP-complete and many other problems can be encoded as a Satisfiability problem therefore by having efficient solvers for this problem means being able to solve many other problems. Some real world applications for SAT solvers include: Model checking, scheduling and problem solving.

Model Checking is used for hardware verification and is a technique of checking some property of the hardware by describing hardware functionality as a model: a set of states  $S = \{X_1, X_2, \dots, X_n\}$  and transition relations between states  $\tau(S_1, S_2), S_1 \in S$  and  $S_2 \in S$ . Some properties checked may include: Liveness, where a property eventually occurs e.g. used to check whether a response is eventually given or to check if cycles occur and Safety where a property always holds e.g. the reachability problem, to check whether or not a bad state can be reached. Bounded model checking where a property is checked within some  $k$  steps can be solved by SAT solvers checking for satisfiability on the formula:

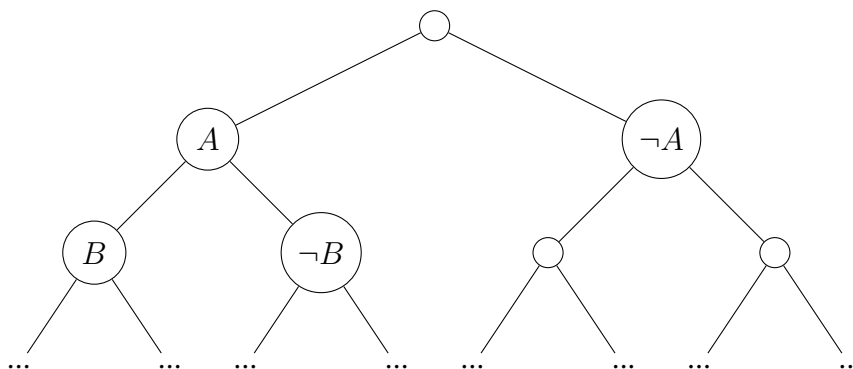
$$I(s_0) \wedge \bigwedge_{i=0}^{k-1} \tau(s_i, s_{i+1}) \wedge \bigvee_{i=0}^k \neg p(s_i)$$

Where  $I(s_0)$  is defined as  $s_0$  being the initial state, and all states  $s_i$  are iterated through by a legal transition state and none of the states reached have the property  $p$ .

## Project Overview

The structure of this project will be focused on gaining insight in the multiple variations of SAT solvers from a theoretical view. Moreover, the projects goal of implementing of SAT solvers will allow a more in depth insight on workings of the algorithms.

DPLL algorithm - Davis Putnam Loveland Logemann - is one such variety and one that is a basis to many modern day solvers, the algorithms involves getting as input a CNF formula and outputs whether the formula is satisfiable and if it is in fact satisfiable then it returns an assignment of variables that does so. The procedure to this algorithm follows a tree where each node of the tree is a set of clauses  $S$  and at each node an assignment to a variable is made.



At each node after a variable has been assigned, we then continually apply inference (propagation) and so reducing the number of assignments and thus nodes in the tree. A node no longer has children if the node has the set  $\{\}$  as its set of clauses or if the set of clauses contain the empty set  $\epsilon \in S$ .

If a branch has  $\{\}$  as its set of clauses then the formula is satisfiable and if all branches' set of all clauses contain the empty set  $\epsilon \in S$  then the formula is unsatisfiable.

CDCL - Conflict Driven Clause Learning - is another variety of SAT solvers first used in Grasp [1] which has influenced many modern SAT Solvers, this variety of solvers is characterised by adding new clauses to its initial set of clauses as the algorithm progresses and non-chronological backtracking. As the search progresses and the branch of the search tree encounters a conflict i.e. a variable is previously set True and the most recent propagation requires the variable to be set False or vice versa. Through the conflict the algorithm learns the clause to be added to our set of clauses (clause database). The non-chronological backtracking and clause learning enables the algorithm to prune the search tree and significantly reduce the time of discovering a satisfiable assignment or to show that the formula is unsatisfiable.

There are also local search algorithms for SAT which is not complete and so may not provide an assignment of variables that satisfy the formula and may not be able to show that the formula is unsatisfiable however these algorithms can be shown to be efficient in specific cases of problems. Furthermore, a more modern approach to solving SAT problems is the use of machine learning and describing the problem as a classification problem, we can classify SAT problems so that solvers more efficient on specific categories of problems can be applied, this is in contrast to the more general SAT solvers.

## **Project Deliverables**

### **Basic deliverables:**

1. Implement a brute force SAT Solver using an intuitive programming language for the problem (may not be efficient)
  - - compromise on efficiency to produce a basic SAT solver using a high level language e.g. Python. Use an intuitive implementation relying on simple data structures.
2. Develop understanding on a programming language that aims to increase efficiency
  - - Read online documentation on a programming language that allows efficient use of data storage and manipulation e.g. C++, consider efficient uses of data structures and lower level data handling.
3. Discover and understand optimisations made to DPLL implementations of SAT Solvers
  - -Research and develop an understanding on faster algorithms and consider the implementation of such an algorithm in the high level programming language to show proof of concept.
4. Develop an understanding on machine learning
  - - Undergo an online course\* on basic machine learning concepts

### **Intermediate Deliverables:**

1. Research how machine learning can be applied to SAT Solving
  - - Read articles and research papers on the use of machine learning in SAT solving
2. Implement a DPLL SAT Solver using an efficient programming language
  - - implement DPLL Sat Solver using the previously studied lower level programming language.
3. Implement a DPLL SAT Solver involving multiple optimisations
  - - implement efficient data structures using the lower level language and implement researched optimisations to the algorithm e.g. watched literals.
4. Explore and implement other versions of SAT solvers e.g. local search

### **Advanced Deliverables**

1. Analyse the efficiency benefits of the different optimisations in DPLL and its possible costs
2. Using the knowledge of most beneficial optimisations create a SAT Solver that is as efficient as possible (with the optimisations explored)
3. Implement a SAT Solver that utilises machine learning giving its benefits and costs

## II DESIGN

The design outcome of this project is to produce multiple SAT solvers that can take as input a text file of the satisfiability problem in DIMACS format [reference to <http://www.satcompetition.org/2009/formbenchmarks2009.html>].

```
c - this is a comment
c
c
p cnf 5 3
1 -5 4 0
-1 5 3 4 0
-3 -4 0
```

Where lines beginning with c is a comment, the line beginning with p is the format of the file e.g. the above file "p cnf 5 3" is in Conjunctive Normal Form (CNF), has 5 variables and 3 clauses. The lines below the one starting with p are the clauses of the satisfiability problem where each number denotes the variable and a minus symbol denotes the variables negation. The end of the clause is also represented by a 0.

### Choice of Programming Language

The two languages utilised in this project will be python and C++, I have used Python throughout my undergraduate studies giving me a firm familiarity with Python and therefore understanding its ease of implementation however lack of low level options resulting in less than optimal [programs].

The high level nature of Python allows for fast proof of concept of algorithms allowing me test and run to different implementations quickly. This will be valuable as there are many variations of SAT solvers and a large portion of this project is to research algorithms in comparison to solely implementing them.

Moreover, there are multiple machine learning libraries for Python and so in combination with Python's high level nature will provide a suitable level of support for testing machine learning concepts, one such example of these libraries is Pytorch a popular machine learning library and so will also result in external support such as tutorials.

On the other hand, C++ is a middle level language allowing for lower level control which will prove useful when producing more optimal implementations with the focus on reducing run time. As we will comparing run times of implementations it will be useful to remove overheads provided from the language and so C++ is a more suitable language for this purpose.

### SAT Solver Variations

#### DPLL

The DPLL algorithm is a backtracking search of possible assignments of variables. Our implementation of this algorithm will follow the simplest depth first search version, a recursive form

that branches on a decided variable.

We initially run unit propagation on our input clauses and check for satisfiability or unsatisfiability. Unit propagation is the search for any unit clauses, where a unit clause is a clause that contains only one literal, after finding a unit clause we then fix the literal (i.e. a variable or its negation) found in the unit clause to True, furthermore, for every clause that contains the literal we remove from our set of clauses as well as removing negations of literals from all clauses.

We then check for an empty set of clause to indicate that the problem is satisfied, whereas if there is an empty set within our set of clause then the problem is unsatisfied.

(reference to graph on previous pages???)

After the initial check for satisfiability and unsatisfiability we then decide a variable to branch on, the function is recursively called twice on the set of clauses with the addition of either adding a unit clause of the variable in one case or a unit clause of it's negation in the other case.

### Unit Propagation

Input Boolean Formula:

$(p \vee q \vee r) \wedge (\neg p \vee r) \wedge (p \vee s)$

Decide:

Set literal to true e.g. assign  $\neg p$  to True.

Update clauses:

$(q \vee r) \wedge (s)$

Find Unit Clauses:

$(q \vee r) \wedge \mathbf{(s)}$

Unit Propagation:

Set literal in unit clause to true e.g. assign  $s$  to True.

**Resolve** - Once all assignments have been propagated and there are no longer any new variables to be assigned a value we check to see if there are any conflicts. If a conflict exists we backtrack, in our basic variation of our DPLL algorithm we backtrack to the last decision stage in which both values has not yet been assigned to the variable.

Input : A CNF formula  $F$  and an initially empty partial assignment  $\rho$

Output : UNSAT, or an assignment satisfying  $F$

begin

$(F, \rho) \leftarrow \text{UnitPropagate}(F, \rho)$

if  $F$  contains the empty clause then return UNSAT

if  $F$  has no clauses left then Output  $\rho$  return SAT

$l \leftarrow$  a literal not assigned by  $\rho$  //the branching step

if DPLL-recursive( $F|_l, \rho \cup \{l\}$ ) = SAT then return SAT

return DPLL-recursive( $F|_{\neg l}, \rho \cup \{\neg l\}$ )

end

### Pseudocode for DPLL Implementation [reference]

The DPLL is an early SAT Solver and our implementation has many optimisations that can be added, for example we can consider:

- More optimally deciding which variable to branch on.
- Ignoring branches that will not have a satisfying assignment.

## CDCL

CDCL Solvers are a variation of the DPLL algorithm and employs clause learning and other optimisations. Clause learning is the addition of learnt clauses to the original boolean formula throughout the running of the algorithm and is what distinguishes a CDCL solver to the original DPLL solvers. Our CDCL implementation will follow a repeating three stage process: Decide, Deduce and Resolve.

1. Decide - At the decide stage of our algorithm we select a variable and assign it a value 0 or 1.
2. Deduce - After the decide stage, remove all clauses with literals of which evaluates to True due to the decided variable assignment, moreover, remove all literals from clauses where that literal evaluates to false after the decided variable. Finally, we can see if we can deduce any new assignment of variables through unit propagation.
3. Resolve - Once all assignments have been propagated and there are no longer any new variables to be assigned a value we check to see if there are any conflicts. If a conflict exists we backtrack to the first unique implication point (UIP), from the conflict we can also learn new clauses that can be added to our input set of clauses.

$\text{CDCL}(\phi, v)$

```

1. if (UnitPropagation( $\phi, v$ ) == CONFLICT)
2.   then return UNSAT
3.  $dl \leftarrow 0$ 
4. while (not AllVariablesAssigned( $\phi, v$ ))
5.   do ( $x, v$ ) = PickBranchingVariable( $\phi, v$ ) [decide stage]
6.      $dl \leftarrow dl + 1$ 
7.      $v \leftarrow v \cup \{(x, v)\}$ 
8.     if (UnitPropagation( $\phi, v$ ) == CONFLICT) [deduce stage]
9.       return SAT
10.    then  $\beta = \text{ConflictAnalysis}(\phi, v)$  if ( $\beta < 0$ ) [resolve stage]
11.    then return UNSAT
12.    else Backtrack( $\phi, v, \beta$ )
13.     $dl \leftarrow \beta$ 

```

Pseudocode for CDCL Implementation [reference]

Definitions:

- **Decision Level:** every variable has a decision level, at each decision stage of our algorithm the decision level is incremented and the decided variable is set the new decision level, during the unit propagation stage if a variable is assigned a value then its decision level is set the same as the decision level of the most recent decided variable.
- **Antecedent:** Every variable has a antecedent, this is the original clause of the unit clause during unit propagation stage that caused the assignment of the variable. Variables assigned at the decide stage is set the empty clause as its antecedent.

In this project I will focus only a few of the many optimisations for CDCL including: clause learning with UIP (unique implication points), lazy data structures with watched literals and backtracking to the first UIP.

1. **clause learning with UIP** - Conflicts occur during the unit propagation stage, we search through the antecedents of variables of the highest decision level, those that were assigned during the most recent unit propagation, from these antecedents we add to a empty clause the literals that have a decision level value lower than the current decision level, finding such variables is achievable through resolution, this new clause will be the learnt clause and is added to our current set of clauses.

Resolution( $\omega_1, \omega_2$ ):

Input: two clauses with one containing unique variable  $x$  and the other containing  $\neg x$  :

$$\omega_1 = (x \vee y_1 \vee y_2 \vee \dots), \omega_2 = (\neg x \vee z_1 \vee z_2 \vee \dots)$$

Output: clause containing all variables found in both clauses except  $x$  and  $\neg x$ :

$$(y_1 \vee y_2 \vee \dots \vee z_1 \vee z_2 \vee \dots)$$

2. **Watched Literals** - This is a lazy data structure, it's implementation is to use references to two literals of each clause in our input set of clauses, simply by referencing only two variables we do not need to scan every literal in our clause to test for a unit clause instead we update our reference to another literal if the negation of a watched literal is fixed True, at the point where there is no longer any literals left of which its variable has not been assigned then the clause is a unit clause.

w	w				
---	---	--	--	--	--

Clause represented by array - first two literals are watched literals.

w	w				
---	---	--	--	--	--

Second literal fixed to False.



w		w			
---	--	---	--	--	--

Second watched literals reference is set to another literal that has not yet been fixed.

## A Main Text

The font used for the main text should be Times New Roman (Times) and the font size should be 12. The first line of all paragraphs should be indented by 0.25in, except for the first paragraph of each section, subsection, subsubsection etc. (the paragraph immediately after the header) where no indentation is needed.

## B Figures and Tables

In general, figures and tables should not appear before they are cited. Place figure captions below the figures; place table titles above the tables. If your figure has two parts, for example, include the labels “(a)” and “(b)” as part of the artwork. Please verify that figures and tables you mention in the text actually exist. make sure that all tables and figures are numbered as shown in Table 1 and Figure 1.

Table 1: UNITS FOR MAGNETIC PROPERTIES

Symbol	Quantity	Conversion from Gaussian
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## C References

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