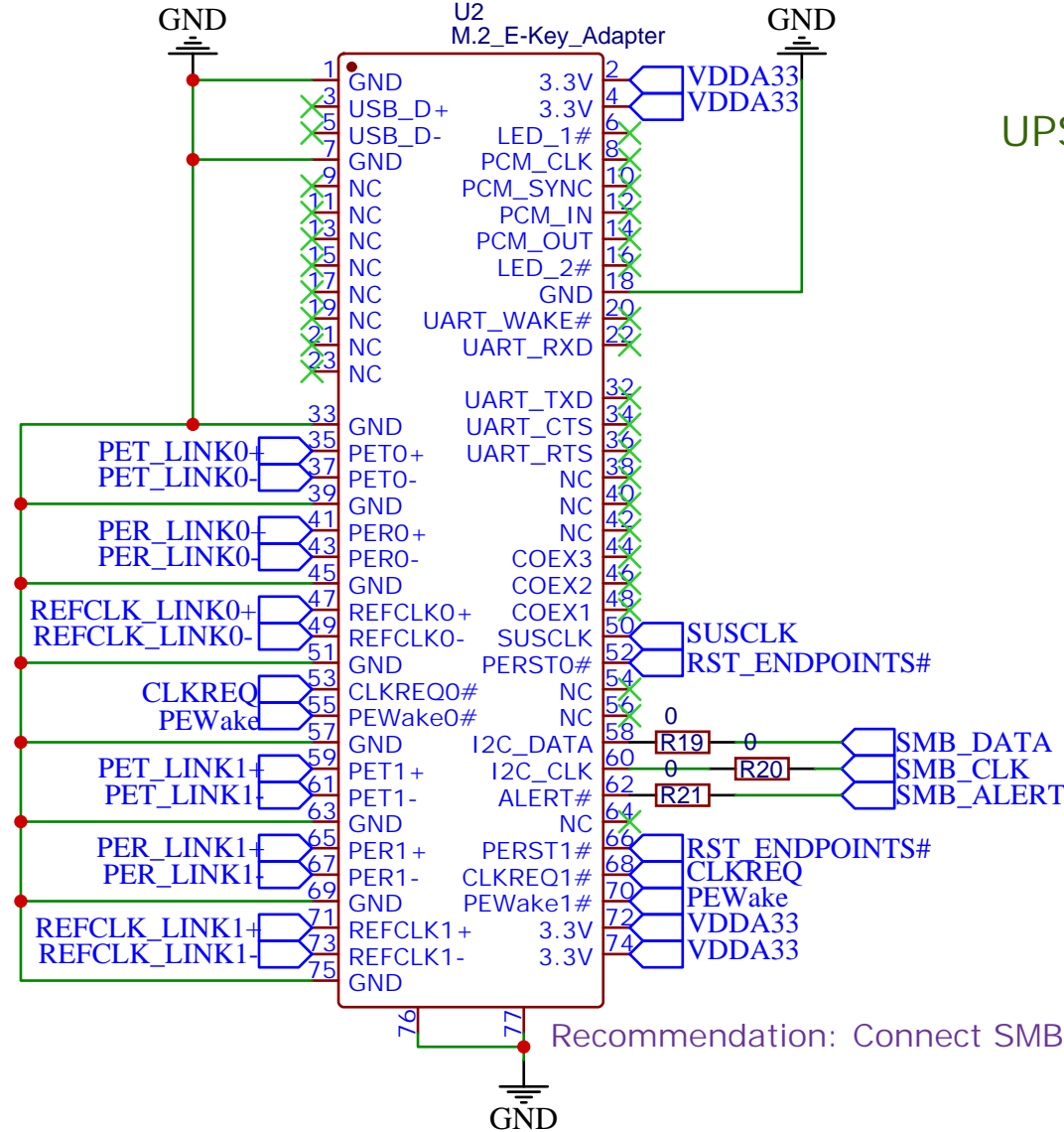
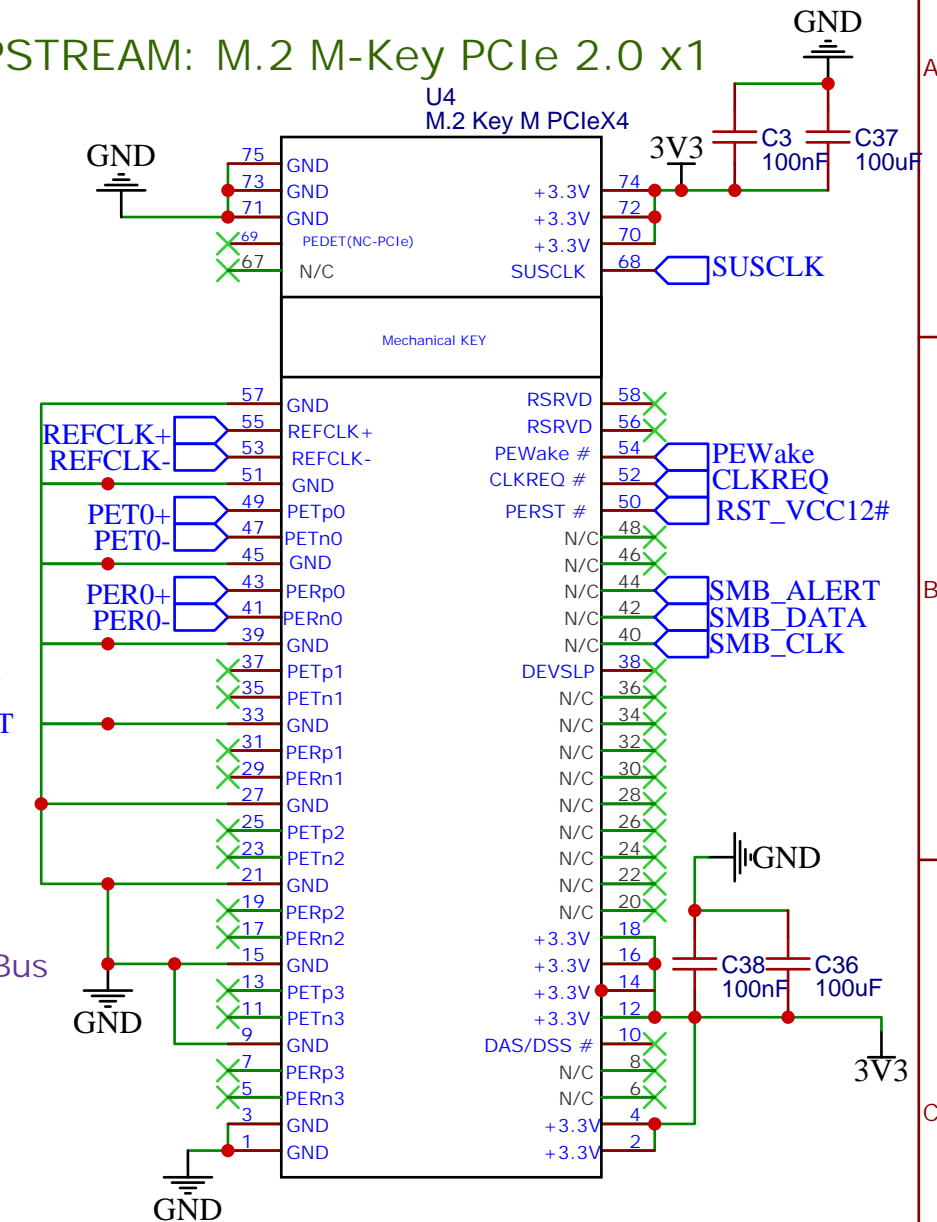


CONFIGURATION: De-Solder to change

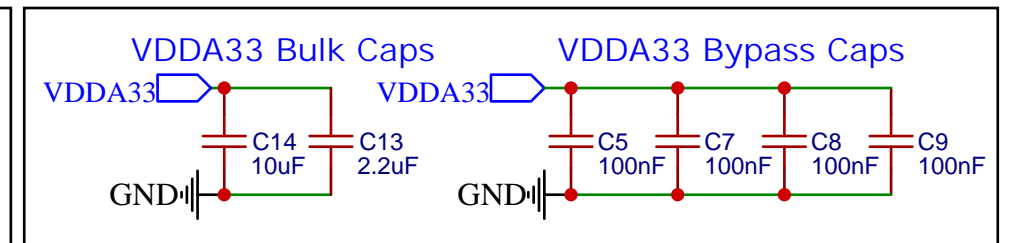
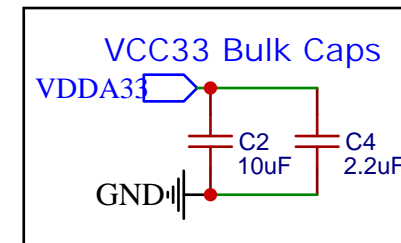
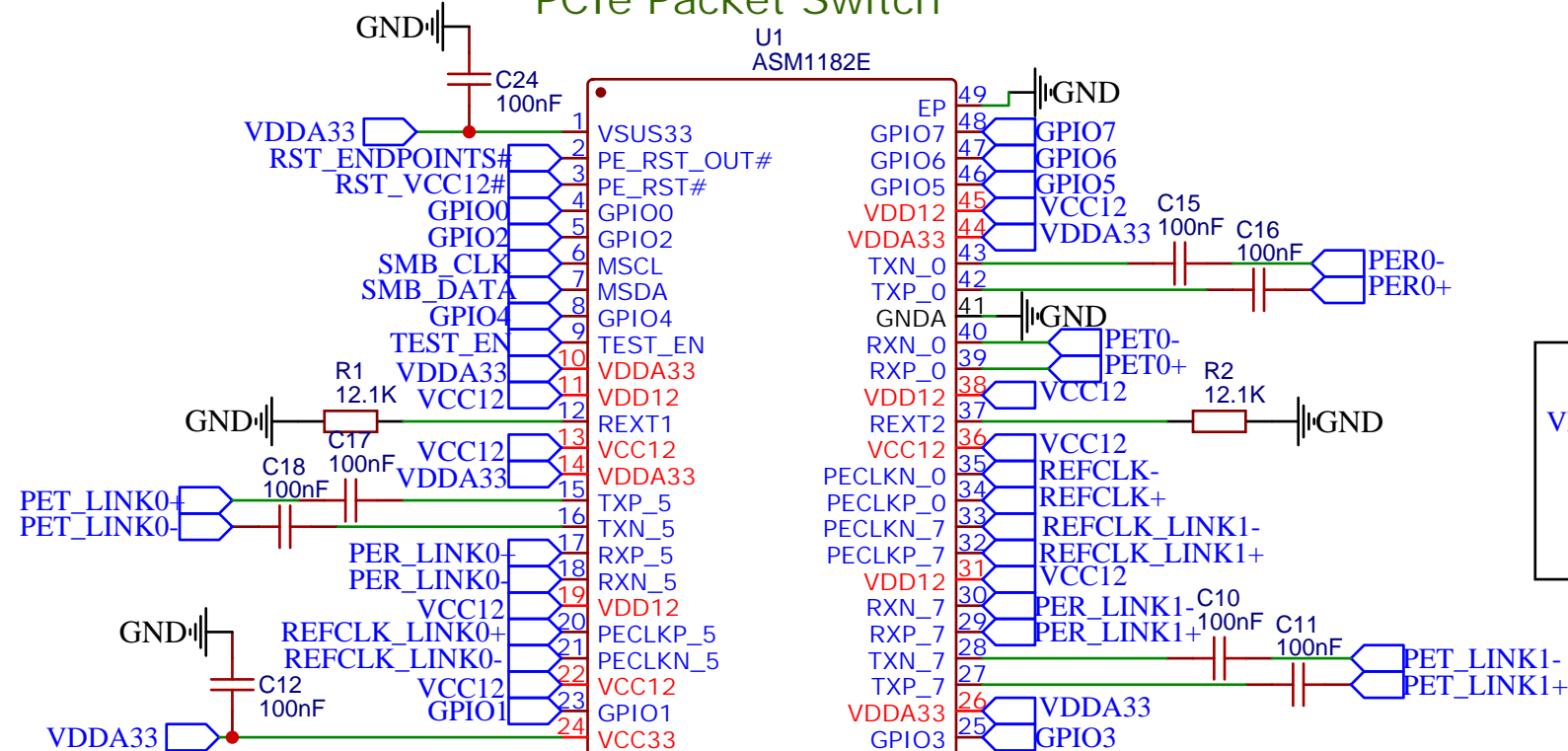
DOWNSTREAM: M.2 E-Key 2x PCIe 2.0 x1



UPSTREAM: M.2 M-Key PCIe 2.0 x1



PCIe Packet Switch



TITLE: PCIe Switch + Connectors		REV: 1.0
Company: Your Company		Sheet: 1/1
Date: 2021-10-09		Drawn By: Florian K.

VCC12 Buck Converter with Disable-on-Reset Logic (optional)
800mA max.

U3
TD6810

ENABLE_VCC12

GND

L2
4.7uH

VCC12

C1
10uF

C19
10uF

C21
22pF

R14
150K

R15
150K

GND

GND

GND

VDDA33

C20
10uF

GND

VCC12 Bulk Caps

VCC12

C25
10uF

C22
2.2uF

C26
10uF

C27
2.2uF

GND

VCC12 and VDD12 Bypass Caps

VCC12

C23
100nF

C28
100nF

C29
100nF

C30
100nF

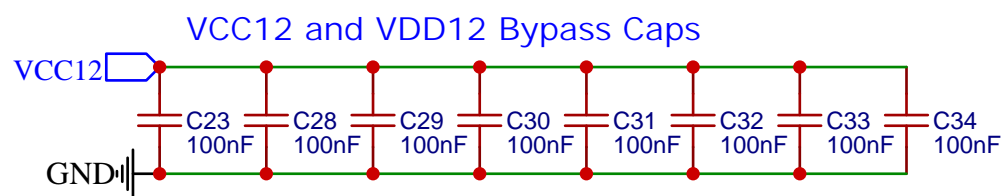
C31
100nF

C32
100nF

C33
100nF

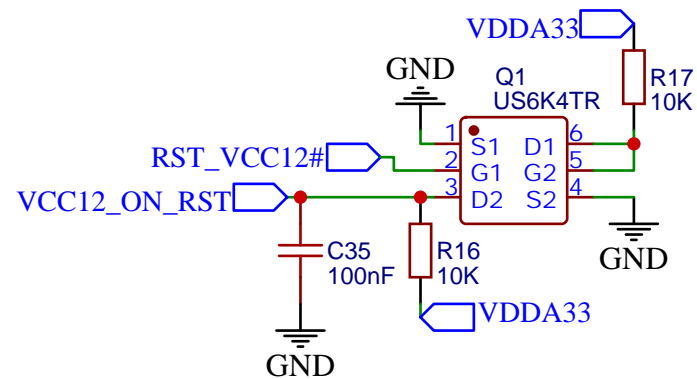
C34
100nF

GND



VCC12 Reset Guard (Dual N-Channel MOSFET)

If PERST# from PCIe Master is deasserted (high):
-> Power on 1.2V Supply for switch



Recommendation: Always-on

Wiring diagram for P1 Header-Female-2.54_1x3:

- VCC12 is connected to pin 1.
- VDDA33 is connected to pin 2.
- GND is connected to pin 3.

TITLE: 1.2V Switch Regulator		REV: 1.0
	Company: Your Company	Sheet: 1/1
	Date: 2021-10-12 Drawn By: Florian K.	