Tovis MNT2 Schematic

Rev. B0

Revision History

Date	Author	Version	Comments
Sep 20.2015	Liu Wanggen	Rev. A0	Initial schematic.
Dec 20.2016	Liu Wanggen	Rev. B0	Page 7:R101 and R102 changed to 1K Page 8:R110 connect to the net of PANEL_ON5V R120 connect to the net of OPC_SEL

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01. Contents	1
02. Top Level	2
03.VGA Input	3
04.DVI Input	4
05.DP Input	5
06.Power	6
07.RTD2486AD	7
08.Backlight and LVDS Out	8

Notes:

1.All resistors are 1/10w, 5%, 0603 unless otherwise indicated. The unit is ohm.
2.ESD Protectors:The analog signal of VGA and Key I/ uses bav99.DVI Signal input uses AZC099-04S.DP signal input uses AZC1045-04SU.

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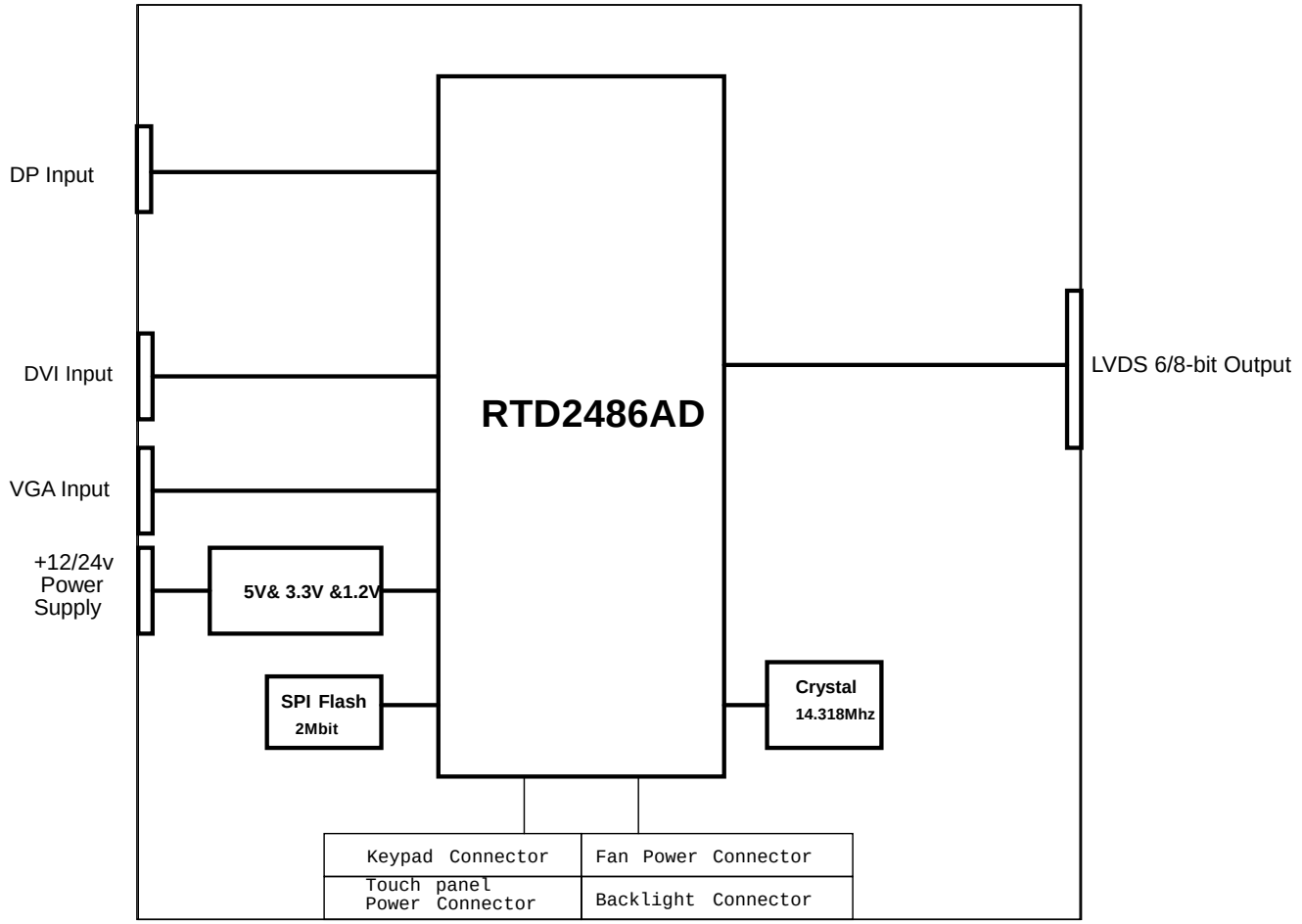

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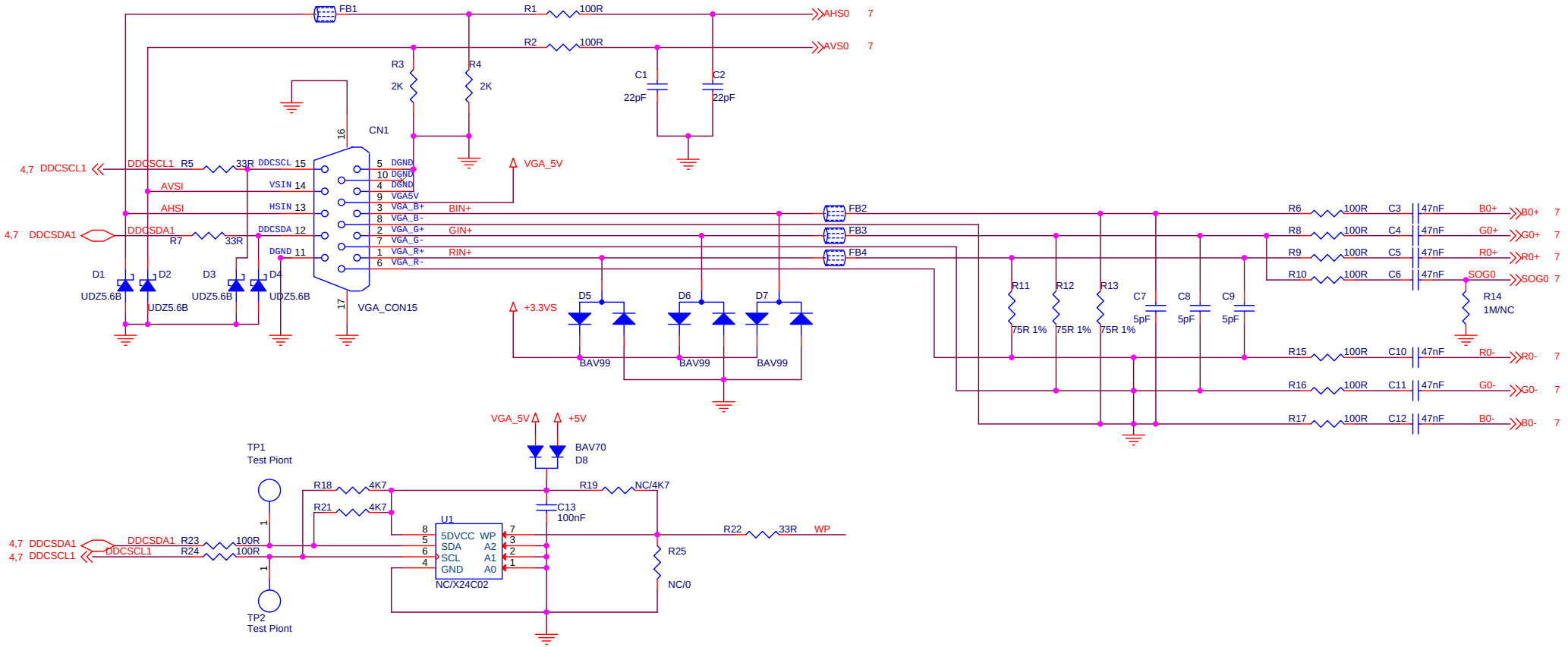

Note:If U1 NC,R20,R23,R26,R44,Q1 also are NC.

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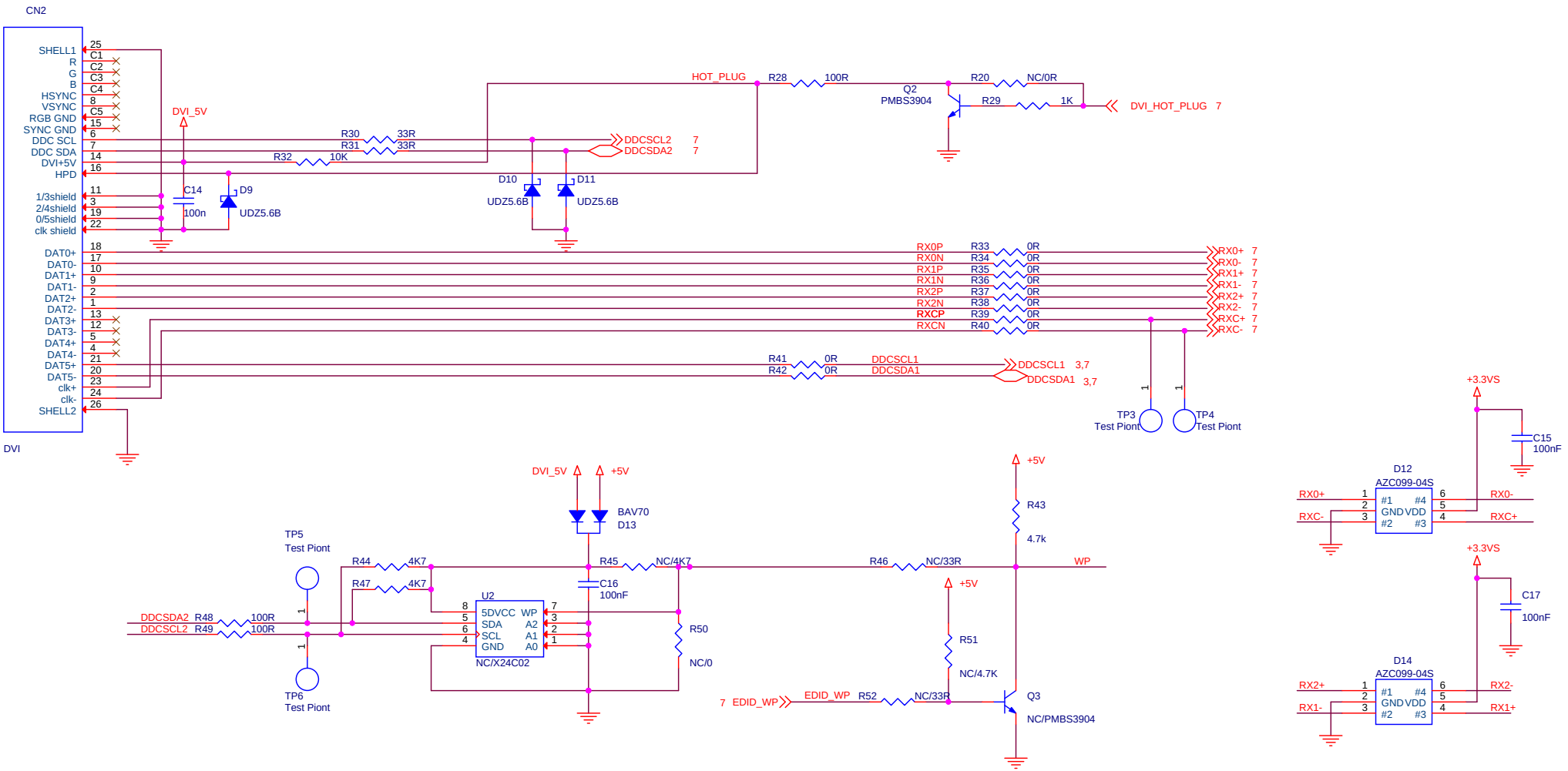

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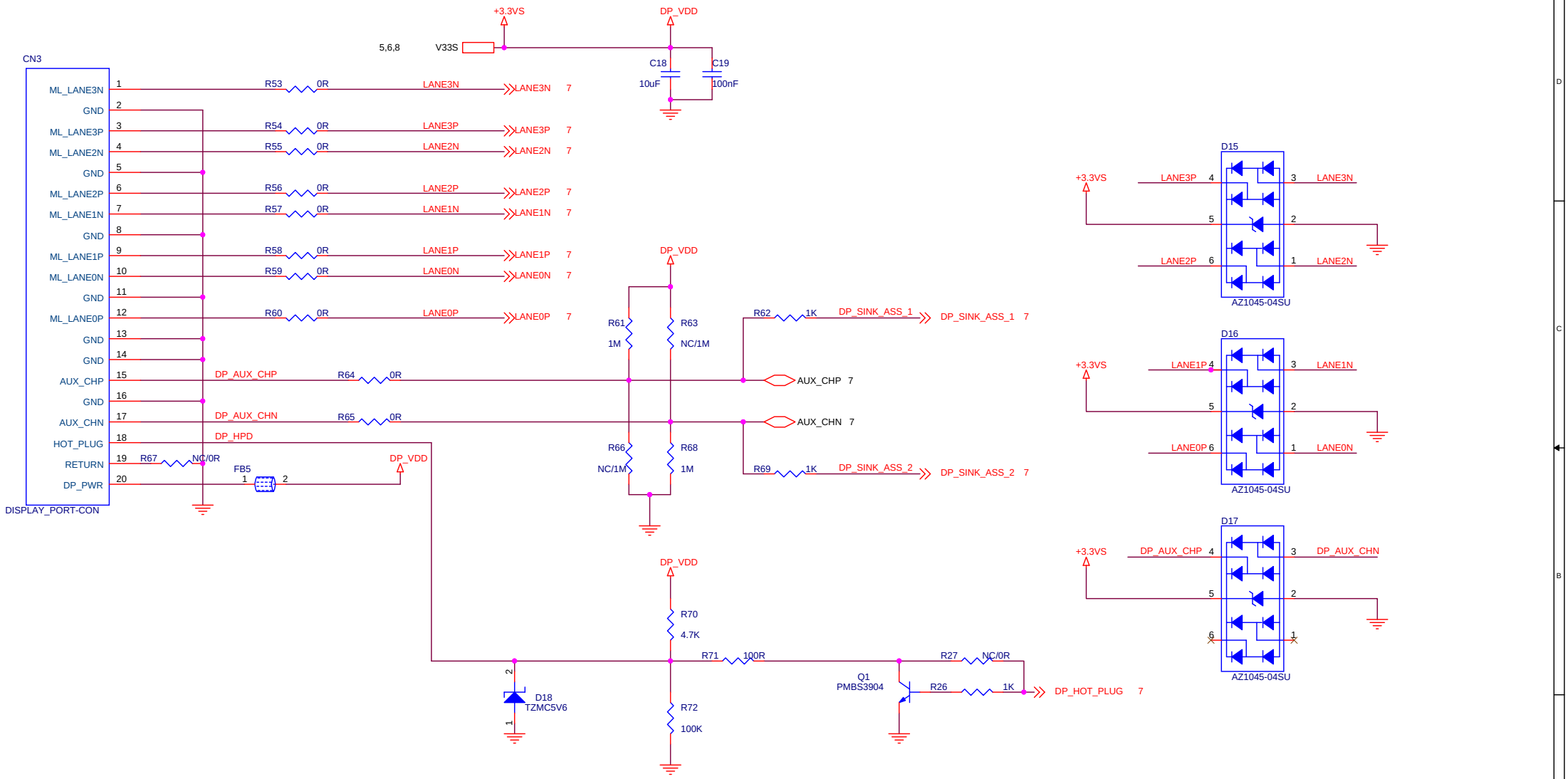

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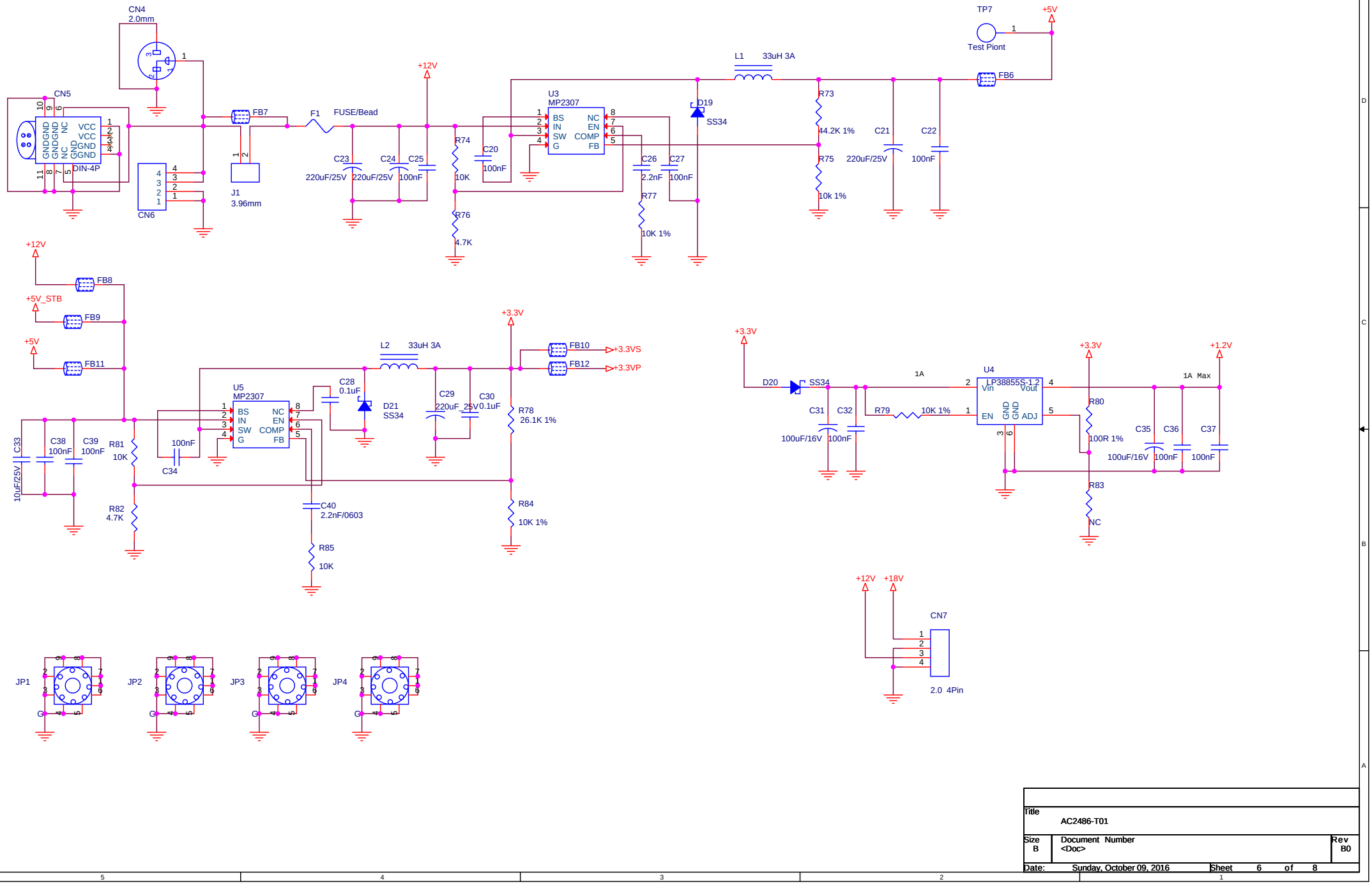

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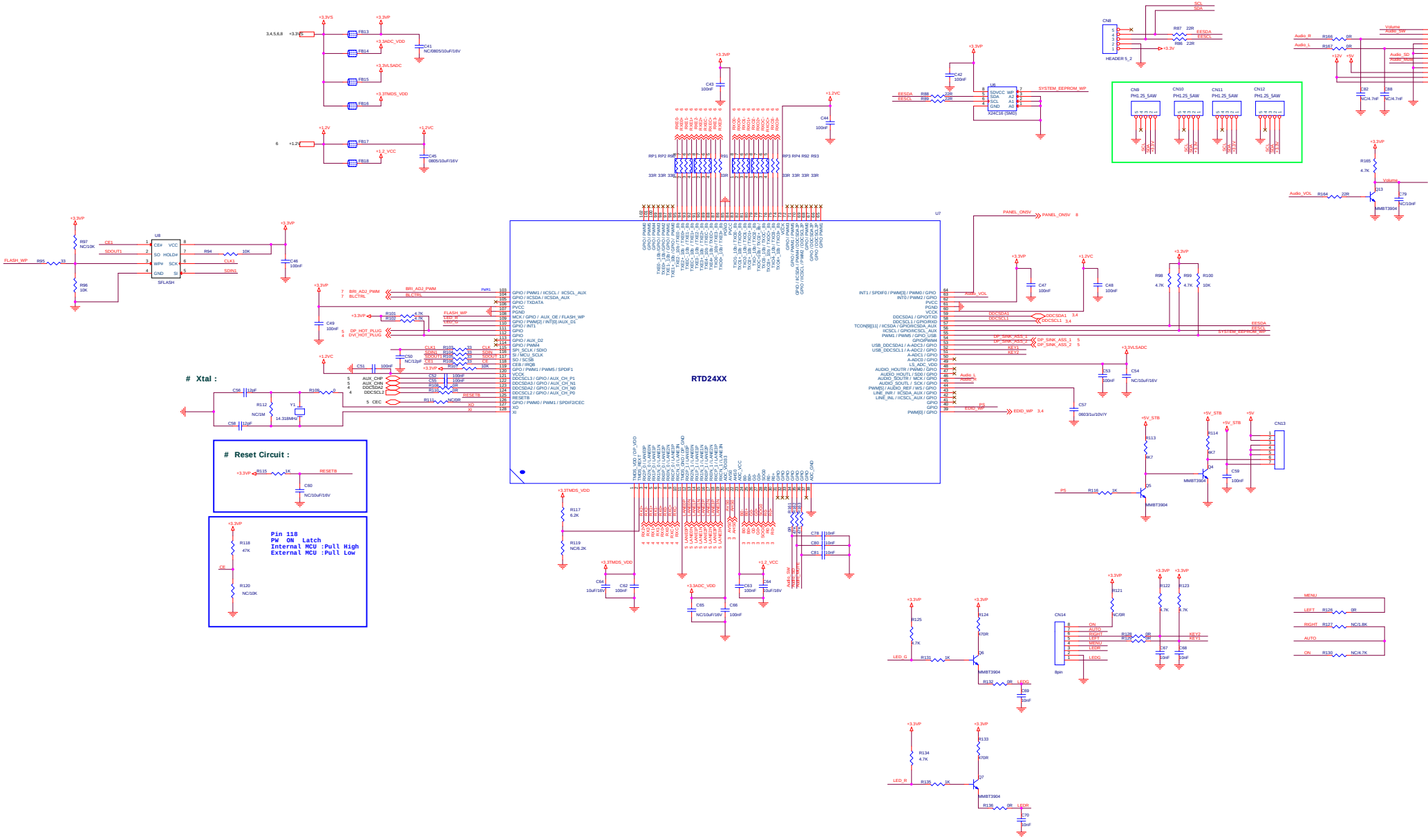
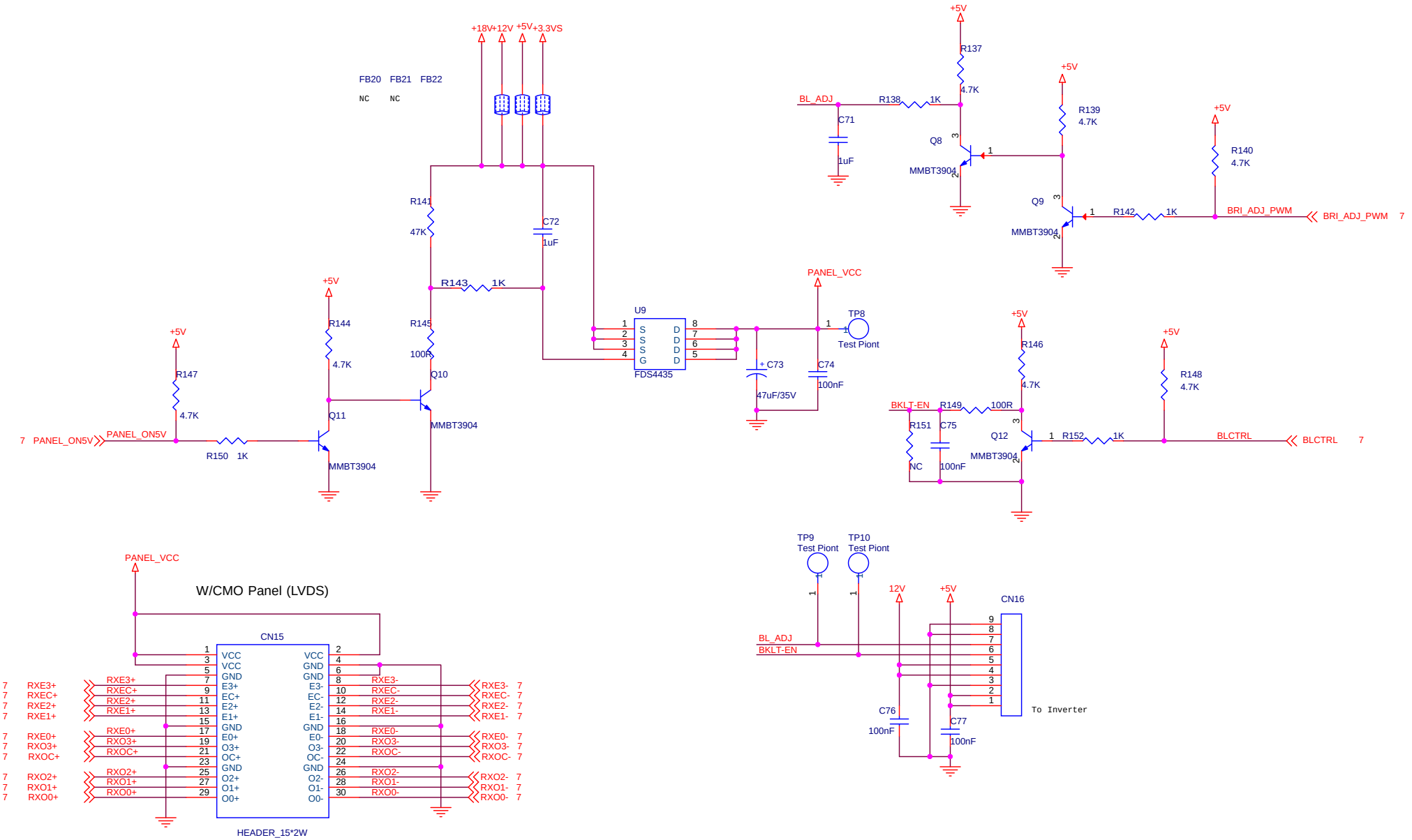

RTD24XX
Xtal :
Reset Circuit :
Pin 118
PW_ON Latch
Internal MCU :Pull High
External MCU :Pull Low
SFLASH
HEADER 5_2
X24C16 (SMD)
Shenzhen Beacon Display Technology Co.,Ltd
AC2486-T01
Wednesday, January 04, 2017



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