

Day	Sem	Time									3rd Semester	Instructors		
		1 7.55-8.50	2 8.55-9.50	3 10.05-11.00	4 11.05-12.00	5 13.00-13.55	6 14.00-14.55	7 15.05-16.00	8 16.05-17.00	9 17.05-18.00		Section S1	Section S2	
MON	III-3S1	CS200	MA204	CS201		,CS202(M)	DSA(B1),DDS(B2)			Open Electives	CS200-Theory of Computation [3-1-0] 4		Shashidhar Koolagudi	
	III-3S2		DSA(B3),DDS(B4)				CS200	CS201	MA204		CS201-Design of Digital Systems [3-1-0] 4		B.R.Chandavarkar,	
	V-5S1	CS701(H)		CS300	CS304	CS252(M)	SE(B1),CN(B2)			Open Electives	CS202-Data Struct. and Algo.[3-1-0]4		Vani M.	
	V-5S2		SE(B4),CN(B3)				CS300	CS304			CS203-Design of Digital Sys Lab(0-0-3)2		B.R.Chandavarkar,	
	VII		CS415	SM300	CS418	CS351(M)	CS421	CS360	CS365	CS204- Data Struct. And Algo Lab(0-0-3)2		Vani M.		
TUE	III-3S1	CS200	MA204	CS202		CS202(M)	DSA(B2),DDS(B1)			Open Electives	MA204-Linear Algebra and Matrices(3-0-0)3		MATHS Slot:A,B	
	III-3S2		DSA(B4),DDS(B3)				CS200	CS202	MA204		CS202-Data Struct. and Algo.[3-1-0]4(Minor)		Annappa B.	
	V-5S1	CS701(H)	CS300	CS301	CS304	CS252(M)	SE(B2),CN(B1)				5th Semester		Section S1	Section S2
	V-5S2		SE(B3),CN(B4)				CS301	CS300	CS304		CS300-Software Engineering[3-1-0] 4		K.Chandrasekaran	
	VII		CS418	CS415	SM300	CS351(M)	CS421	CS360			CS301-Computer Networks[3-1-0]4		Saumya Hegde	
WED	III-3S1		CS201	CS200	CS202	MA204	DDS(B2)			Open Electives	CS302-Computer Networks Lab(0-0-3)2		Saumya Hegde	
	III-3S2	MA204	DDS(B4)			CS200	CS202	CS201			CS303-Software Engineering Lab(0-0-3)2		K.Chandrasekaran	
	V-5S1	CS701(H)		CS304	CS300	CS301	CP(B11)				CS304-Compiler Design[3-1-0]4	P.Santhi Thilagam	Biswajit R. Bhowmik	
	V-5S2		CP(B3)				CS301	CS300	CS304			CS305-Compiler Design Lab[0-0-3]2		P.Santhi Thilagam
	VII		CS418	CS415	CS365	SM300	CS421				CS252-Operating System[3-1-0] 4 (Minor)		Mohit P. Tahiliani	
THUR	III-3S1	CS202(M)	CS202	CS200	CS201		DDS(B1)			Open Electives	CS701-High Performance Computing(3-0-2] 4 (Honor)		Biswajit R. Bhowmik	
	III-3S2		DDS(B3)			CS200	CS202	CS201			7th Semester		Instructors	
	V-5S1	CS252(M)		CS300	CS304	CS301	CP(B2)				CS400- Practical Training(0-0-2)1	Manu Basavaraju	Jeny Rajan	
	V-5S2		CP(B4)				CS301	CS300	CS304					
	VII	CS351(M)	CS418	CS415	CS365		CS421	CS360			SM300- Engineering Economics(3-0-0)3		SOM Department	
FRI	III-3S1	CS202(M)	CS201		CS202					Open Electives	CS351-Machine Learning(3-1-0)4(Minor)		Dr. Asoke Talukder	
	III-3S2			CS201			CS202				7th sem Electives		Instructors	
	V-5S1	CS252(M)		CS701(H)		CS301		HPC Lab			CS421-Digital Image Processing[3-1-0]4	Jeny Rajan		
	V-5S2					CS301	CS415-Wireless Networks(3-1-0)4					Mohit P. Tahiliani		
	VII	CS351(M)	SM300	CS360	CS400		CS365				CS418-Topics in Information Security(3-1-0)4		Mahendra P Singh	
											CS360- Graph Theory (3-1-0)4		Manu Basavaraju	
											CS365-Advanced Data Structures(3-1-0)4		Sourav Kanthi Addya	
NOTE: M-Minor H-Honour														

NOTE: M-Minor, H-Honour

Time Table-Incharge

Head of the Department

Department of Computer Science & Engineering,NITK-Surathkal , M. Tech Master Time-Table, July- Dec 2021 (ODD) -													
Day	Sem	Time									I.M.Tech CSE	Instructor	
		1 7.55- 8.50	2 8.55- 9.50	3 10.05- 11.00	4 11.05- 12.00	5 13.00- 13.55	6 14.00- 14.55	7 15.05- 16.00	8 16.05- 17.00	9 17.05- 18.00			
MON	CSE	CS701	CS721	MA714	CS700	CS739	Computing Lab		CS737	Open Electives	CS700-Algorithms and Complexity[3-0-2]4	Alwyn.R.Pais	
	CSE-IS		Alogo.Comp Lab				CS800	MA714			CS701-High Performance Computing[3-0-2]4	Biswajit R. Bhowmik	
TUE	CSE	CS701	CS721	MA714	CS700	CS739	Alogo.Comp Lab		CS740		CS709-Seminar(2)	Alwyn.R.Pais	
	CSE-IS		NTC Lab				CS800	MA714			CS702-Computing Lab(0-0-3)2	AL1	
WED	CSE	CS701	CS721	MA714	CS737		Seminar		CS740		MA714-Math. Foundations of Comp. Sci. [3-0-0]3	MACS Department	
	CSE-IS		Computing Lab				CS800	MA714			Elective Course(CSE)		
THU	CSE			CS721	CS700		CS737	CS740	CS739		CS721-Advanced Algorithms(3-1-0)4	Manu Basavaraju	
	CSE-IS		Seminar								I M.Tech CSE-IS	Instructor	
FRI	CSE	CS737	CS740	CS701	CS700		CS739	HPC LAB				CS700-Algorithms and Complexity[3-0-2]4	Alwyn.R.Pais
	CSE-IS			CS800							CS800-Number Theory & Cryptography[3-0-2]4	Mahendra P Singh	
											CS809-Seminar(2)	Alwyn.R.Pais	
											CS801-Computing Lab(0-0-3)2	AL2	
											MA714-Math. Foundations of Comp. Sci. [3-0-0]3	MACS Department	
											Open Electives(CSE,CSE-IS)		
											CS740- Blockchain Design Architecture(3-1-0)4	Sourav Kanti Addya	
											CS737-Deep Learning(3-1-0)4	Jeny Rajan	
											CS739-Cyber-Physical Systems(3-1-0)4	Biswajit R. Bhowmik	
											III M.Tech CSE, CSE-IS (Dept.Lab)		Instructor
											CS753-Practical Training(2)	Prof.K Chandrasekeran	
											CS754-Major Project(4)		
											CS853-Practical Training(2)	Dr. Alwyn R.Pais	
											CS854-Major Project(4)		

Time Table-Incharge

Head of the Department

Department of Computer Science & Engineering, N.I.T.K- Surathkal
B. Tech & M. Tech Laboratory Timetable ODD Semester July – Dec 2022

Day	UG/PG	Sem	Morning Time			Lab No.	Afternoon Time			Lab No.
			9-10	10-11	11-12		2-3	3-4	4-5	
Mon	UG	III	Data Structures and Algorithms Lab-III B3			501	Data Structures and Algorithms Lab-III B1			501
			Design of Digital Systems Lab-III B4			202	Design of Digital Systems Lab- III B2			202
		V	Software Engineering Lab-V B4			311	Computer Networks Lab- V B2			401
			Computer Networks Lab- V B3			401	Software Engineering Lab- V B1			311
	PG	CSE				302	Computing Lab			302
		CSE-IS	Algorithm and Complexity			211				211
Tue	UG	III	Data Structures and Algorithms Lab- III B4			501	Data Structures and Algorithms Lab- III B2			501
			Design of Digital Systems Lab- III B3			202	Design of Digital Systems Lab- III B1			202
		V	Software Engineering Lab- V B3			311	Computer Networks Lab- V B1			401
			Computer Networks Lab- V B4			401	Software Engineering Lab- V B2			311
	PG	CSE				302	Algorithm and Complexity			302
		CSE-IS	Number Theory and Cryptography			211				211
Wed	UG	III				202				202
			Design of Digital Systems Lab-III B4			501				501
						401	Design of Digital Systems Lab- III B2			401
		V	Compiler Design Lab- V B3			311	Compiler Design Lab- V B1			311
	PG	CSE				302	Seminar			302
		CSE-IS	Computing Lab			211				211
Thu	UG	III				202				202
						401	Design of Digital Systems Lab- III B1			401
			Design of Digital Systems Lab- III B3			501				501
		V	Compiler Design Lab- V B4			311	Compiler Design Lab- V B2			311
	PG	CSE				302				302
		CSE-IS	Seminar			211				211
Fri	UG					202				202
						311				311
						501				501
		V				401	High Performance Computing			401
	PG	CSE				302	High Performance Computing			302
		CSE-IS				211				211

Note: III B1- 211CS101- 211CS132, 191559, 191CS243 III B2- 211CS133- 211CS166 III B3 - 211CS201- 211CS232, 191CS251 III B4- 211CS233- 211CS265
V B1- 201CS101- 201CS136(35) V B2- 201CS137- 201CS170, 201CS269 V B3- 201CS201- 201CS234, 191CS248 V B4- 201CS235- 201CS270

Please refer to the Signboard in the entrance of the department for exact location of the laboratories.

H.O.D
14/7/22