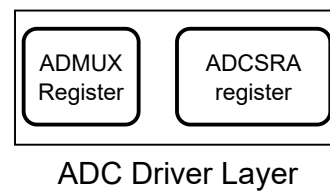
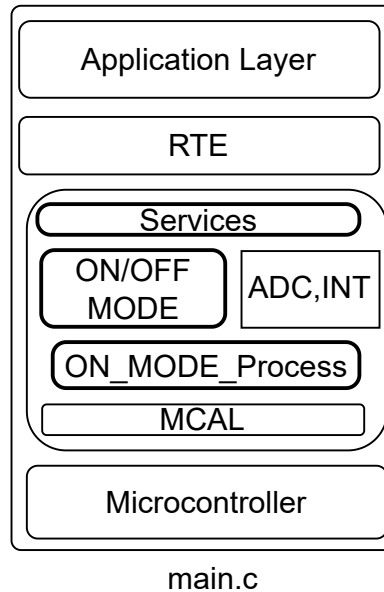
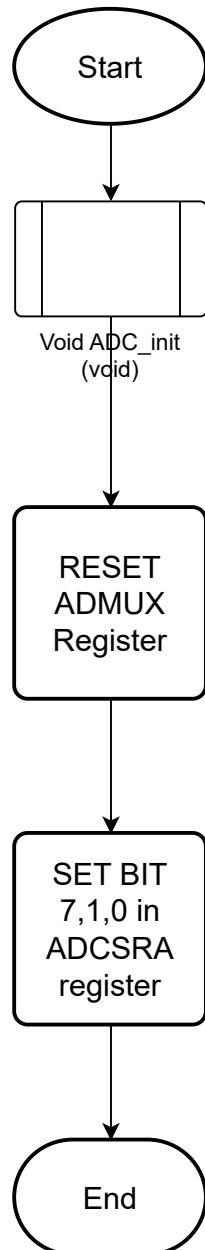


Initializtion Analog Digital coverter Driver



ADC Driver Layer of project .
Each Layer depend on many layer
"Top_down approach".
Designed by:Eng/Kirollos Gerges Asaad
Presented to:AMIT
Graduation Project of Gp Nasr53