Assignment 10

	1. Pre-Order Traversay Output			
_	>VLR 50 10 8 5 9 30 26 31 150 100 79 126 152 151 180			
	2. In-Order Traversal output			
	-> LVR 5 8 9 10 26 30 31 50 79 100 126 150 151 152 180			
	3.	Post-Order Traversal Output		
	-> L (2v 59826313010791	26 100 151 180 152 150 50	
	4) in Order Travers ay (6); // poot is 6			
		in Order Traversol (null)		
-		inOrderTraversal(null)	Console	
72	76	in Order Traverson (10)	13466910	
		in Order Traversay (null)		
		in Order Thaverson (null)		
み	76	in Order Traversay (6)		
72	36	in OrderTraversal (9)		
		in Order Troversay (Null)		
		in Order Traversal (null)		
72	76	in Order Traversay (4)		
		in Order Traversay (null)		
		in Order Traversal (null)	Ř. C.	
72	76	in Order Troversal (1)		
72	76	in Order Traversoy (3)		
72	70	inOrderTroversai (6) Main()		
	-	marce		