



دانشگاه صنعتی امیر کبیر
(پلی تکنیک تهران)

Electrical and Electronic Circuits

chapter 5. Capacitor-Inductor

Afarghadan@aut.ac.ir

عظیم فرقدان 

مهر ۱۴۰۳

Objectives of the Lecture

- Capacitor and inductor
- Voltage and current characteristics
- Energy
- Series and parallel connection
- duality

The Capacitor

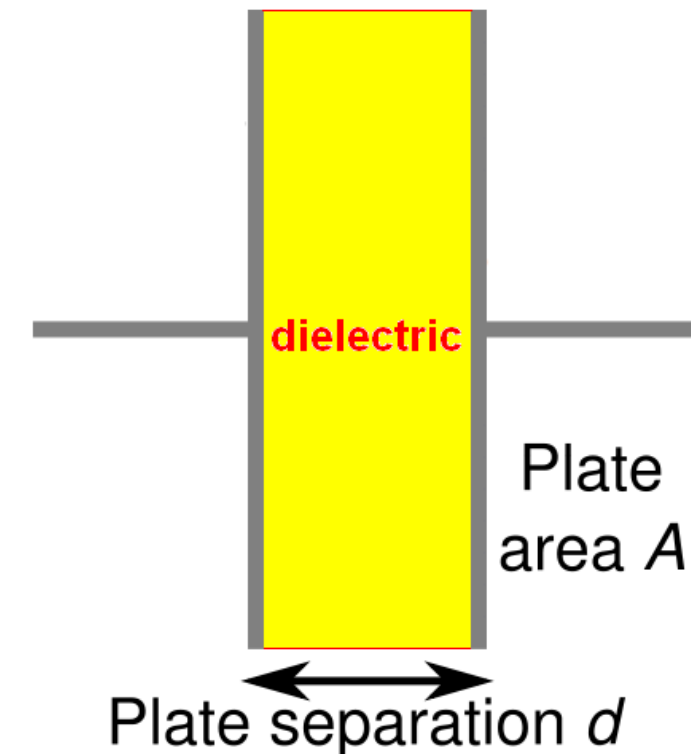
- Composed of two conductive plates separated by an insulator (or dielectric).
 - Commonly illustrated as two parallel metal plates separated by a distance, d .

$$C = \epsilon A/d$$

where $\epsilon = \epsilon_r \epsilon_0$

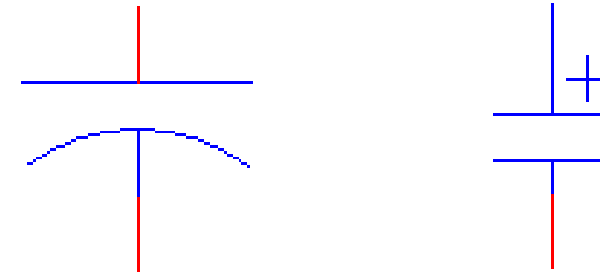
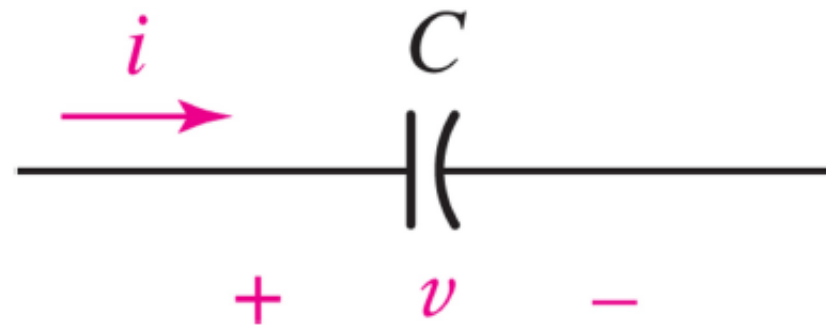
ϵ_r is the relative dielectric constant

ϵ_0 is the vacuum permittivity



The Capacitor

- The ideal capacitor is a **passive element** with circuit symbol:



- We define capacitance C by the voltage-current relationship:

$$i_C = \frac{dq}{dt} \quad q = Cv_C$$

$$i = C \frac{dv}{dt}$$

$$v(t) = \frac{1}{C} \int_{t_0}^t i(t') dt' + v(t_0)$$

- The unit of capacitance is **Farad** (F).

The Capacitor

- Ceramic capacitors (typically 1 picofarad to 1 microfarad)
- Electrolytic capacitors (typically 1 microfarad to 10 millifarads)
- Supercapacitor (1 to 100 Farads)



Capacitor power:

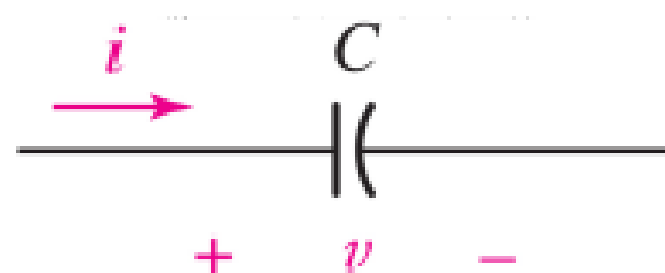
$$p(t) = i(t)v(t) = \left(C \frac{dv}{dt} \right) v$$

Energy stored in capacitor with voltage v :

$$E = \int p dt = \int C v dv = \frac{1}{2} C v^2$$

Capacitor behavior

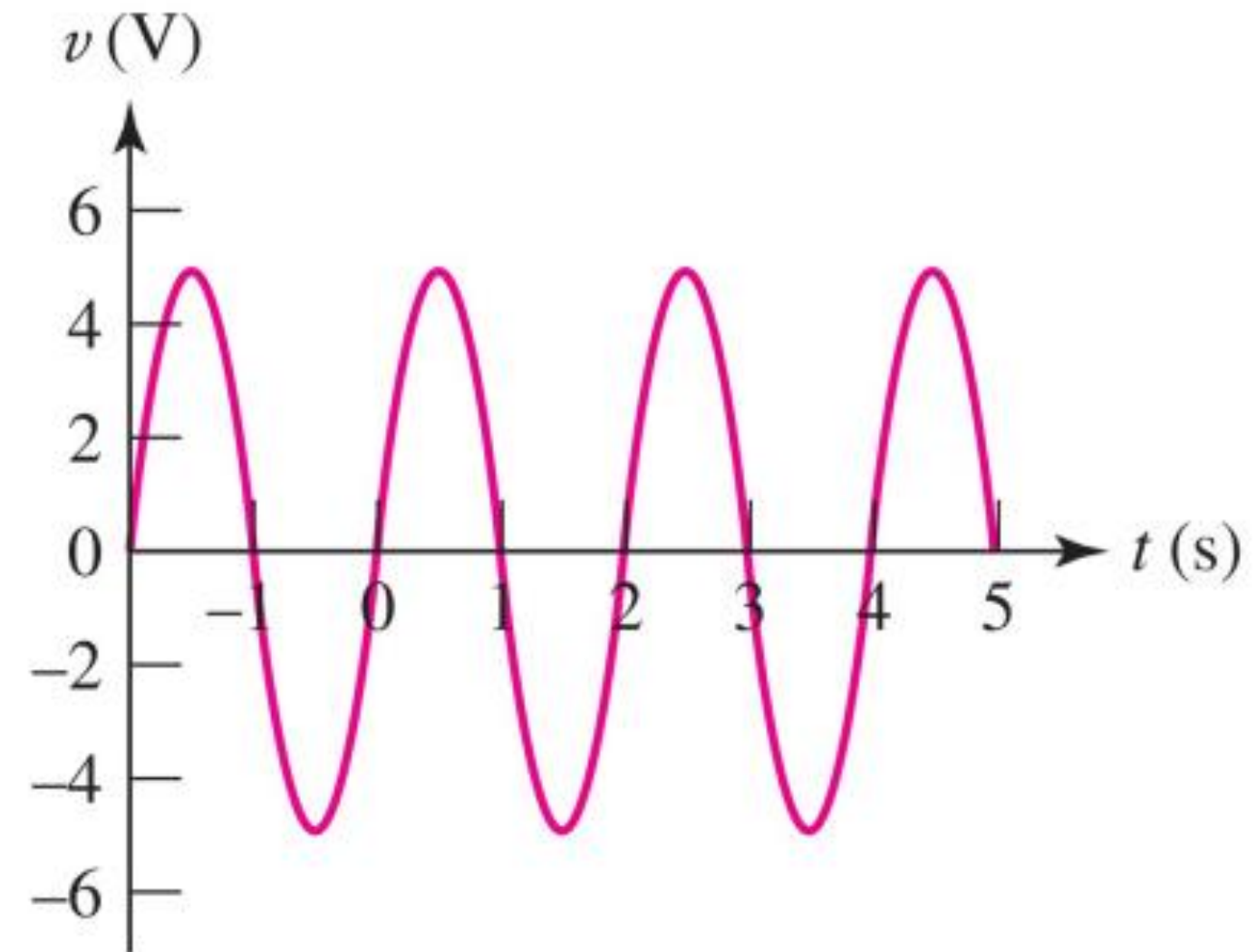
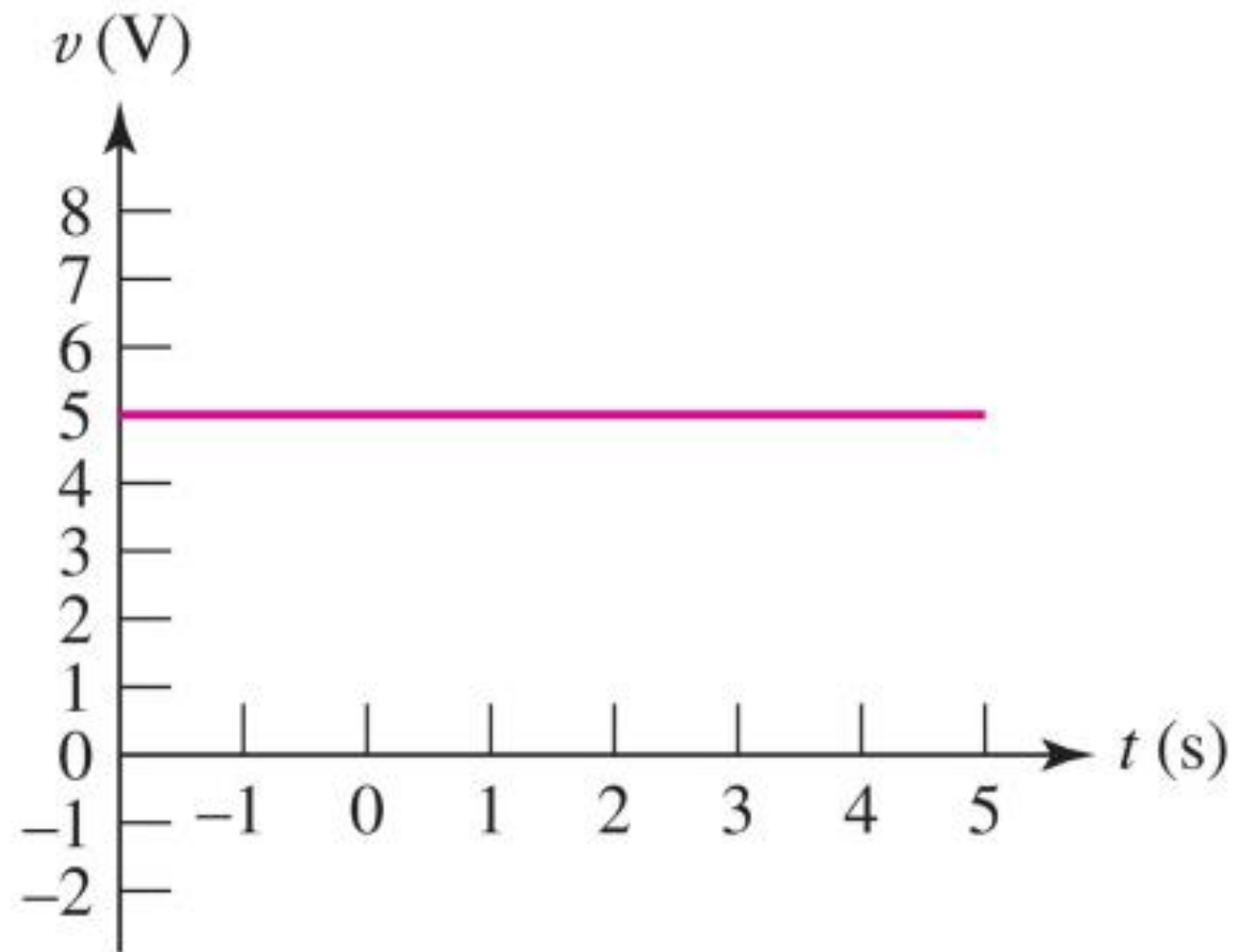
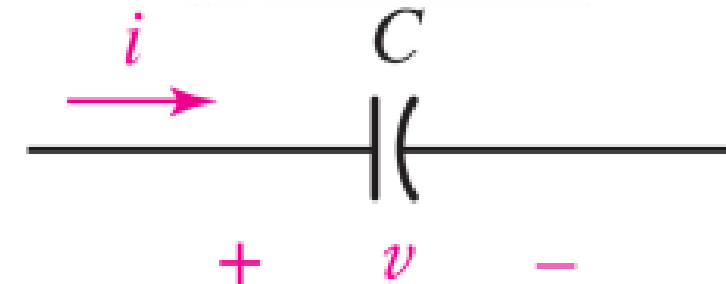
- ✓ Capacitors are **open circuits** to dc voltages. Why?
- ✓ the voltage on a capacitor **cannot jump**. Why?
- ✓ If we take the direction of current and voltage according to the convention below, if P is positive, the capacitor is **storing energy** and if it is negative, it is **delivering energy**.



$$i = C \frac{dv}{dt}$$

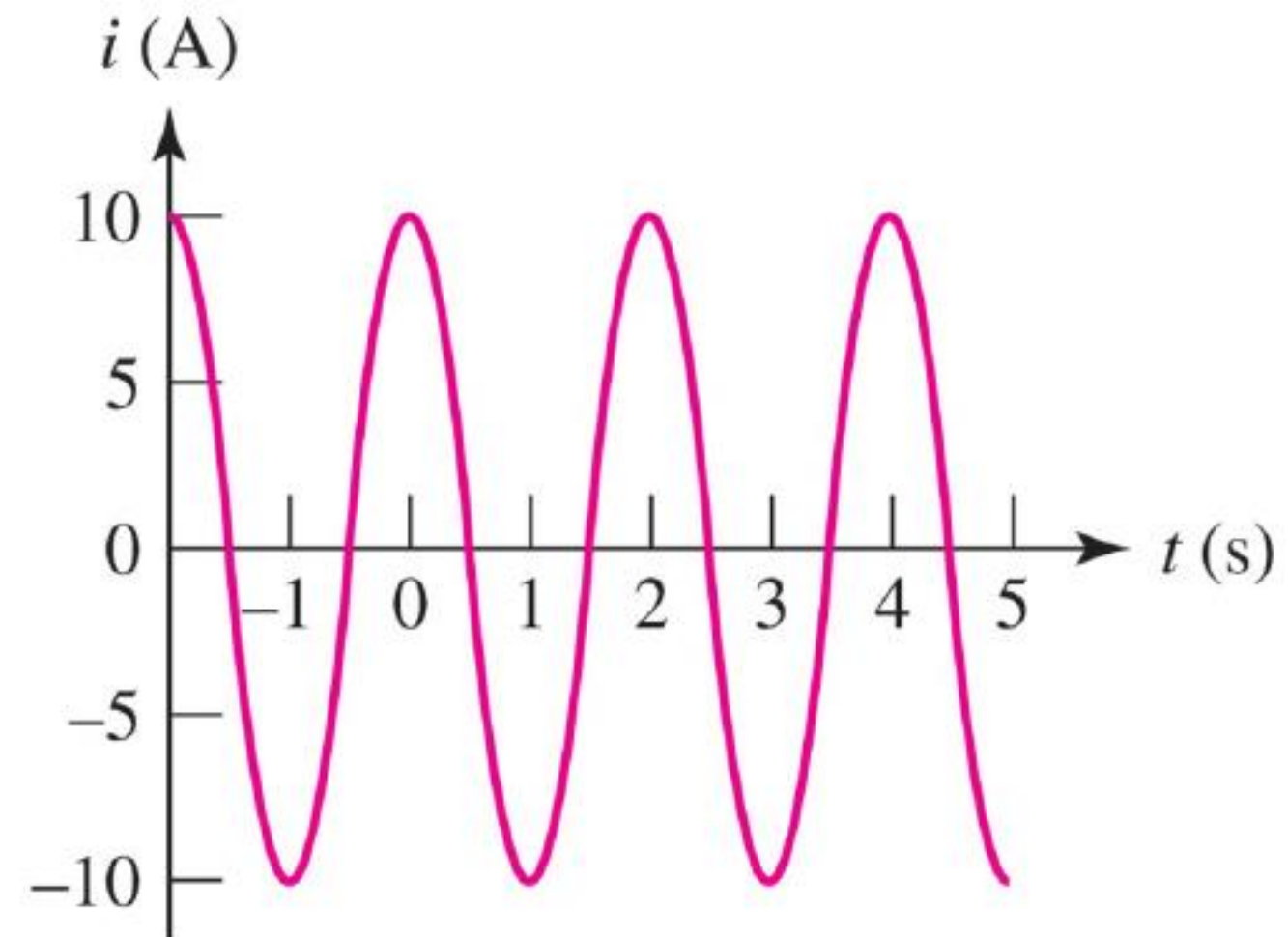
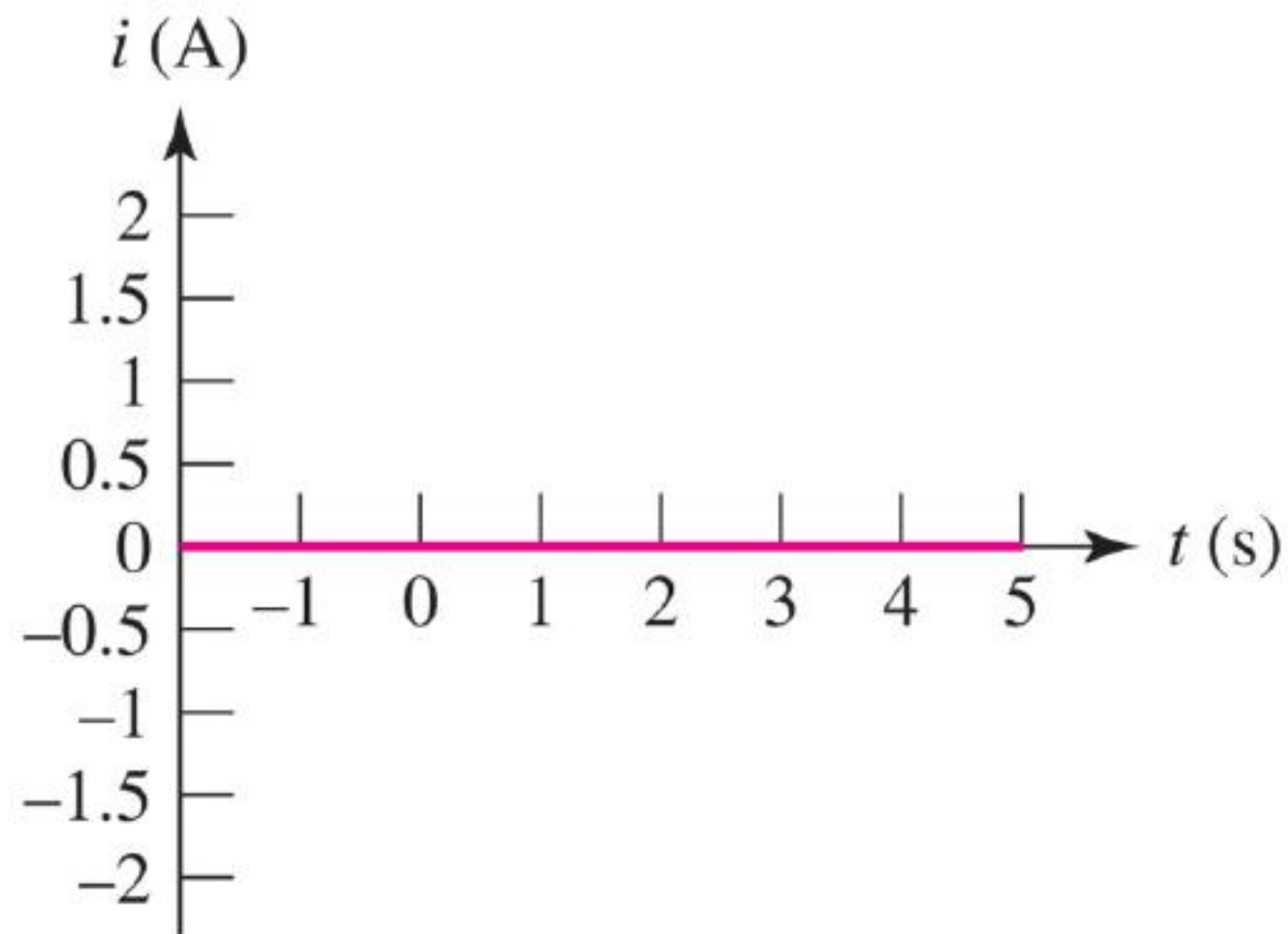
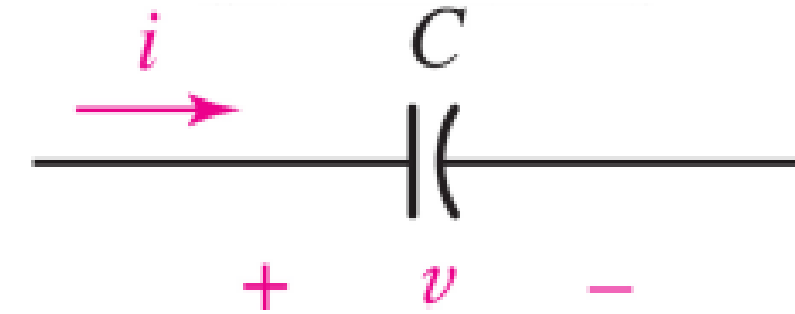
Capacitor voltage-current characteristic: example 1

Find $i(t)$ for the voltages shown, if $C=2$ F.

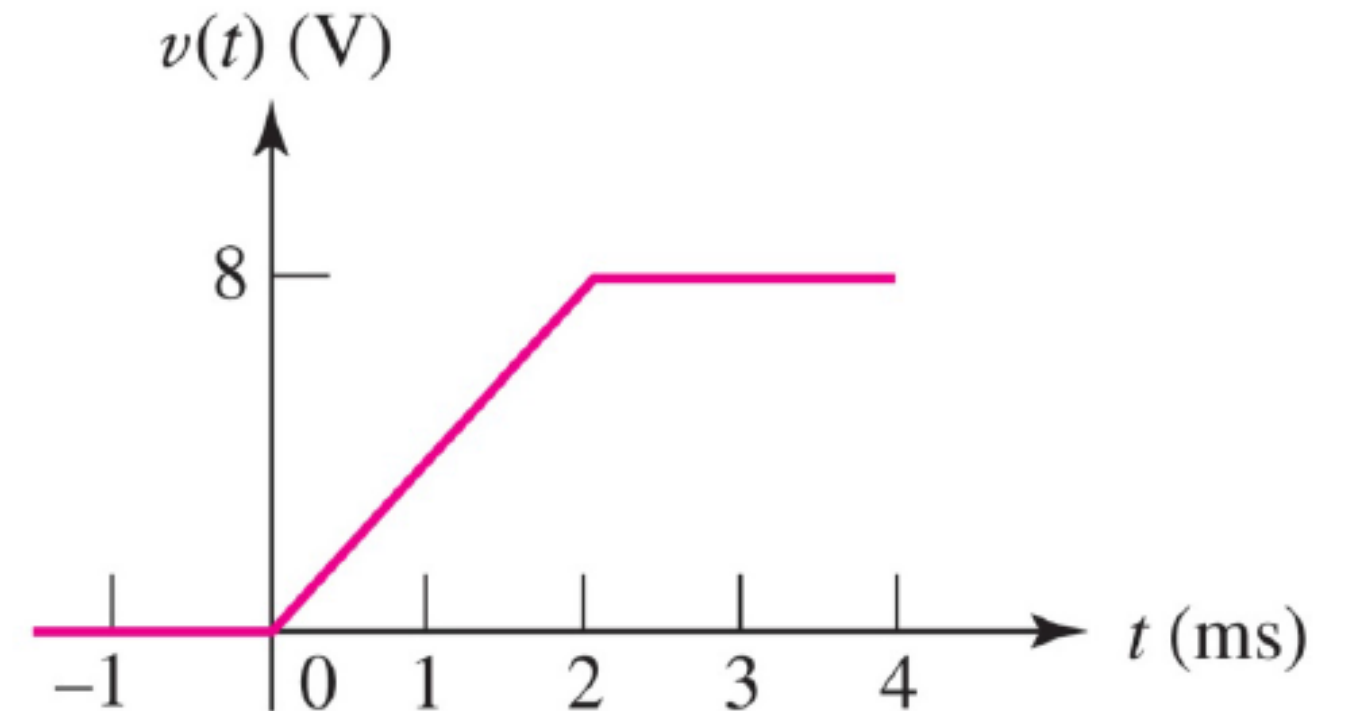
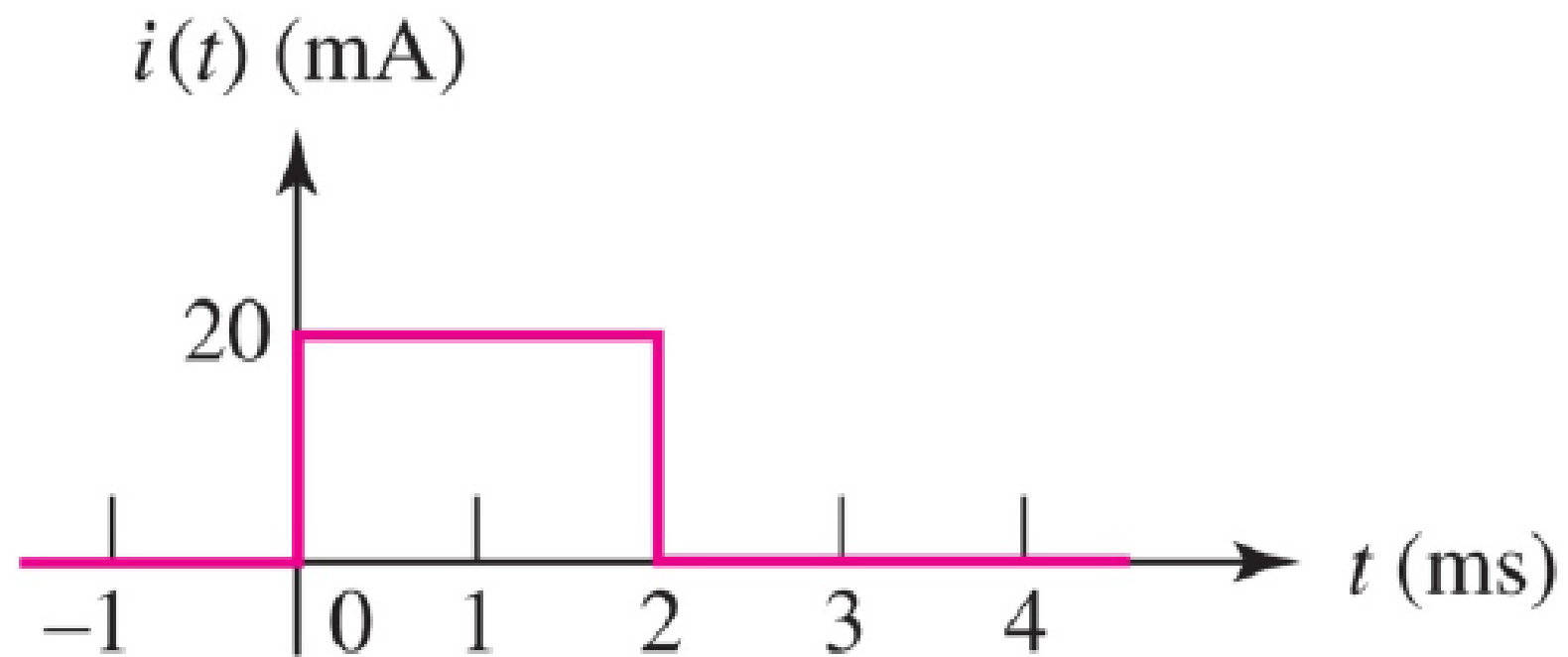


Capacitor voltage-current characteristic: example 1

Solution: apply $i(t)=2dv/dt$ and graph:



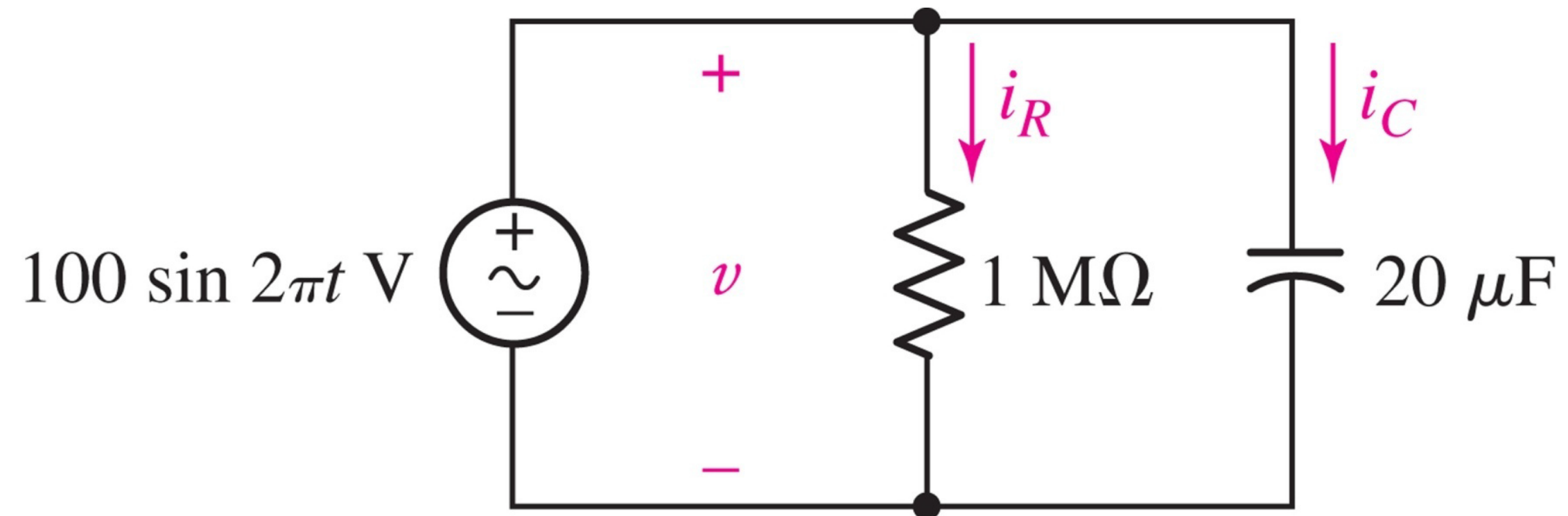
Show that the following graphs are matching voltage and current graphs for a capacitor of $C=5\mu\text{F}$.



$$v(t) = \frac{1}{C} \int_{t_0}^t i(t') dt' + v(t_0)$$

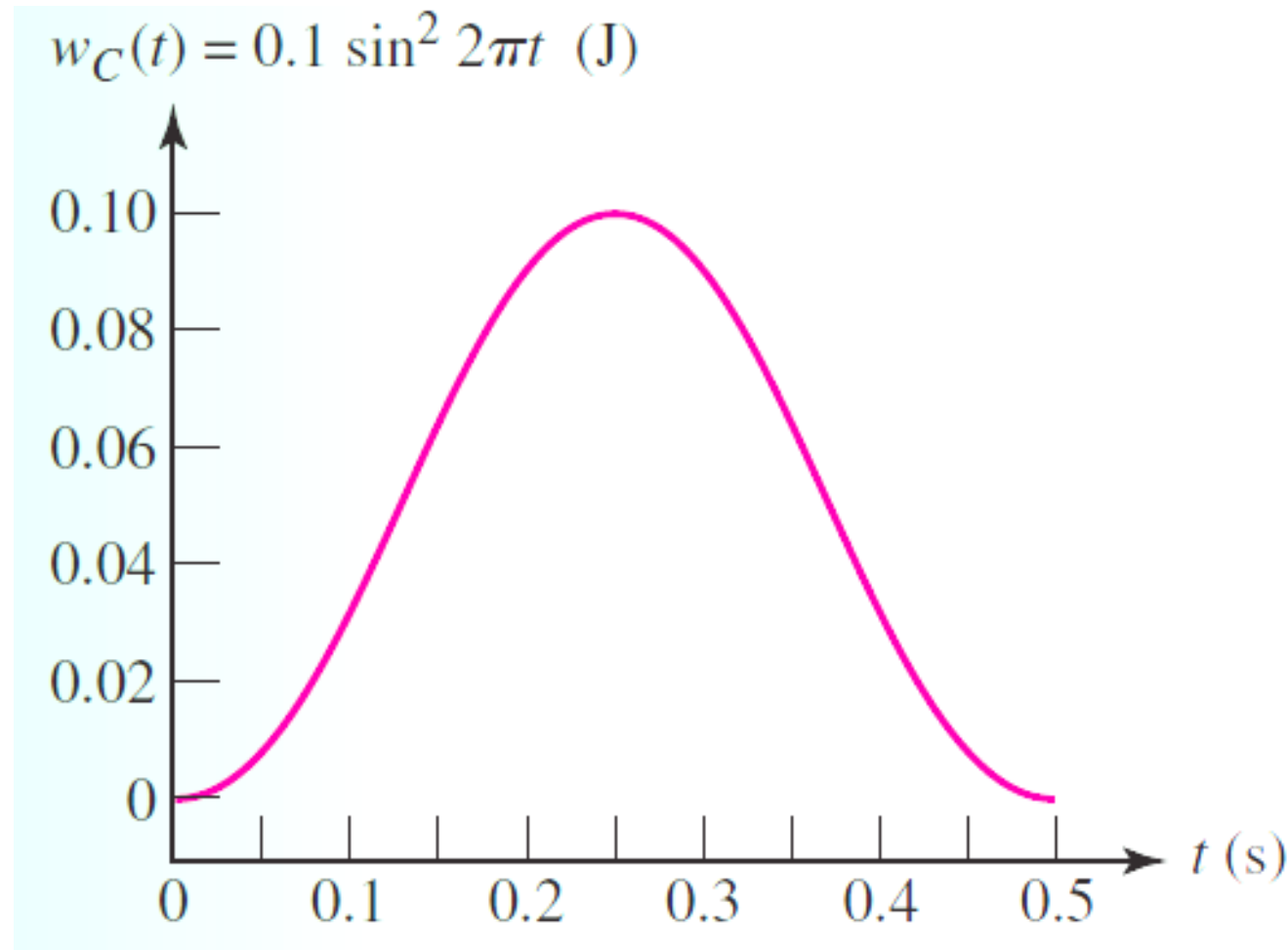
Example: Capacitive energy

Determine the maximum energy stored in the capacitor, and plot i_R and i_C .

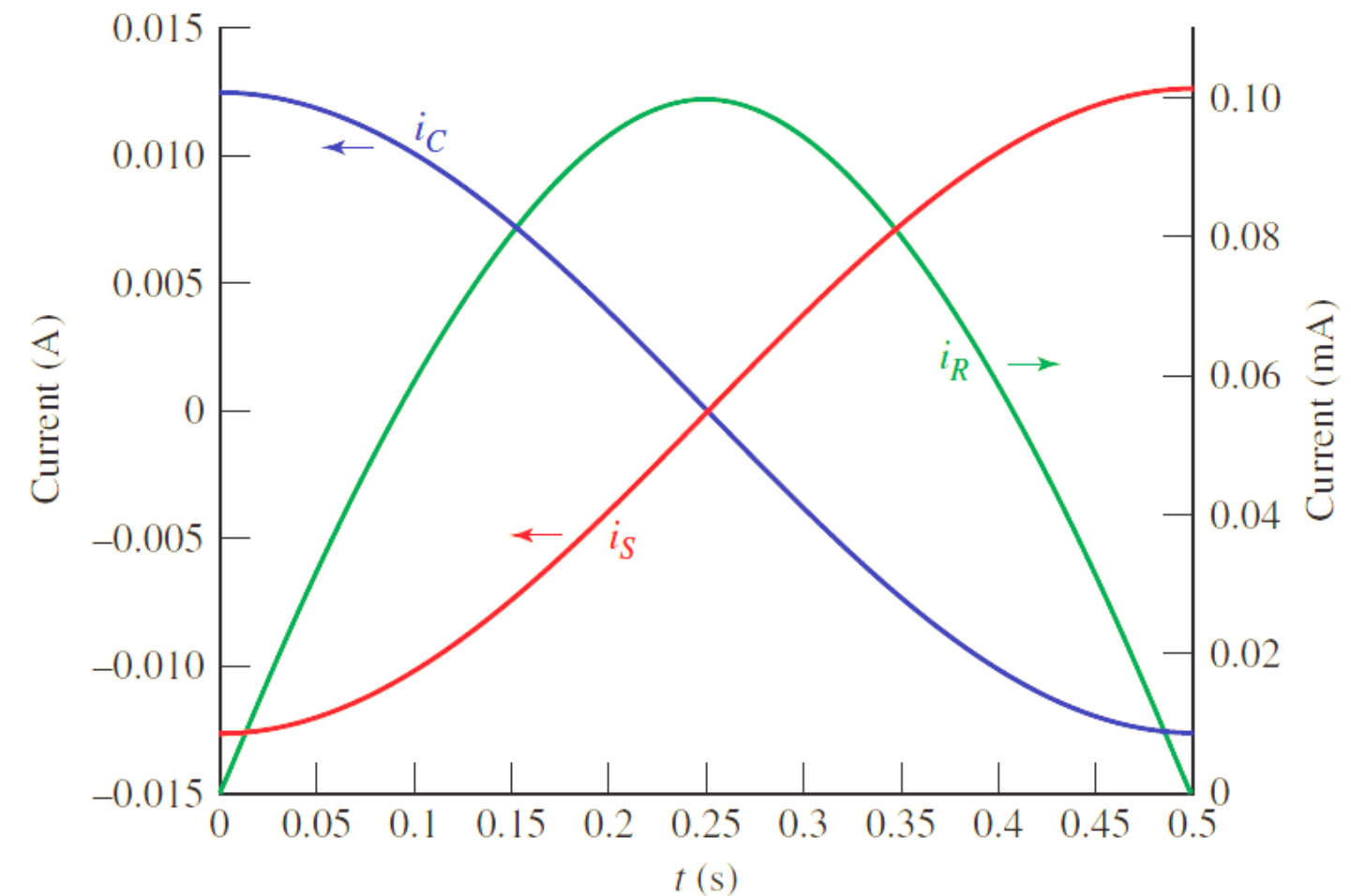


Example: Capacitive energy

Determine the maximum energy stored in the capacitor, and plot i_R and i_C .

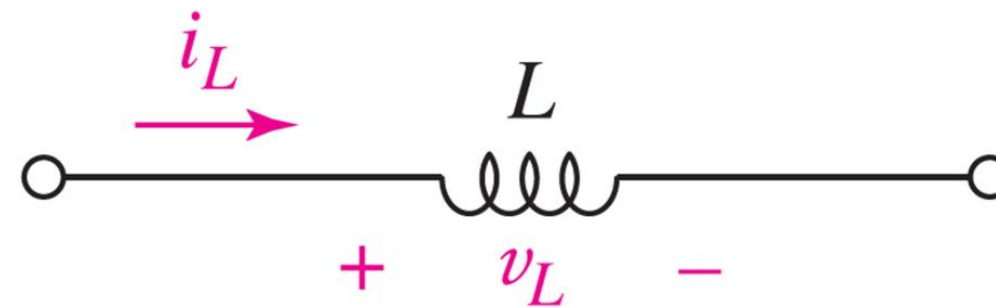


$$i_C = 20 \times 10^{-6} \frac{dv}{dt} = 0.004\pi \cos 2\pi t$$
$$i_R = \frac{v}{R} = 10^{-4} \sin 2\pi t$$



The inductor

- The ideal inductor is a **passive element** with circuit symbol



- The **current-voltage** relation is

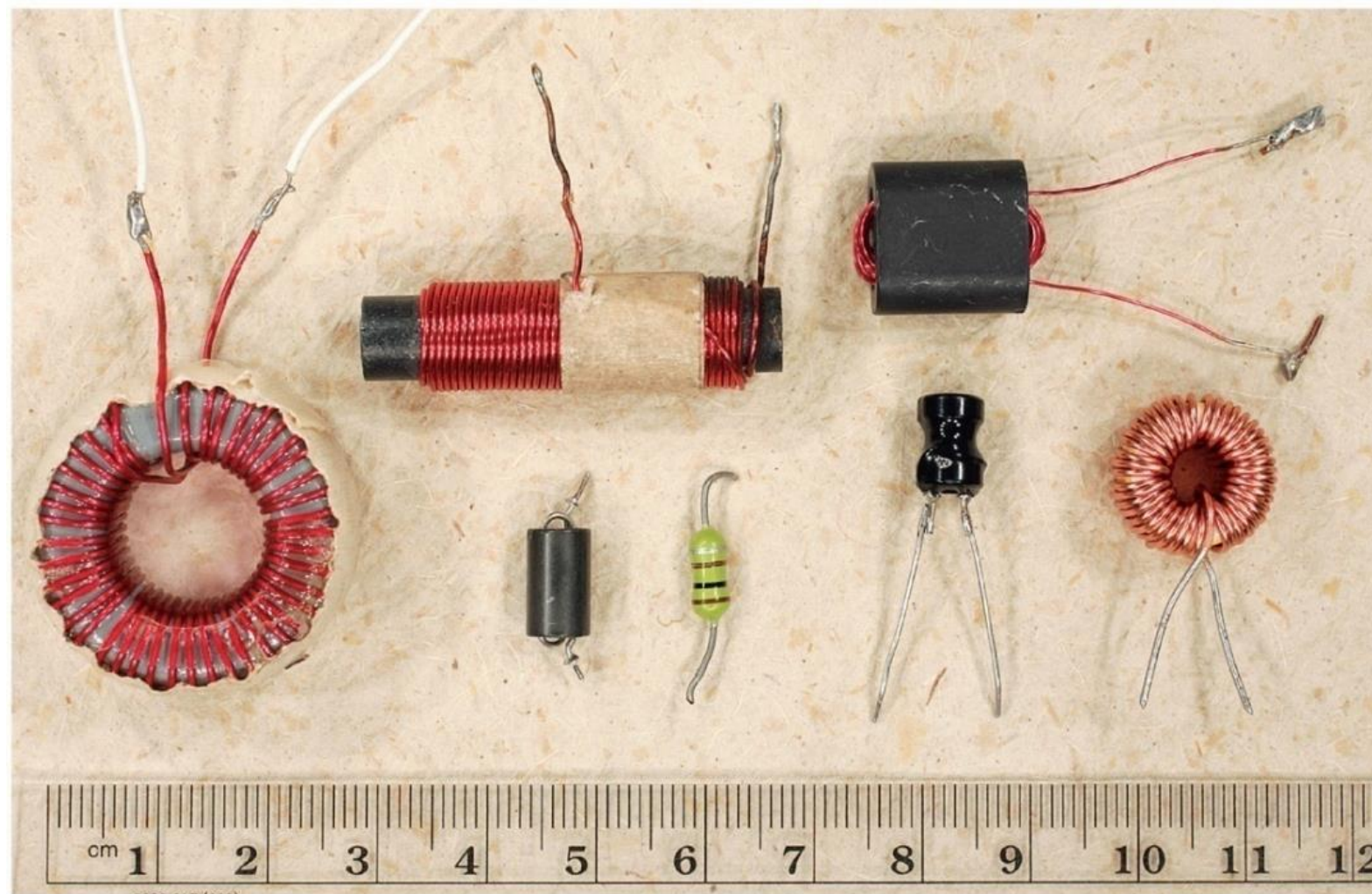
$$v = L \frac{di}{dt}$$

$$i(t) = \frac{1}{L} \int_{t_0}^t v dt' + i(t_0)$$

- The unit of inductance L is **henry** (H)

Some inductors

Inductors can be bulky and typical values range from μH to H



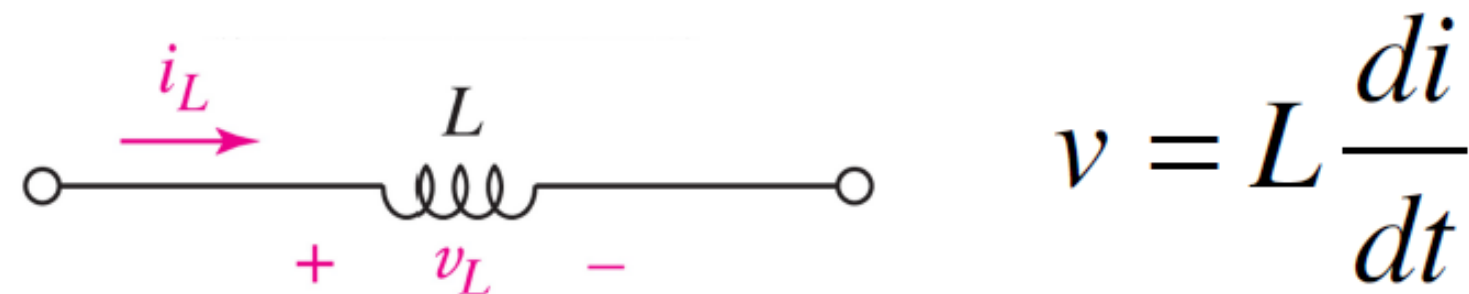
Since:

$$p(t) = i(t)v(t) = i \left(L \frac{di}{dt} \right)$$

then the energy stored in a inductor is:

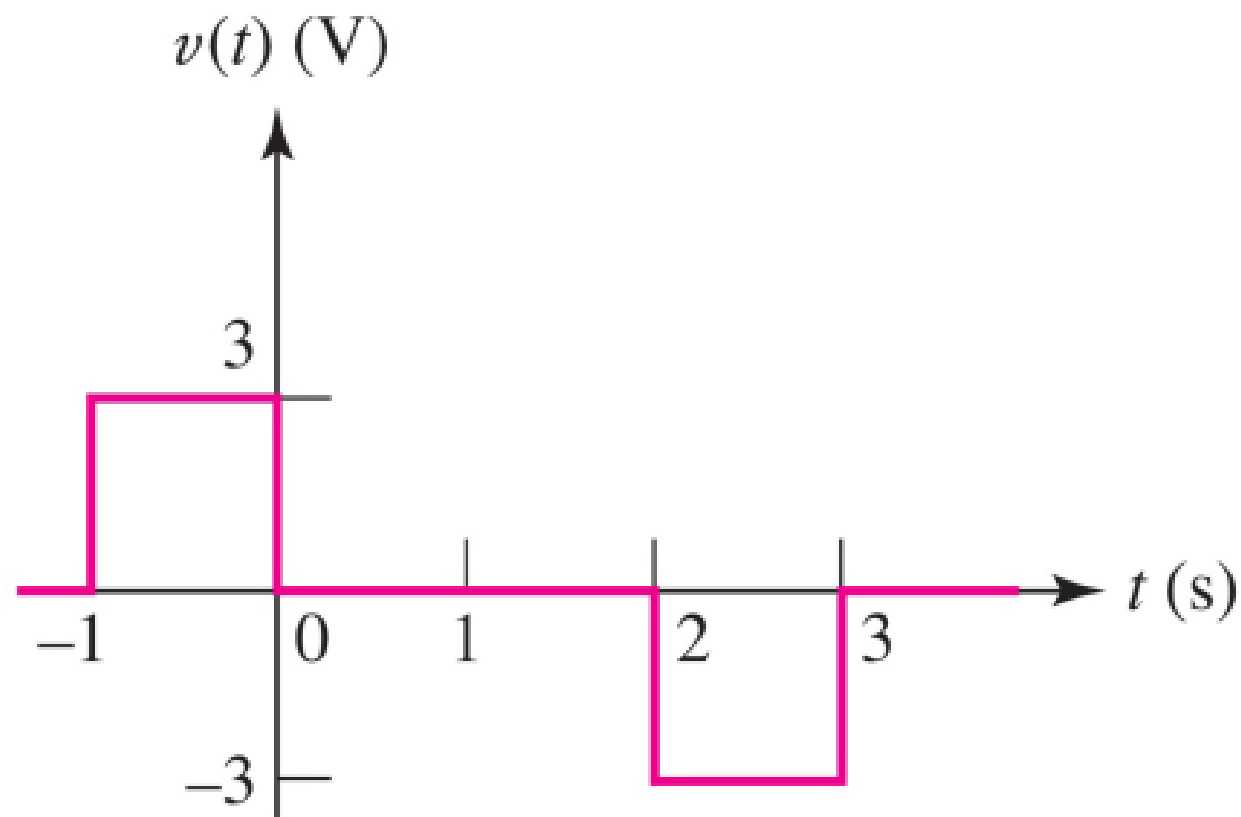
$$E = \int p dt = \int L i di = \frac{1}{2} L i^2$$

- ✓ Inductors are **short circuits** to dc voltages
- ✓ The current through an inductor **cannot jump**
- ✓ If we take the direction of current and voltage according to the convention below, if P is positive, the inductors is **storing energy** and if it is negative, it is **delivering** inductors.

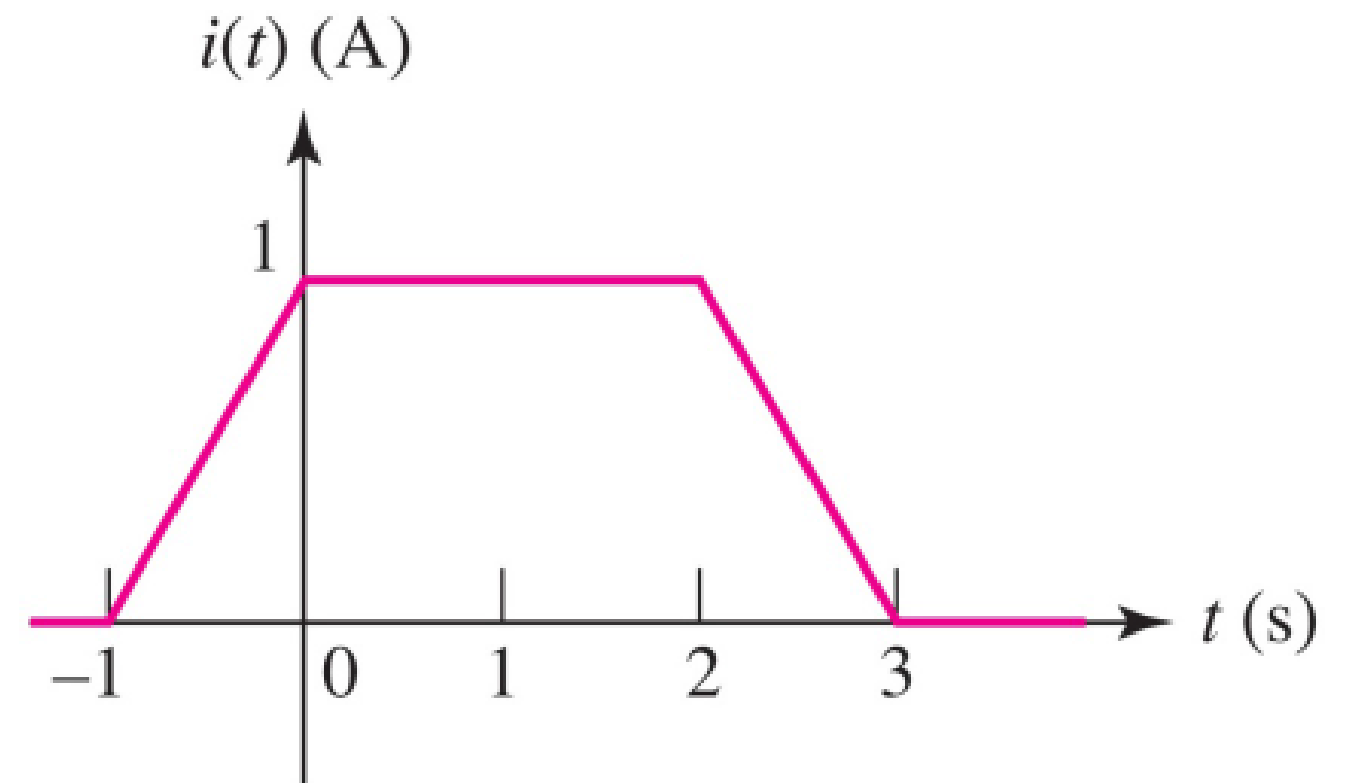


Inductor voltage-current characteristic

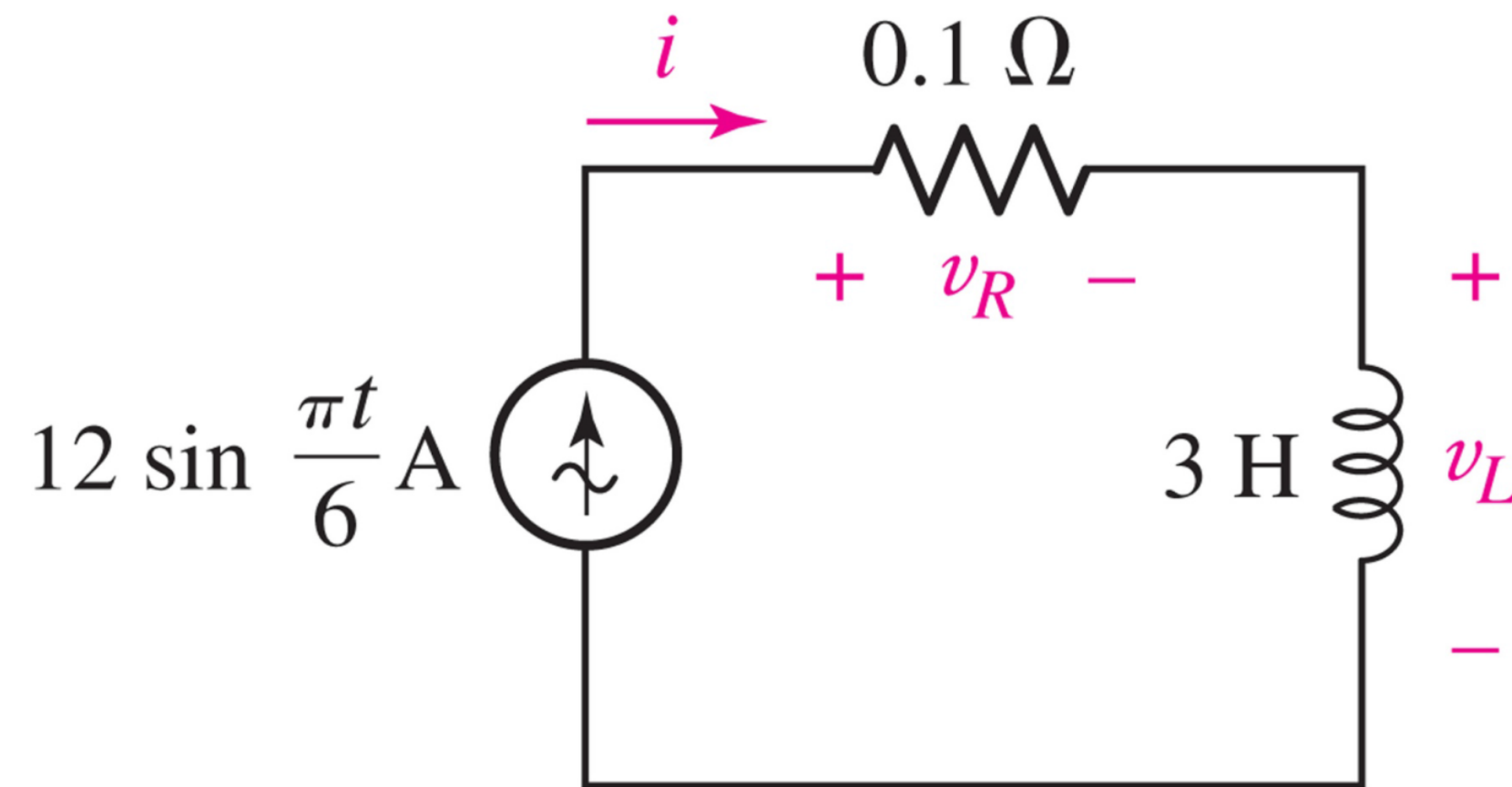
Show that the following graphs are matching voltage and current graphs for an inductor of $L=3$ H.



$$i(t) = \frac{1}{L} \int_{t_0}^t v dt' + i(t_0)$$

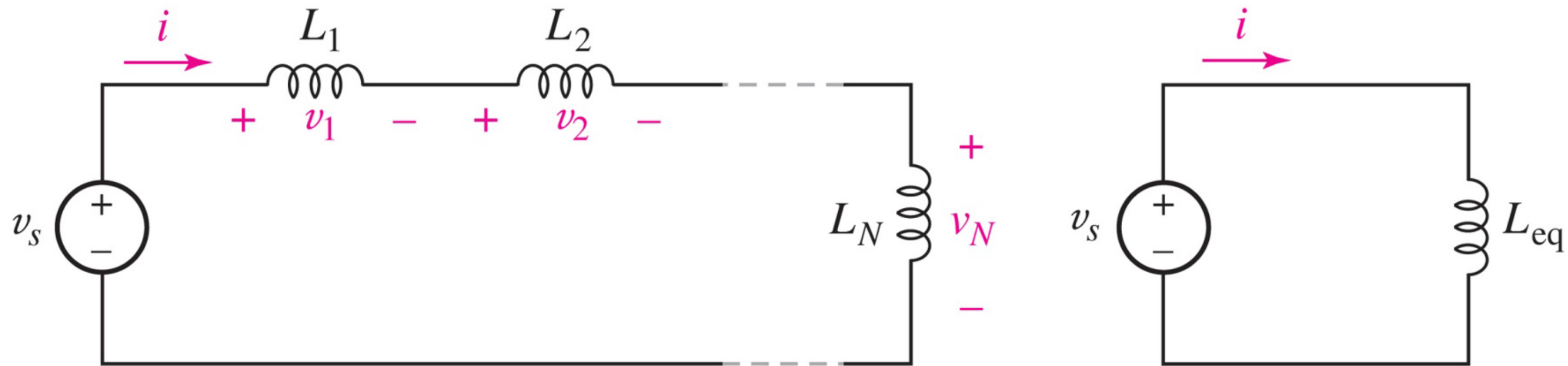


Determine the maximum energy stored in the inductor, and find the energy lost to resistor from $t=0$ to $t=6$ s.



Answer: 216 J, 43.2 J

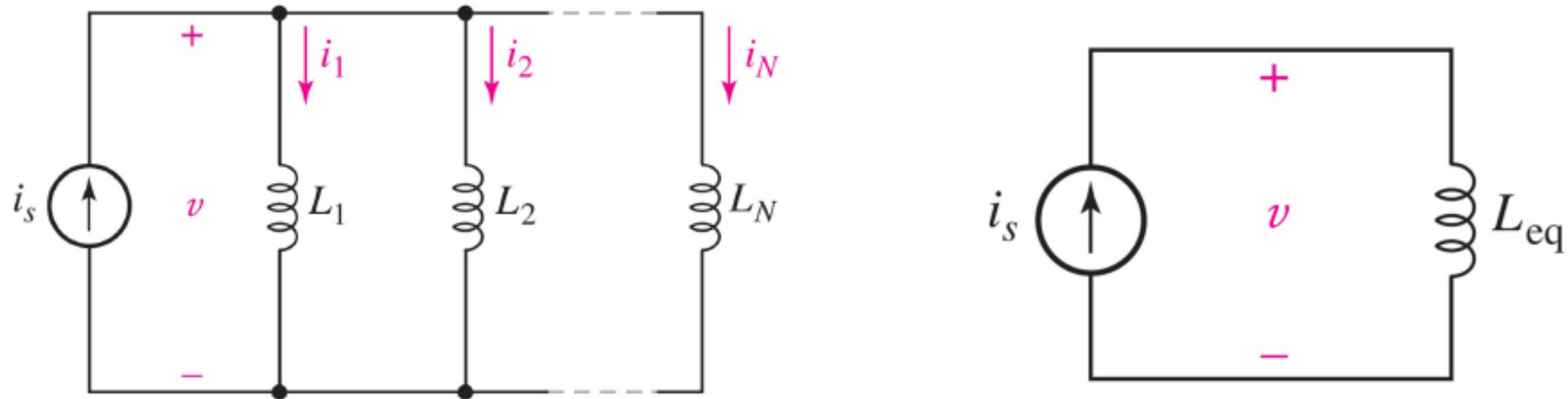
Series connection of inductors



Apply KVL to show:

$$L_{eq} = L_1 + L_2 + \cdots + L_N$$

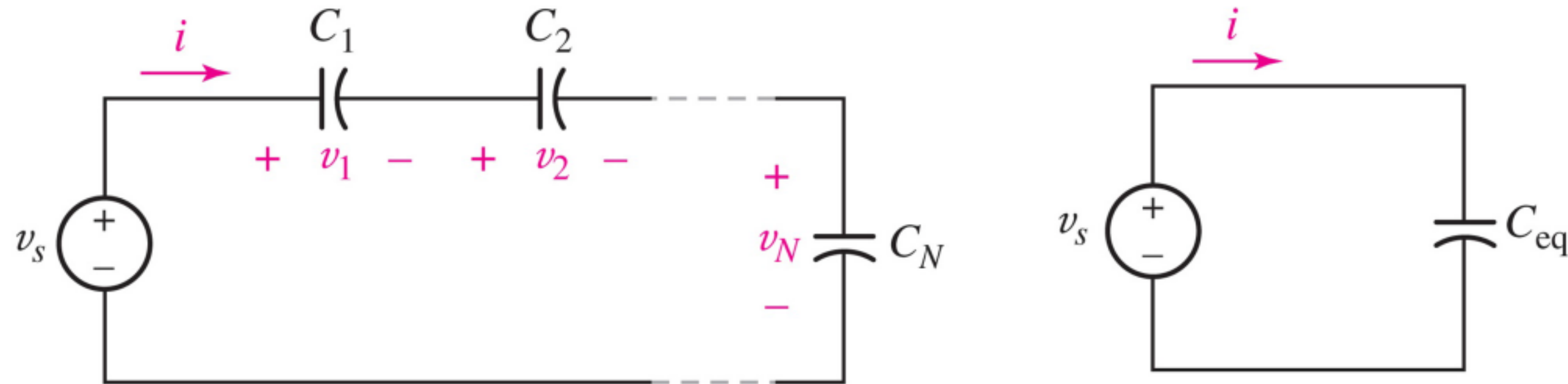
Parallel connection of inductors



Apply KCL to show

$$L_{eq} = \frac{1}{\frac{1}{L_1} + \frac{1}{L_2} + \dots + \frac{1}{L_N}}$$

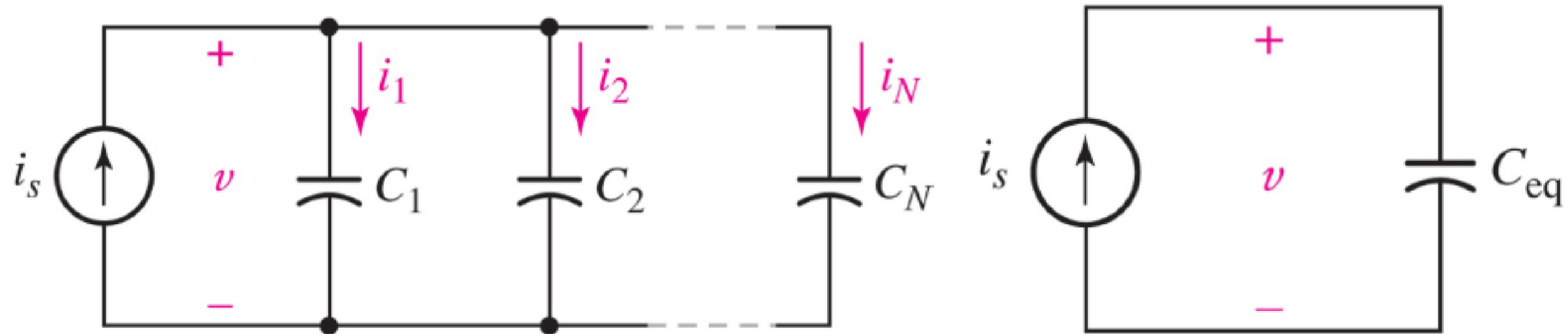
Series connection of capacitors



Apply KVL to show:

$$C_{eq} = \frac{1}{\frac{1}{C_1} + \frac{1}{C_2} + \dots + \frac{1}{C_N}}$$

Parallel connection of capacitors



Apply KCL to show

$$C_{eq} = C_1 + C_2 + \dots + C_N$$

Two capacitors in *series*:

$$C_{eq} = \frac{C_1 C_2}{C_1 + C_2}$$

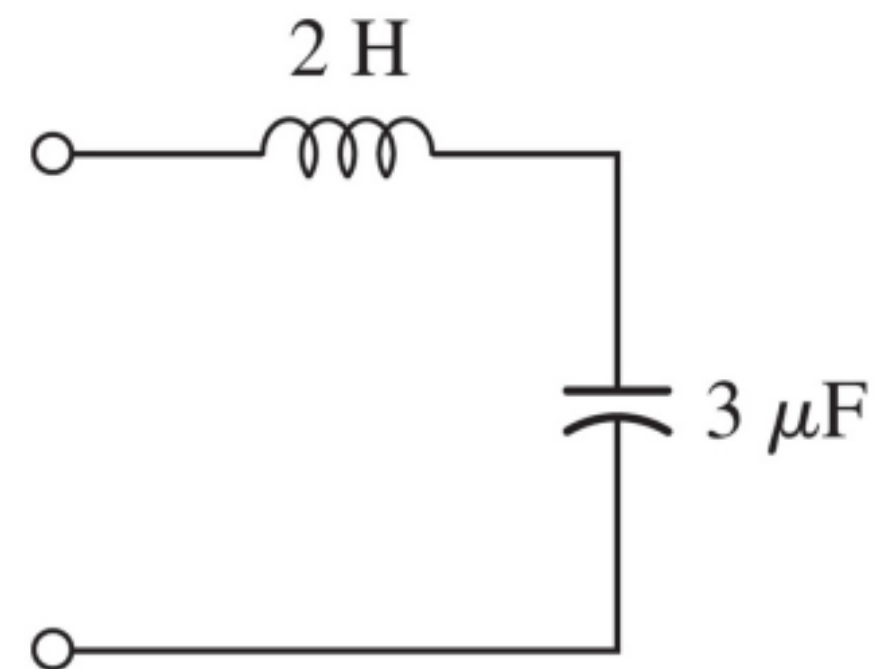
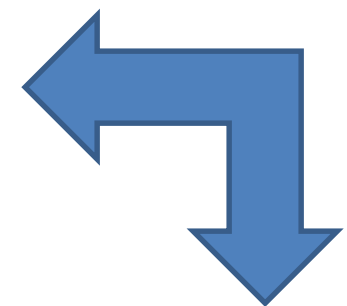
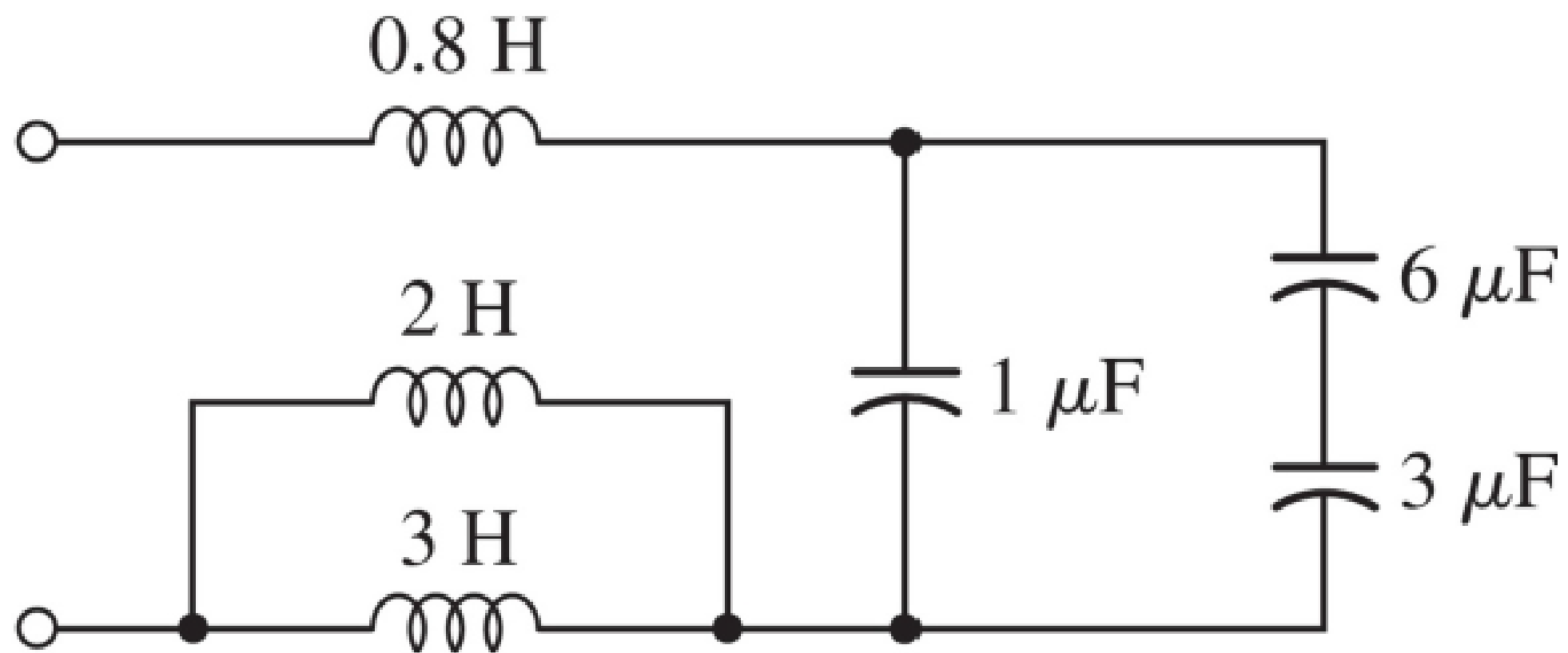
Two inductors in parallel:

$$L_{eq} = \frac{L_1 L_2}{L_1 + L_2}$$

Two resistors in parallel:

$$R_{eq} = \frac{R_1 R_2}{R_1 + R_2}$$

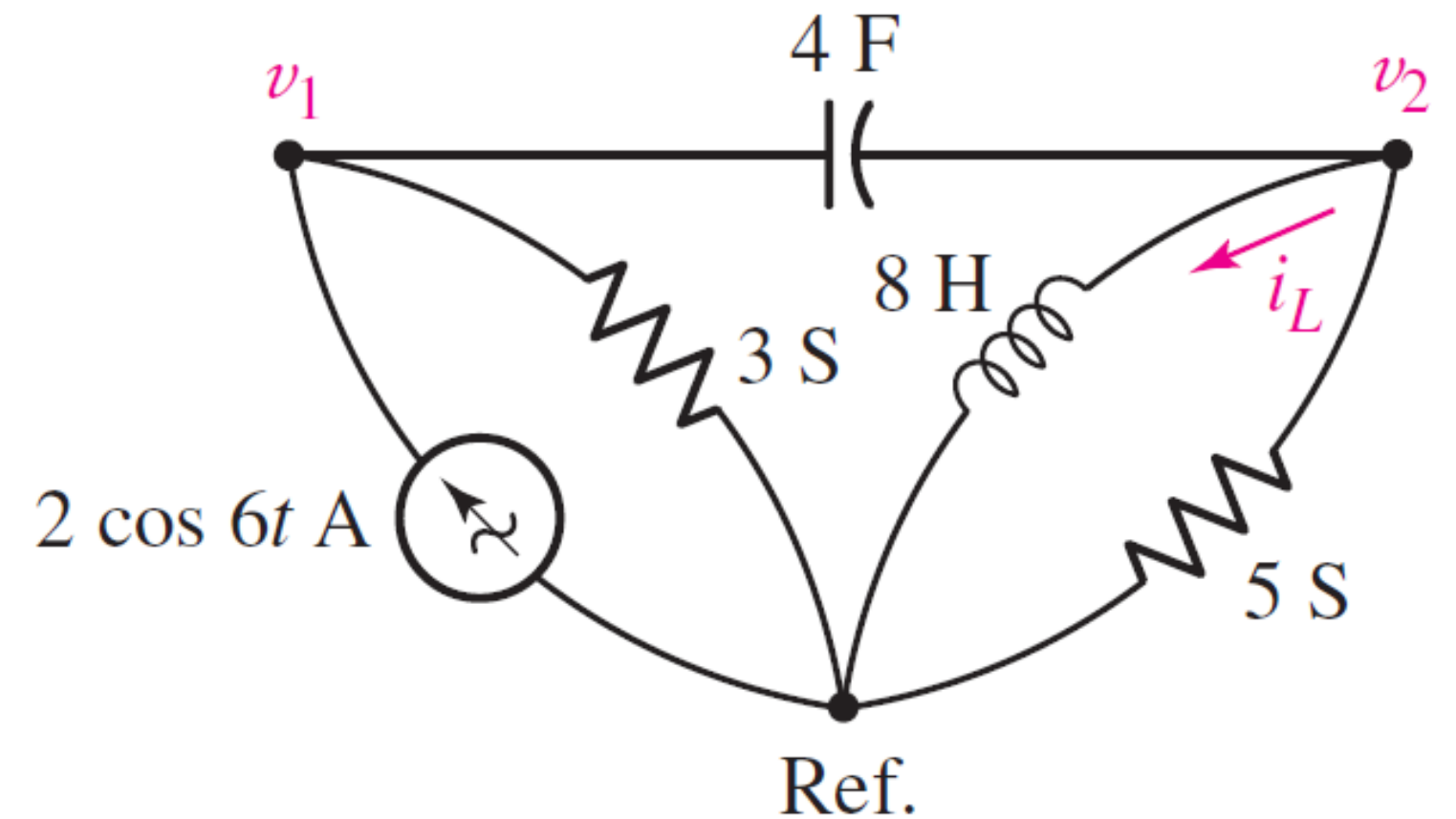
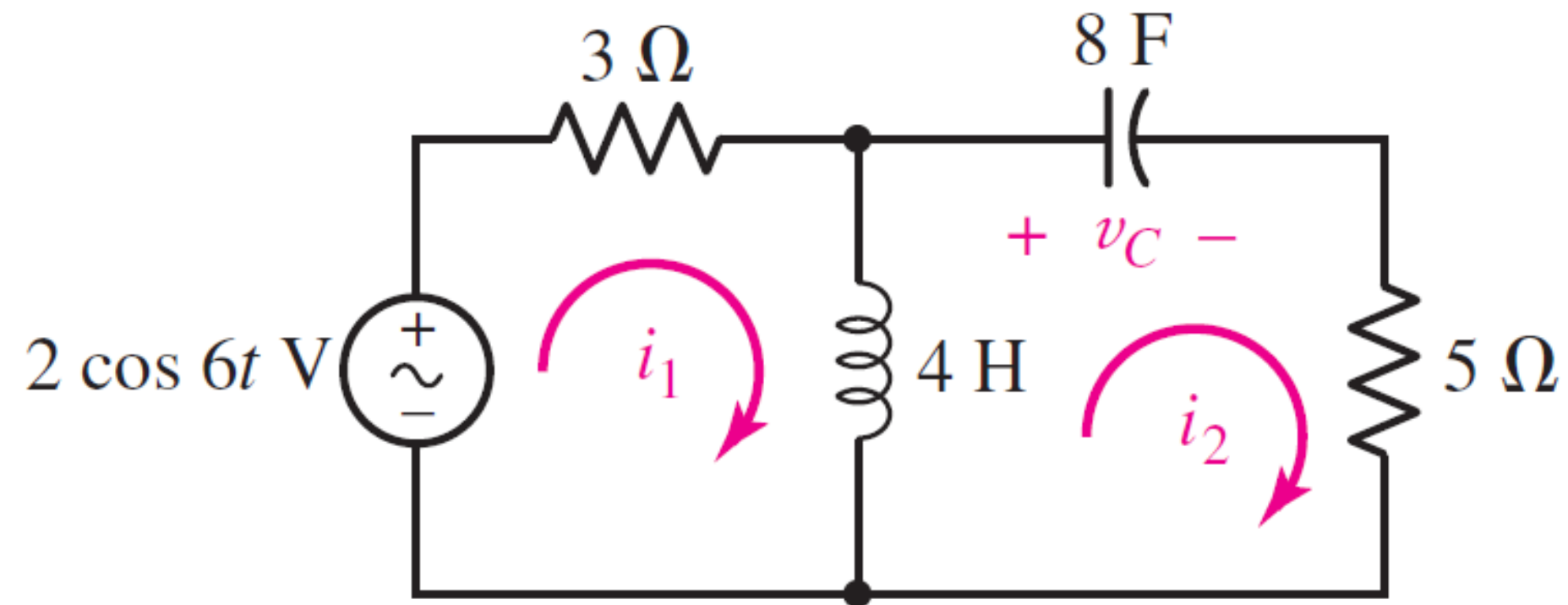
Example: Simplifying LC



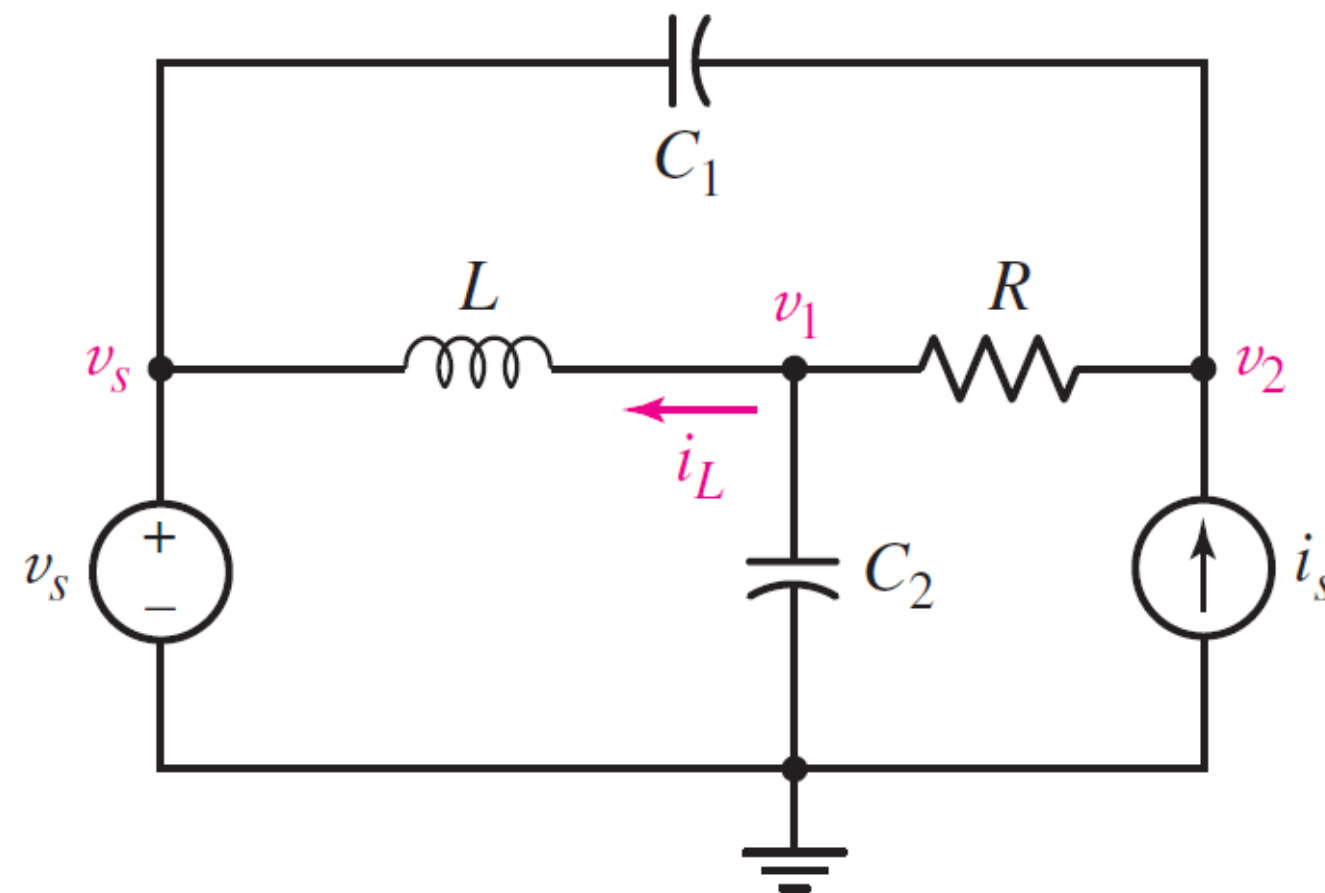
- Two circuits are dual if:
 - The current equations of the first mesh should be equal to the voltage equations of the second node. (and vice versa)

Duality	
R	G
KVL	KCL
C	L
$v(t)$	$i(t)$
$v_s(t)$	$i_s(t)$
$v_C(0)$	$i_L(0)$

Example of duality



Write the nodal equations for the nodes v_1 and v_2 . Also, get the dual of the circuit and write the current equations of its meshes.





Thanks
