

***AIN SHAMS UNIVERSITY FACULTY OF ENGINEERING***

***ECE413 Project 2 – MIPS16 PnR***

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**• Perform placement and routing for MIPS16 RTL (inside VM).**

**• Objective: Optimize both area and frequency as much as possible.**

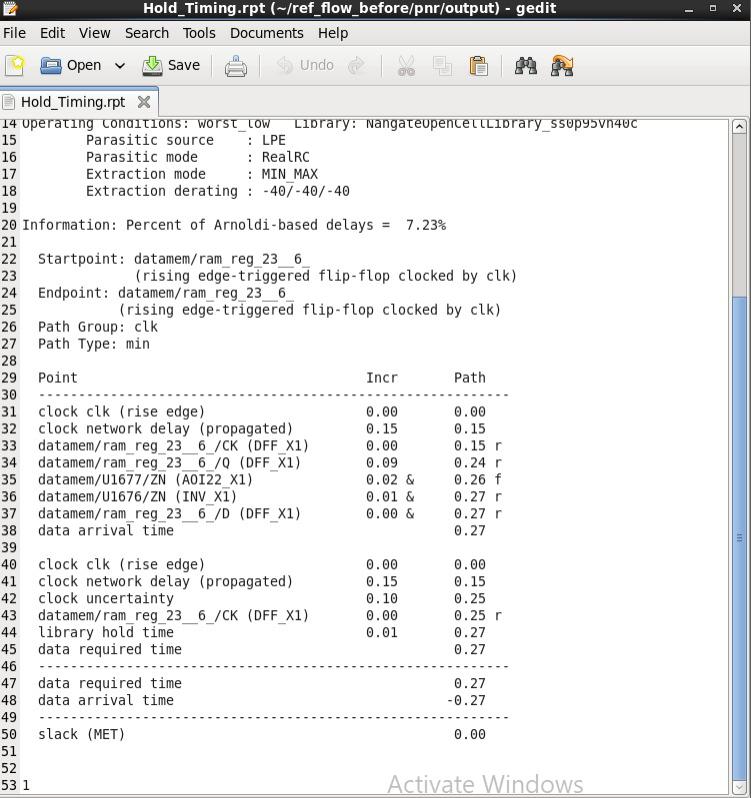
**• Constraints: Floorplan not square, Pins not on M3 & M2 in one line of TCL**

**Command, Do not use compile ultra.**

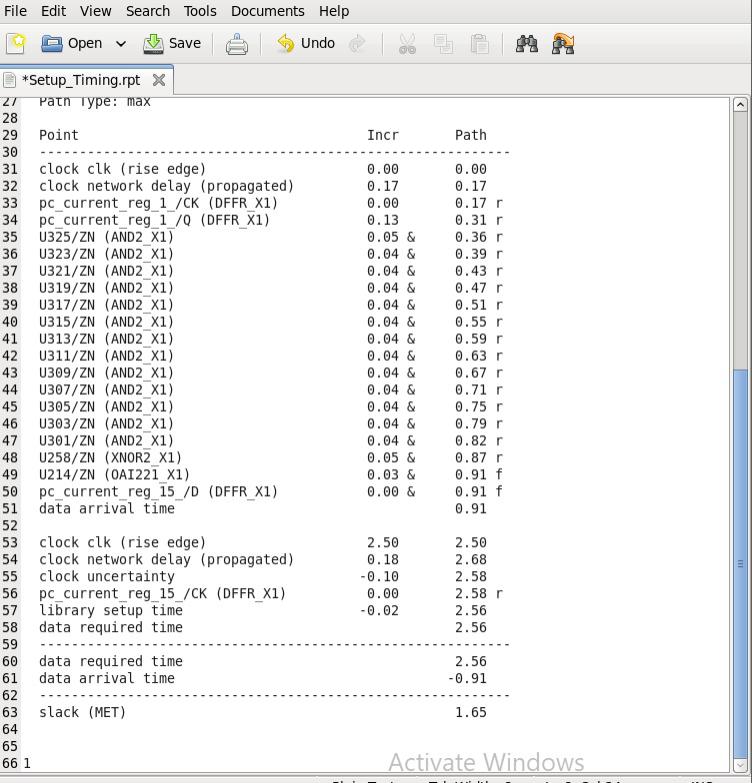
**• Deliverables: Edits done to any scripts highlighted in the flow, Area, Frequency,**

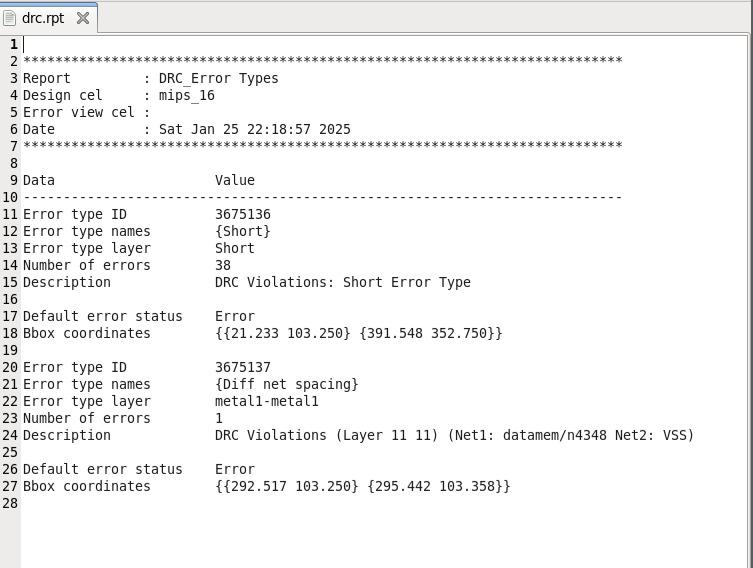
**Timing Report (Setup & Hold), DRC Report in ICC.**

**Hold report**

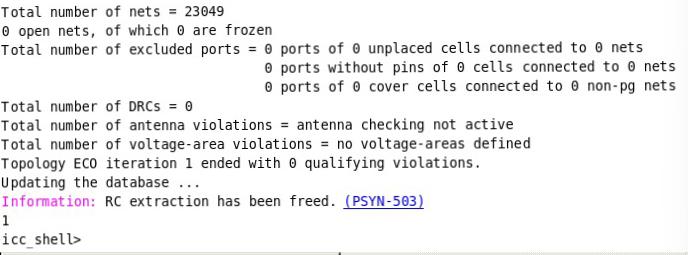


**Setup report**

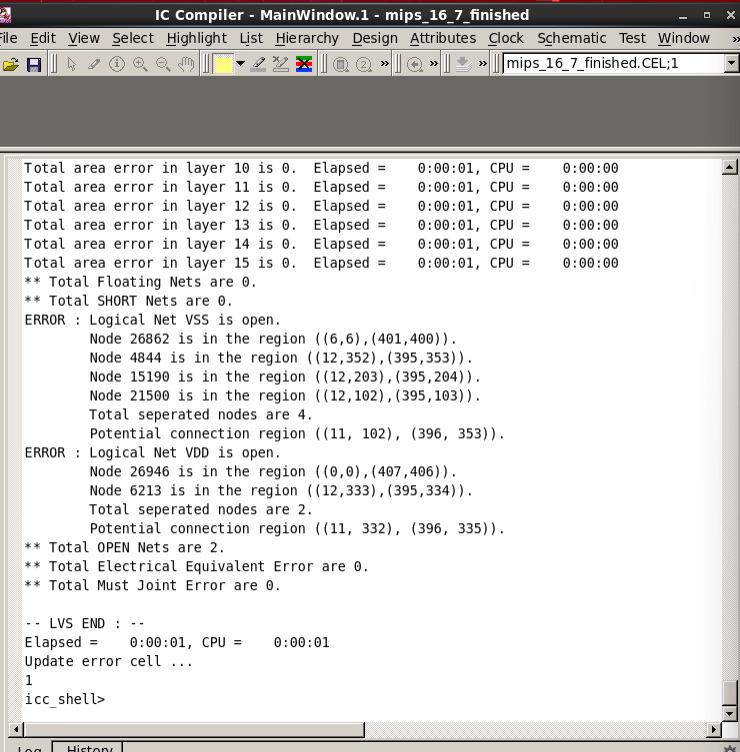


**DRC**

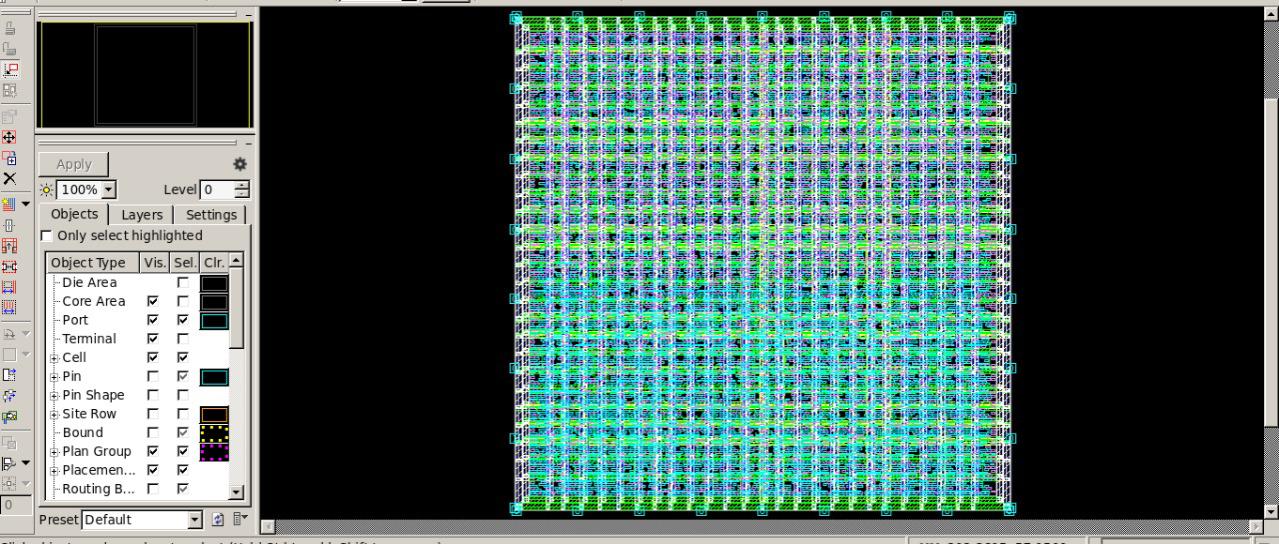
**No DRC**

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**Area report**

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**Final layout**

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