

## Mapping on FPGA (Cyclone IV GX)

<b>OPs</b>	128
<b>Frequency</b>	132.82 MHz
<b>Dynamic Power</b>	32.72 mW
<b>GOPs/s</b>	21
<b>GOPs/w</b>	0.00358
<b>Logic Elements</b>	22126



## IP Hierarchy