KOTA KOUSHIK

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SUMMARY

A diligent and meticulous student currently pursuing a Bachelor's degree in Electronics and Communication Engineering with a strong foundation in analogue and digital circuit design, embedded systems, and VLSI design. Actively involved in hand and laboratory exercises, demonstrating proficiency in using various electronic testing and measurement equipment. troubleshooting and implementing innovative solutions to complex electronics challenges. Seeking an internship to apply theoretical knowledge in a practical industrial setting and gain invaluable experience in the electronics domain

EXPERIENCE

Intel Corporation - Bangalore, IN, Intern
MAY 20 24 - JULY 2024
Working on a Cryptography Problem statement related to digital certificates and securing a communication channel between server and client

Team Everest - Bangalore, IN, Volunteer
SEPTEMBER 2023 - OCTOBER 2023
Team Everest is a volunteer organization that provides education for poor children and my role is a scriptwriter.

EDUCATION

- **GITAM Deemed University Bangalore** **2021 - Present**
  Bachelor of Technology in Electronics and Communication Engineering
  Cumulative GPA: 8.86/10
- **Narayana Junior College** **2019 - 2021**
  Board of Intermediate Education
  Percentage: 93.6%
- **Montessori High School** **2018 - 2019**
  Secondary Board of Education
  Percentage: 92.15%

SKILLS

PROBLEM-SOLVING
COMMUNICATION
TIME MANAGEMENT
ADAPTABILITY
LEADERSHIP• DECISION-MAKING
TEAMWORK
VLSI DESIGN

TECHNICAL SKILLS

C/C++
MBD
Python
SQL
Verilog
Data Structures and Algorithm
Matlab

SOFTWARE

MS Office
Vivado
Ansys & Cadence
Autodesk Fusion 360
Cisco packet tracking
OpenSSL
Wireshark

LANGUAGES - English , Hindi , Telugu

ACADEMIC PROJECTS

- **3D Bridge Design - Autodesk Fusion 360**

Designed a 3D Bridge, employing advanced features and tools within the Fusion 360 software. - Produced comprehensive views, including Top View, Front View, and Side View, showcasing a detailed representation of the train model.

- **Traffic Light Controller with FPGA -Xilinx Vivado**

It is the implementation of a four-way road traffic controlled by the FPGA kit using Verilog code.

- **ML Based Channel Coding Techniques for beyond 5G networks**

It aims to enhance transmission reliability and network performance through adaptive ML-based coding schemes.

TRAINING/COURSES

- Google's Introduction to Generative AI
- Introduction to Cybersecurity Cisco Networking Academy
- AWS Academy Machine Learning
- Google AI & ML Virtual Internship
- HubSpot Digital Marketing

WORKSOPS

- Participated in 'Workshop on VLSI' held on Jan'2024 organised by Department of ECE, GITAM.
- Participated in a hackathon on solving problem statements related to Traffic Control on highways on March 2023 organised by the VDC department, GITAM.