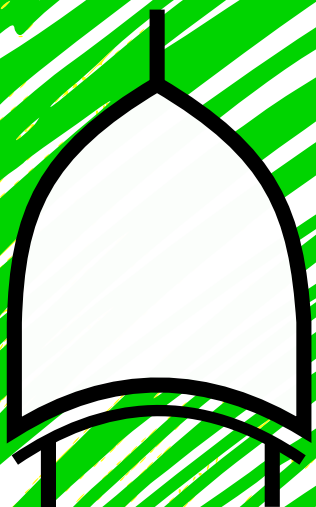
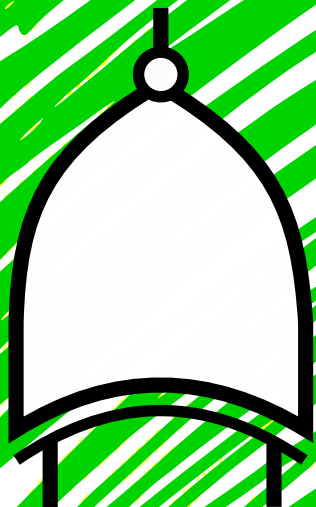


XOR



XOR

XNOR



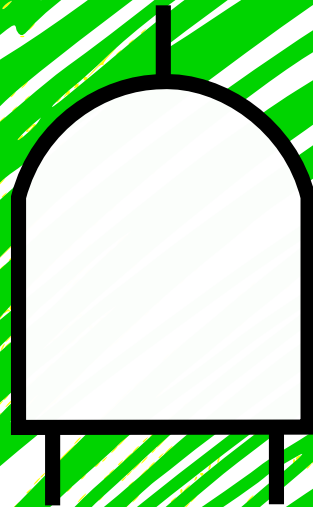
XNOR

NOT



NOT

AND



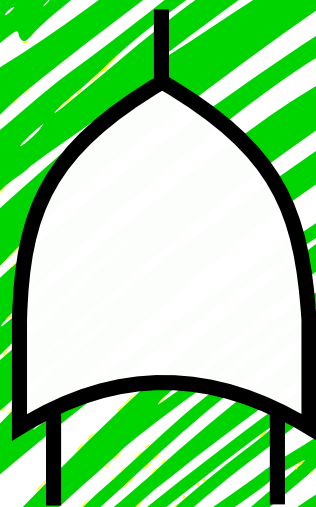
AND

0



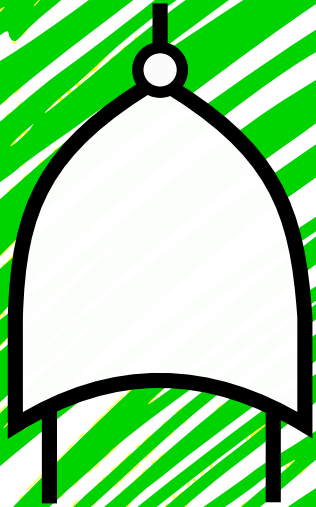
0

OR



OR

NOR



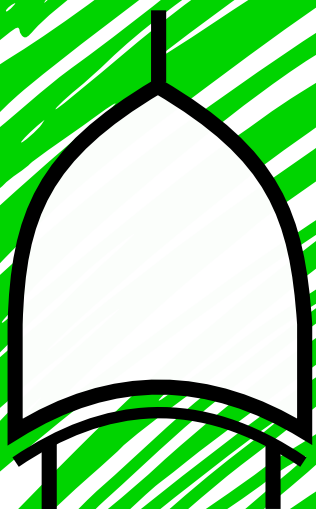
NOR

NAND



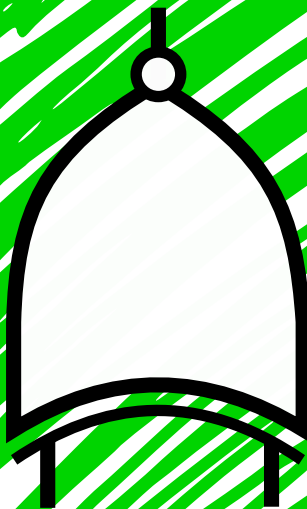
NAND

XOR



XOR

XNOR



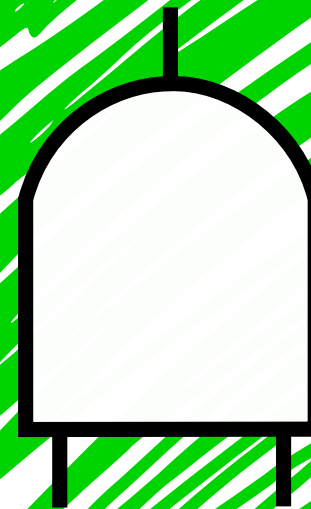
XNOR

NOT



NOT

AND



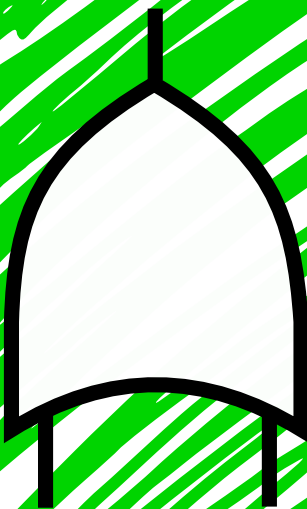
AND

1



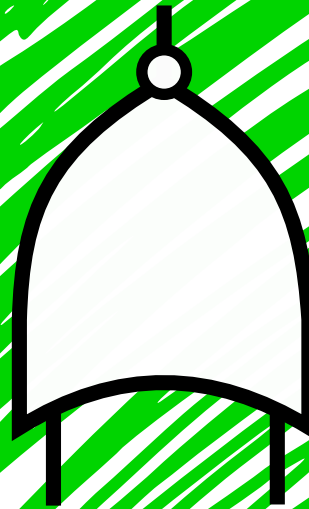
1

OR



OR

NOR



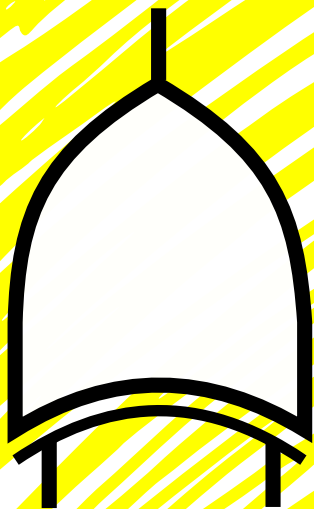
NOR

NAND



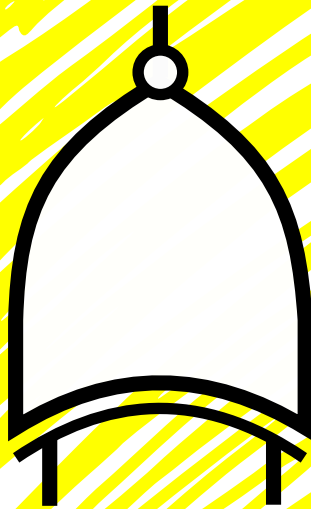
NAND

XOR



XOR

XNOR



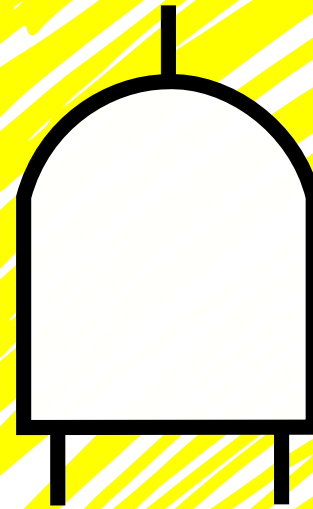
XNOR

NOT



NOT

AND



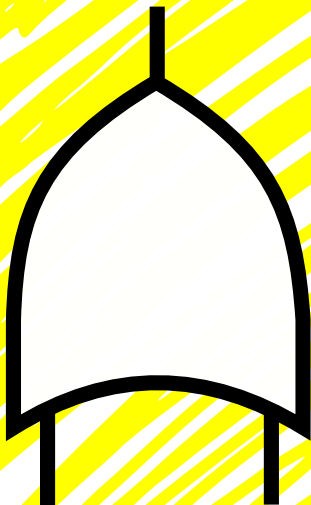
AND

1



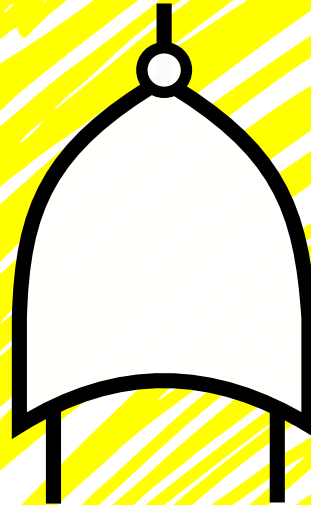
1

OR



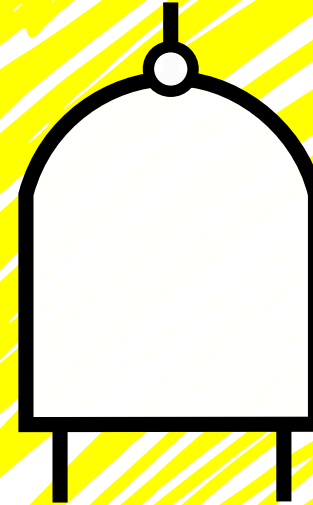
OR

NOR



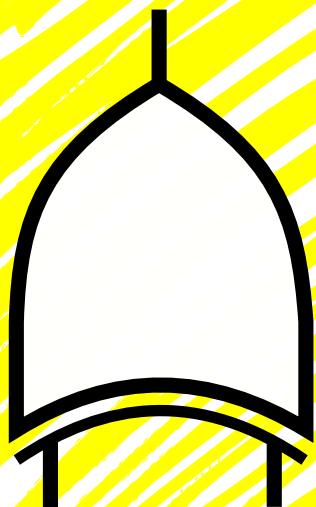
NOR

NAND



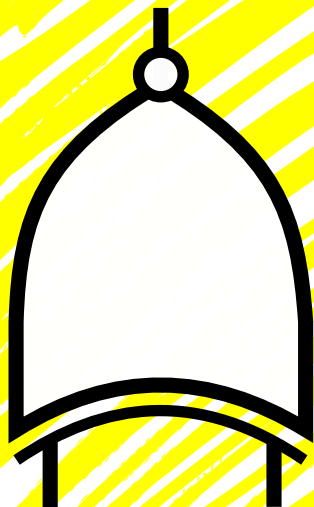
NAND

XOR



XOR

XNOR



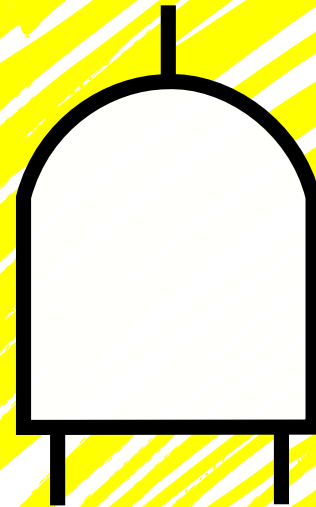
XNOR

NOT



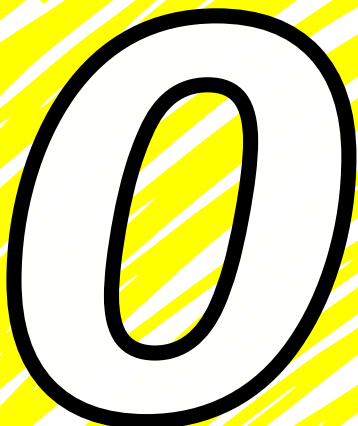
NOT

AND



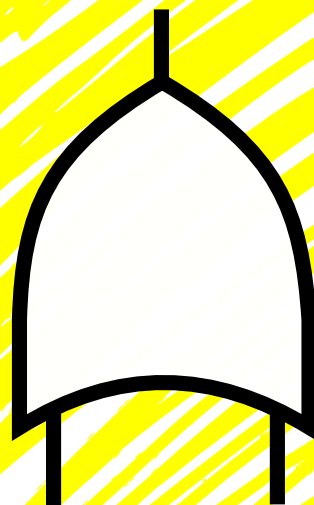
AND

0



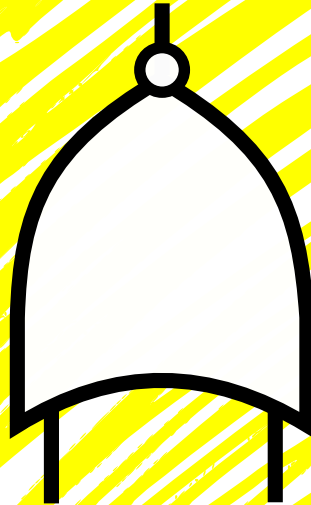
0

OR



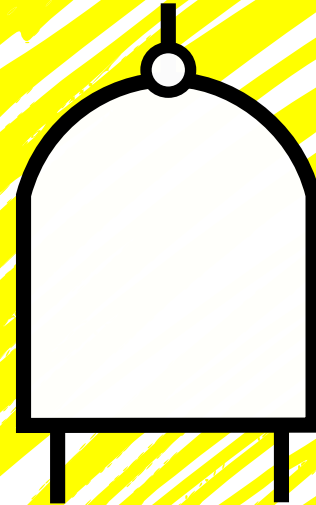
OR

NOR



NOR

NAND



NAND



1

1

1

1

1

1

1

1

1

1

1

1

1

1

1

1

1

1

1

1

1

1

1

1

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

0

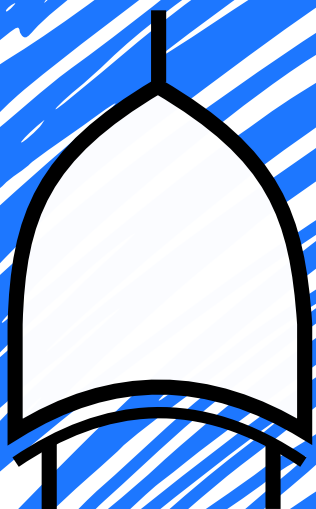
0

0

0

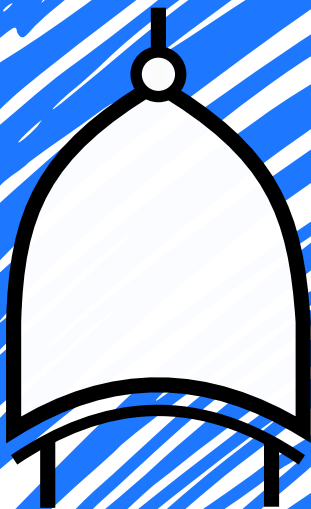
0

XOR



XOR

XNOR



XNOR

NOT



NOT

AND



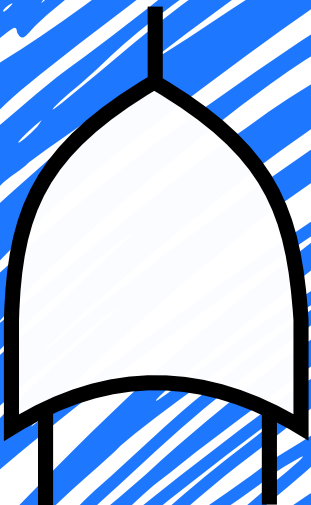
AND

1



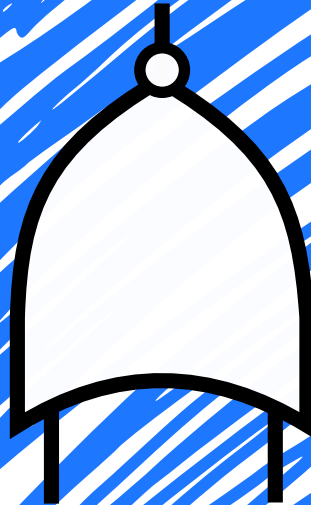
1

OR



OR

NOR



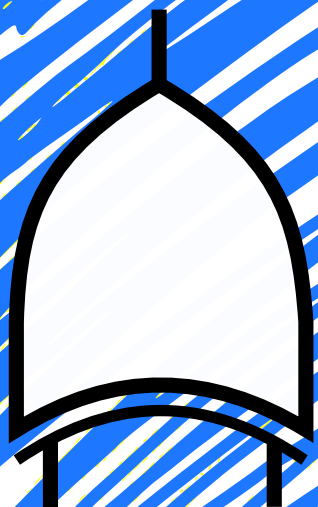
NOR

NAND



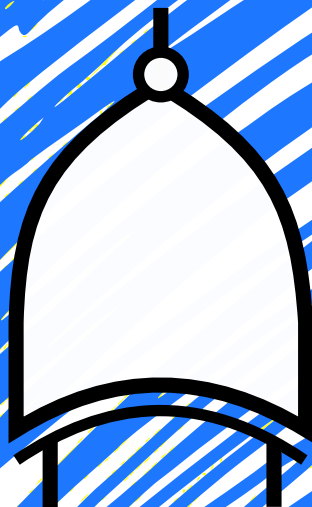
NAND

XOR



XOR

XNOR



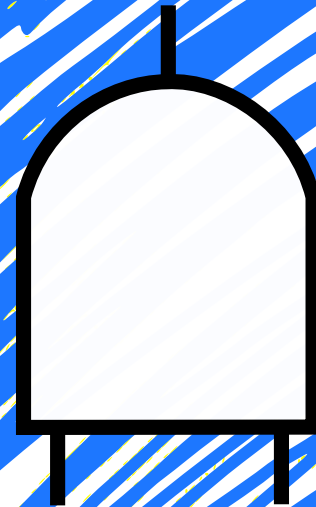
XNOR

NOT



NOT

AND



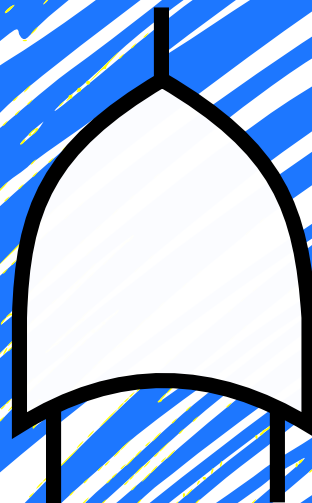
AND

0



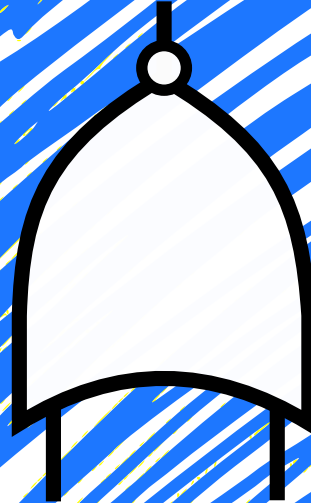
0

OR



OR

NOR



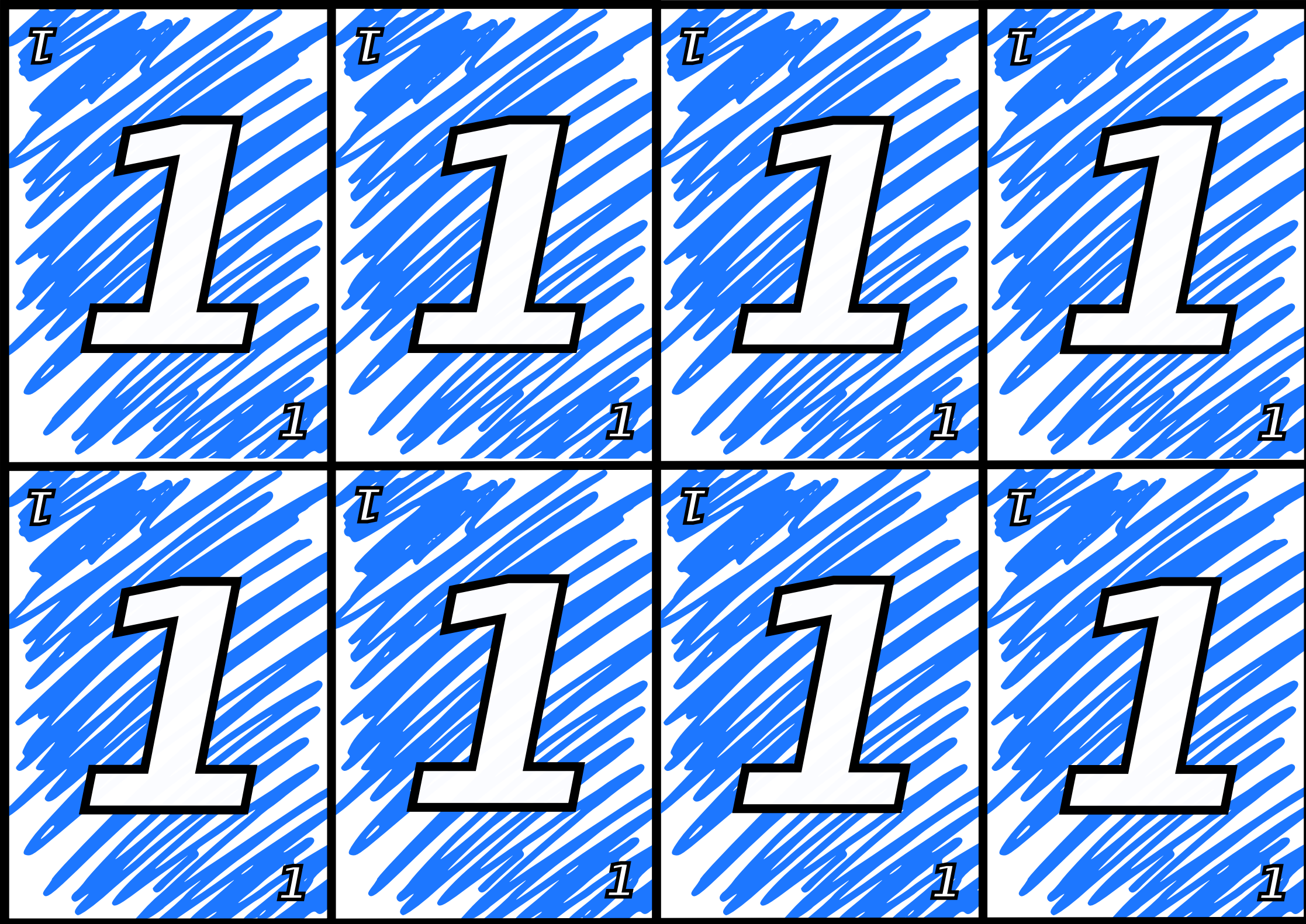
NOR

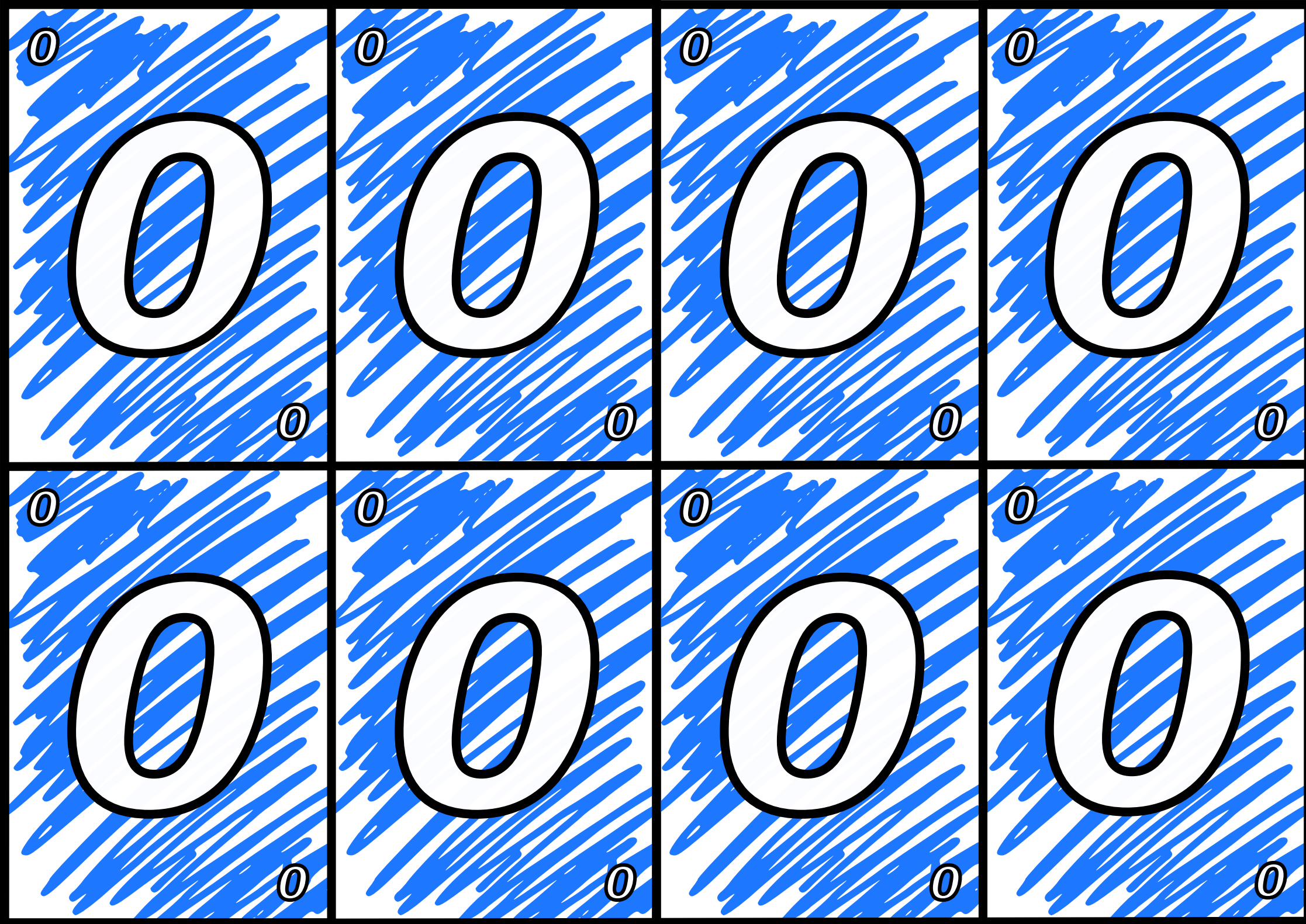
NAND

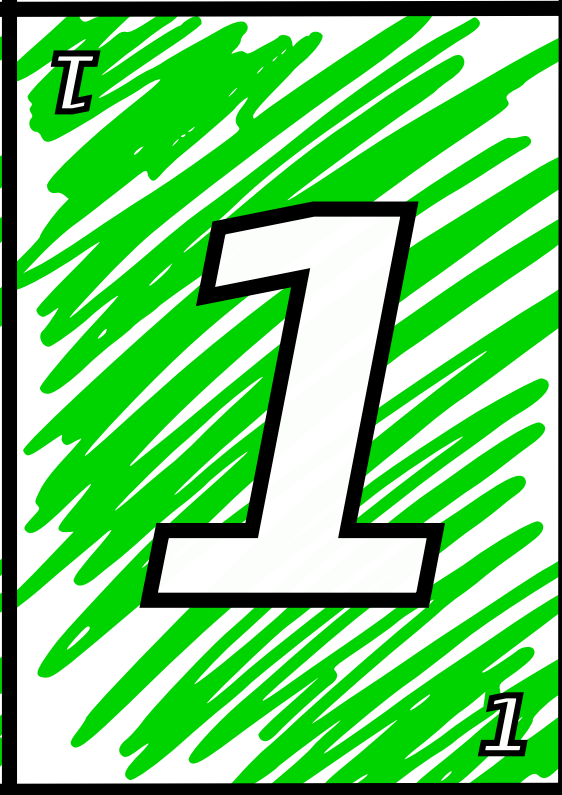
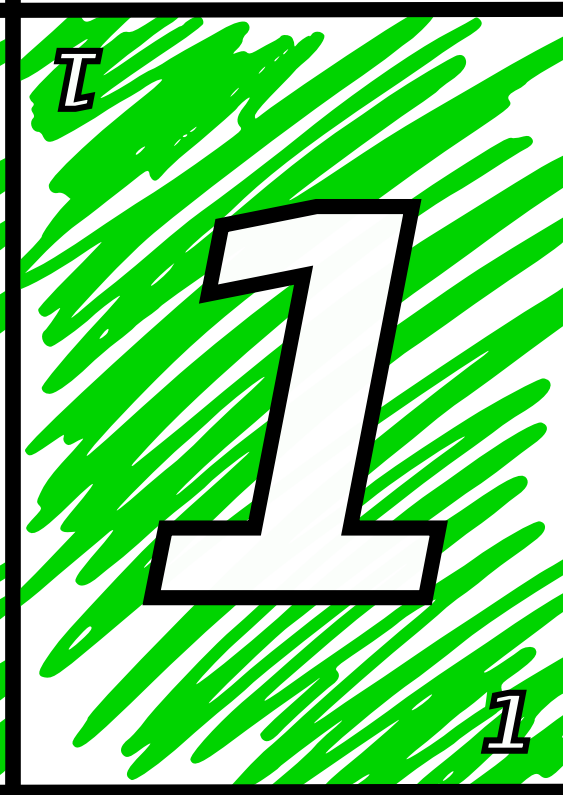
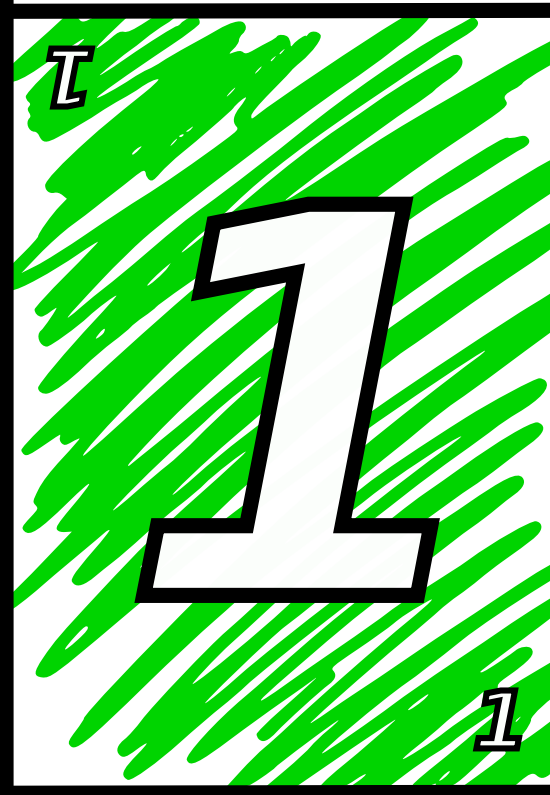
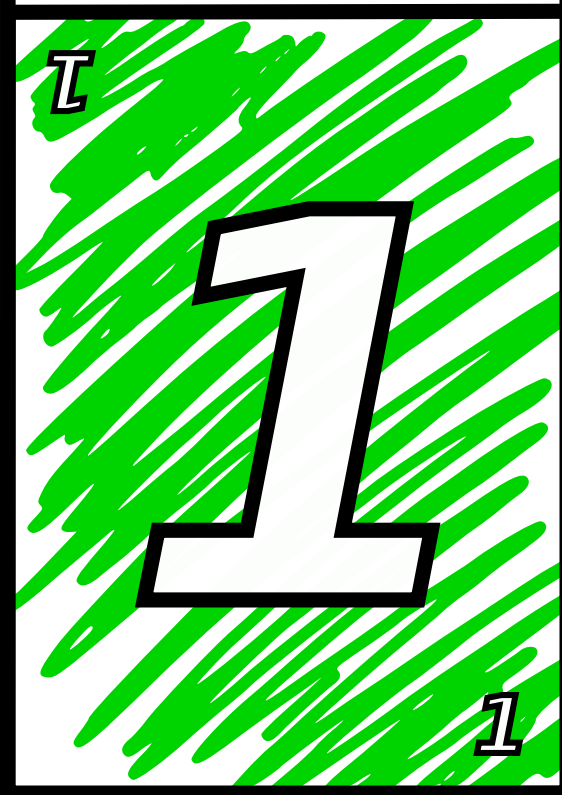
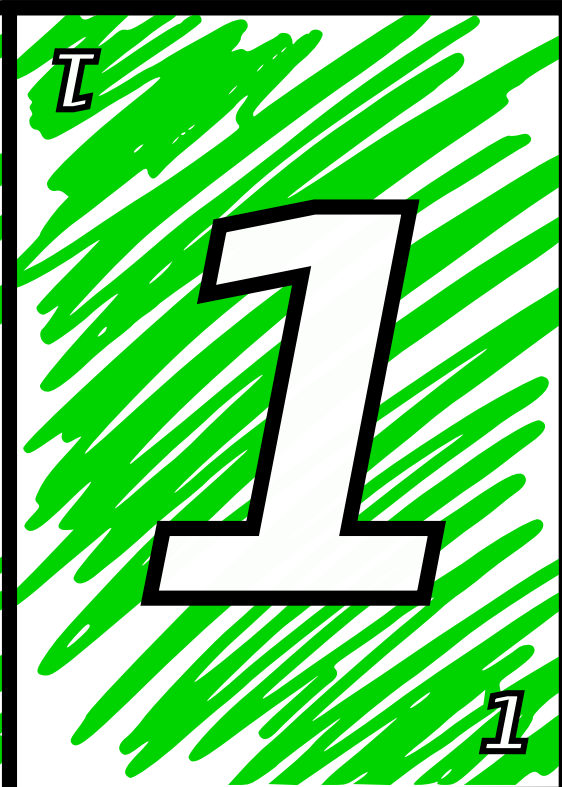
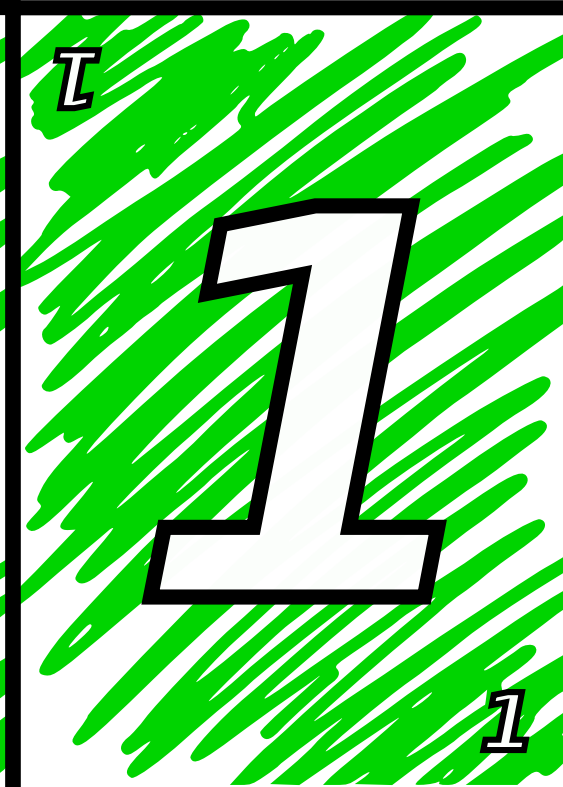
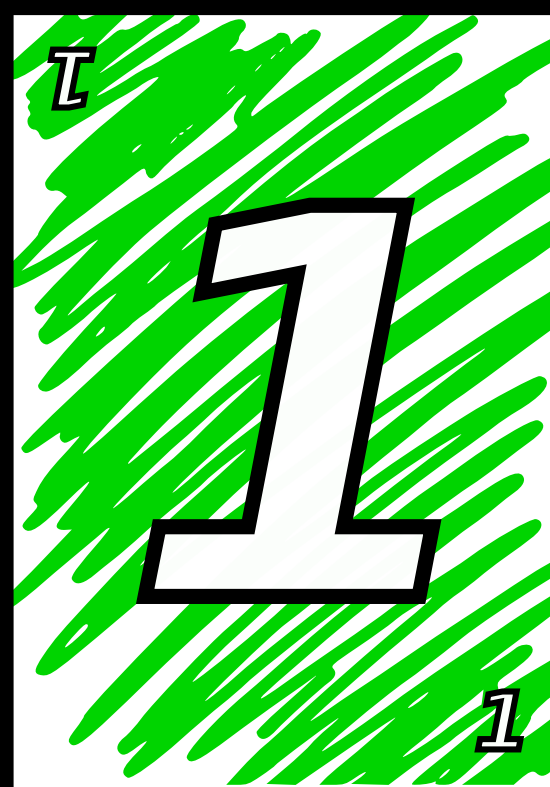
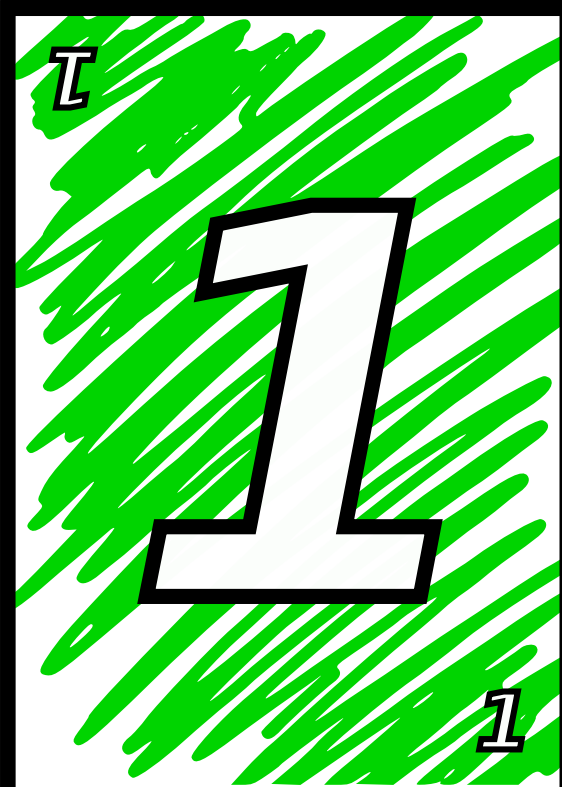


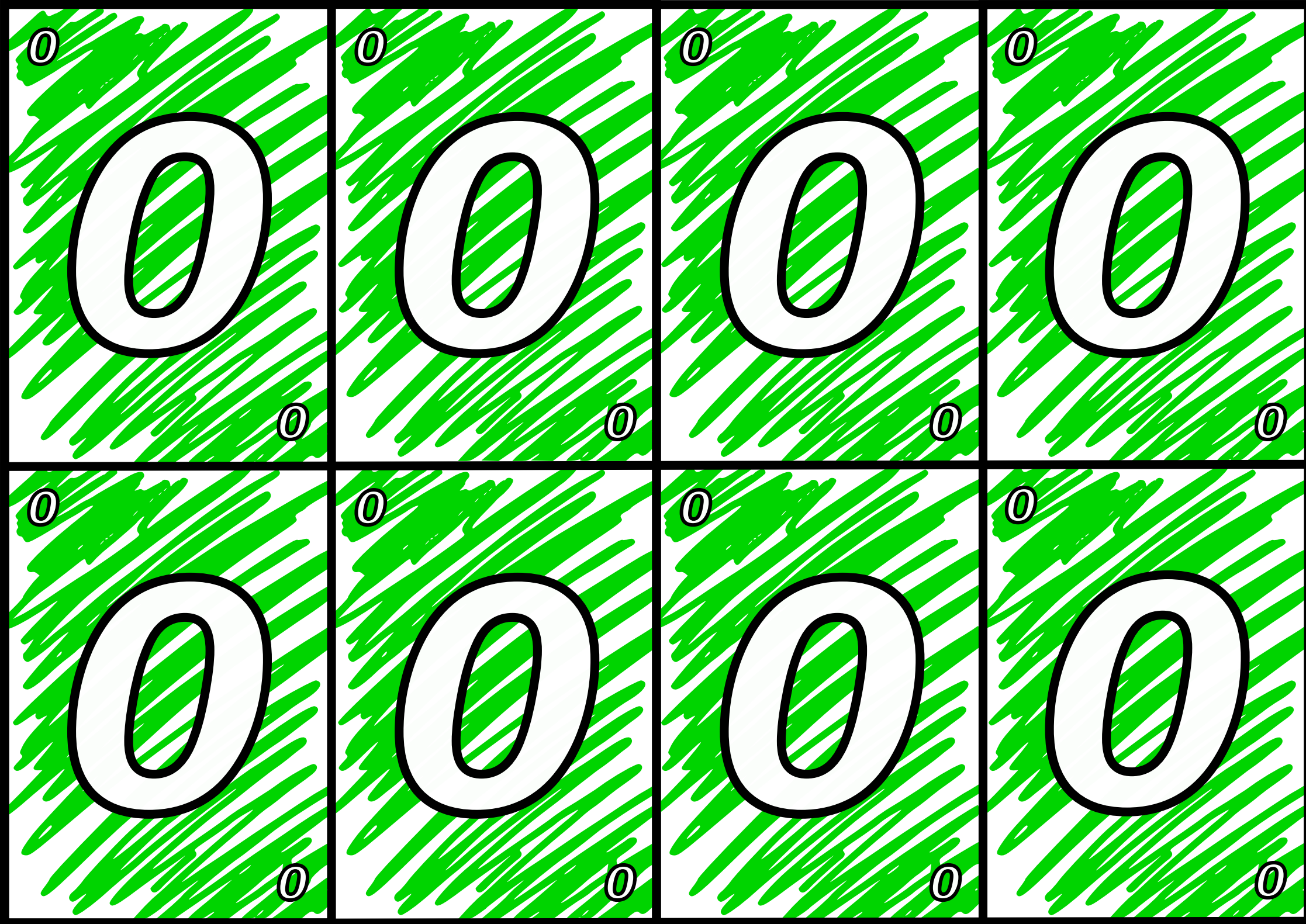
NAND





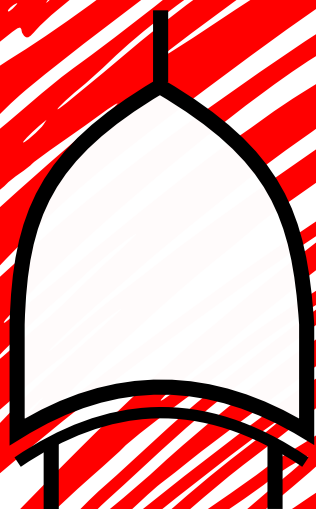






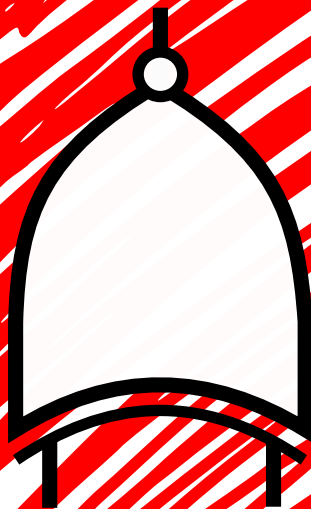


XOR



XOR

XNOR



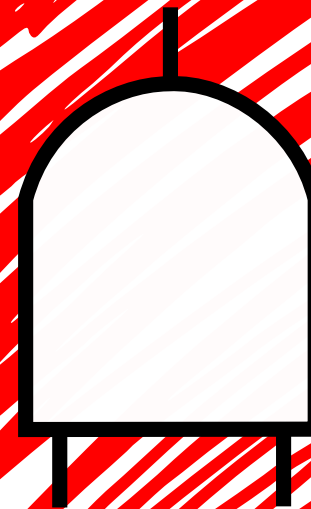
XNOR

NOT



NOT

AND



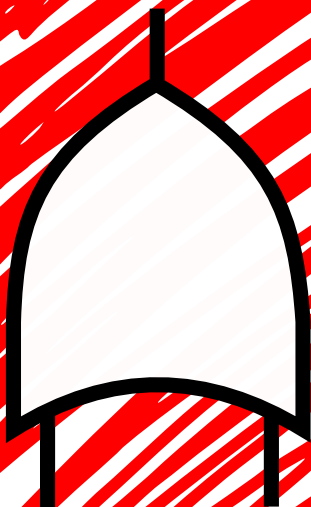
AND

1



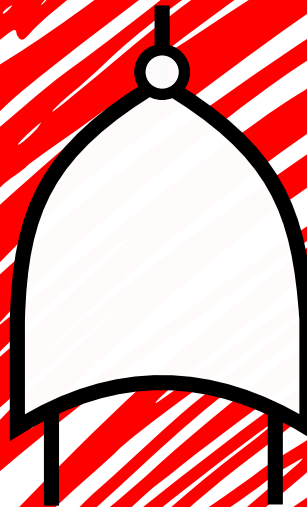
1

OR



OR

NOR



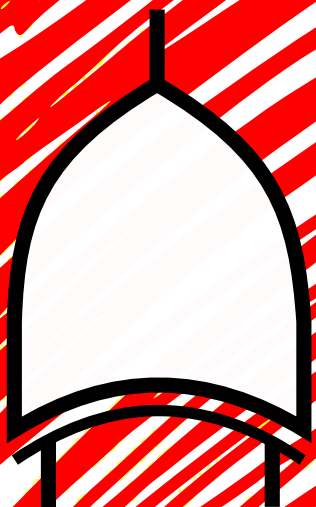
NOR

NAND



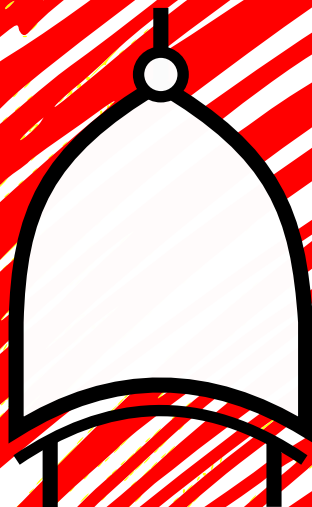
NAND

XOR



XOR

XNOR



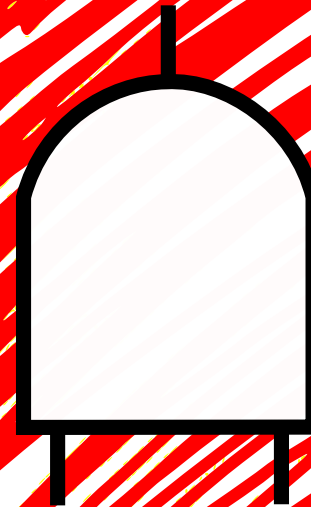
XNOR

NOT



NOT

AND



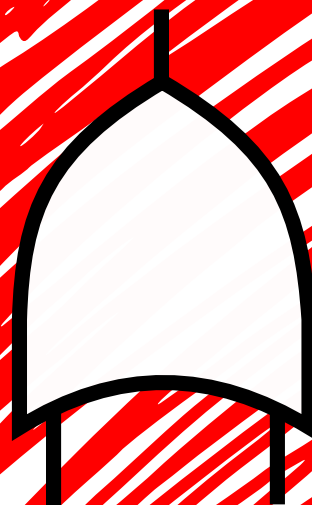
AND

0



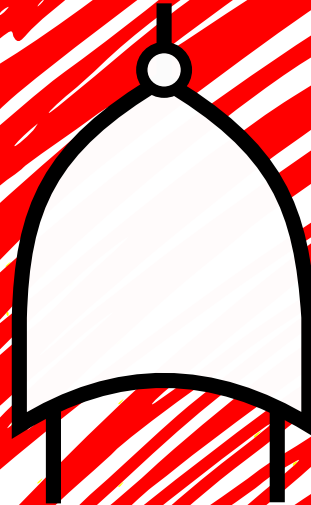
0

OR



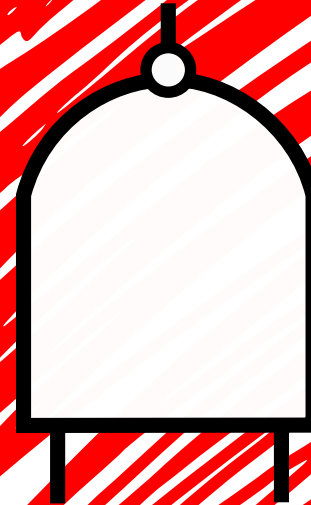
OR

NOR

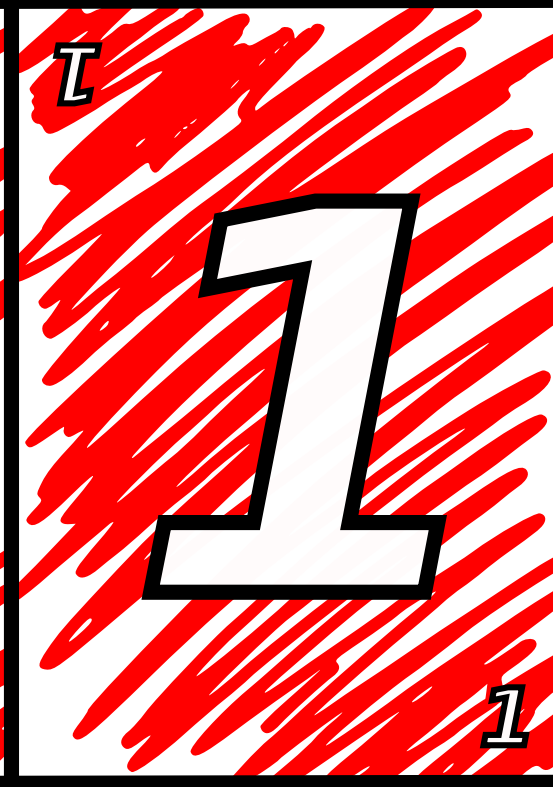
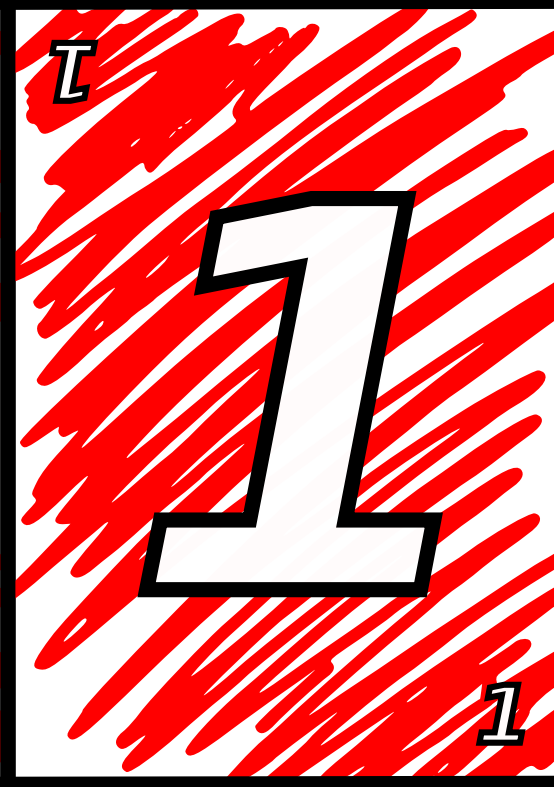
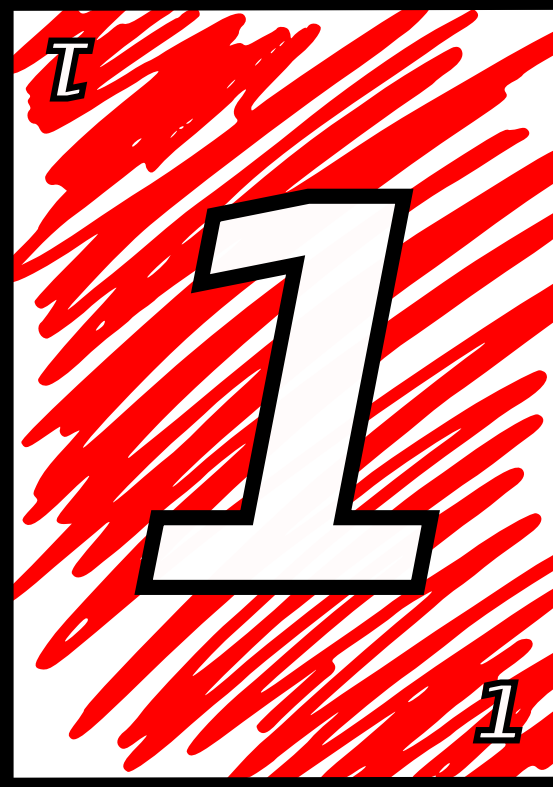
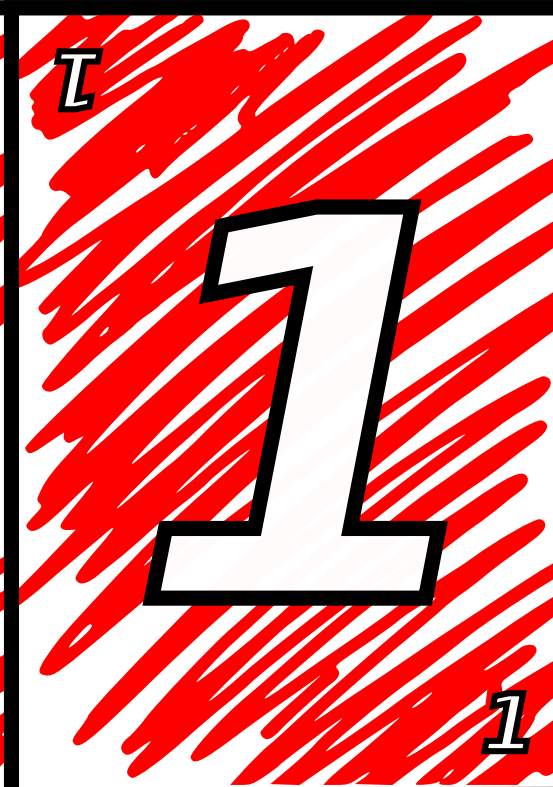
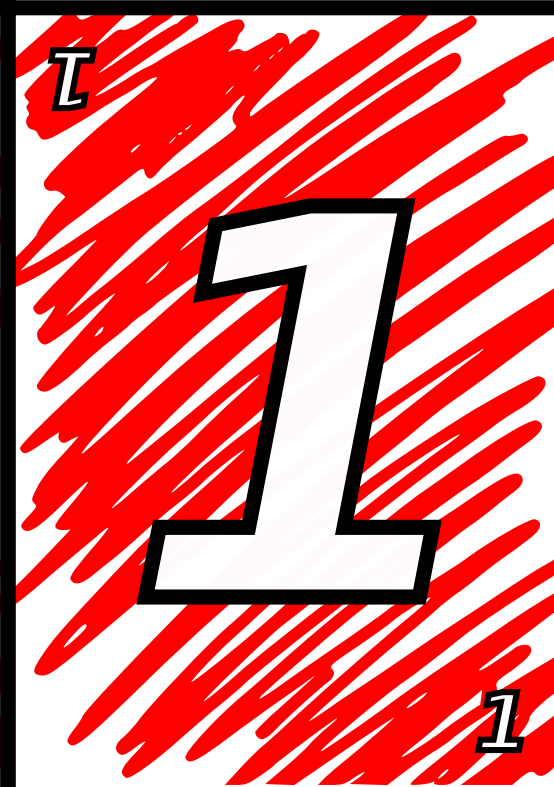
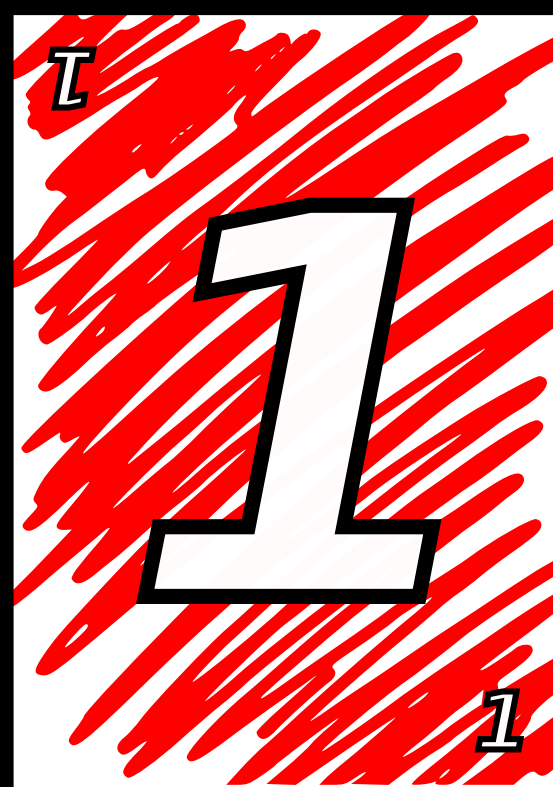
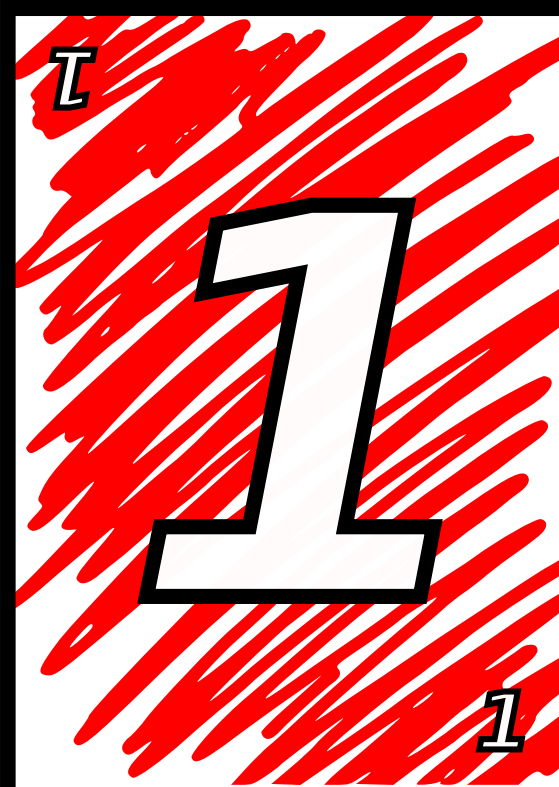


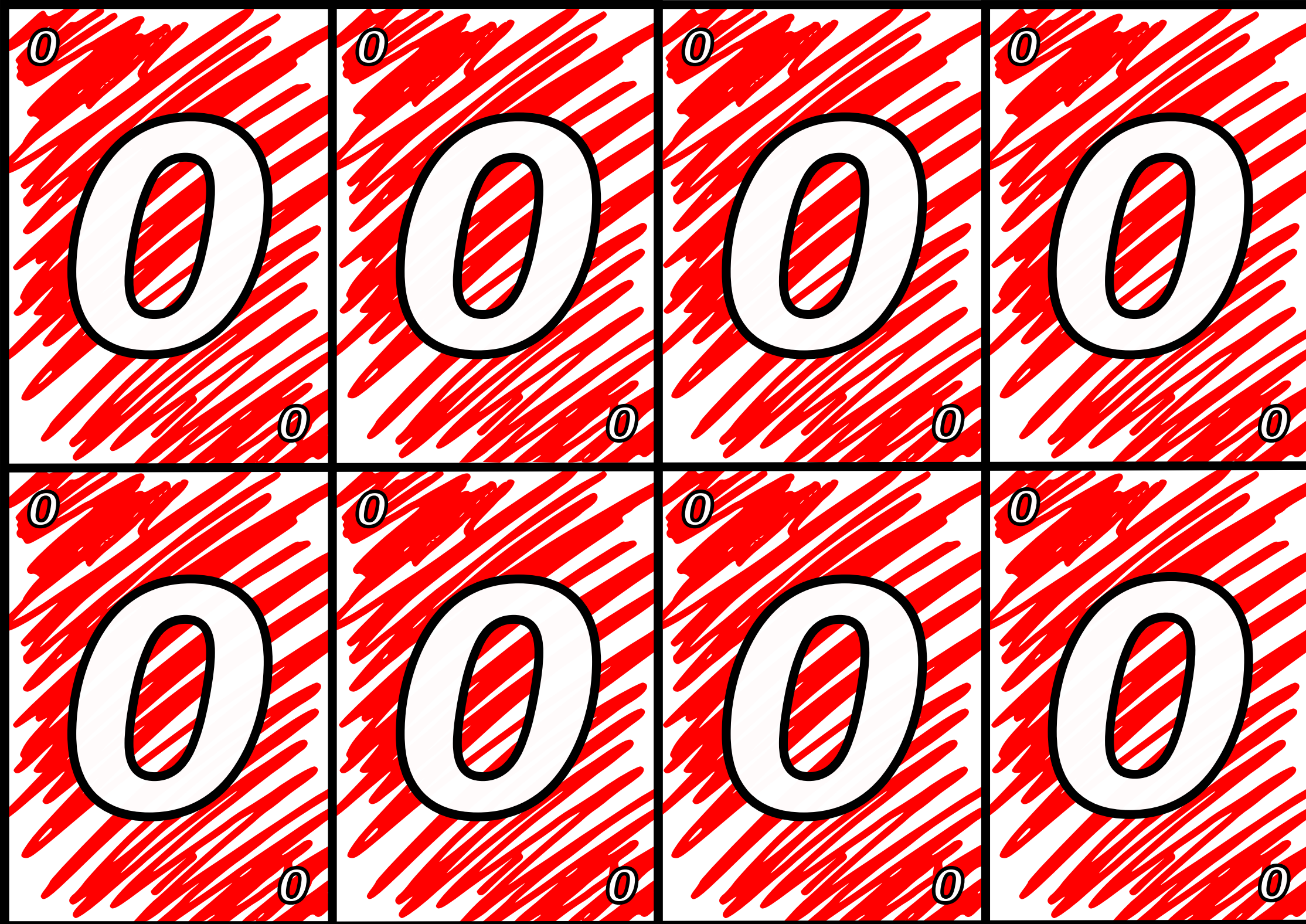
NOR

NAND



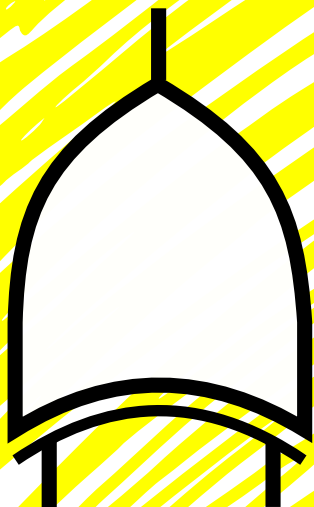
NAND





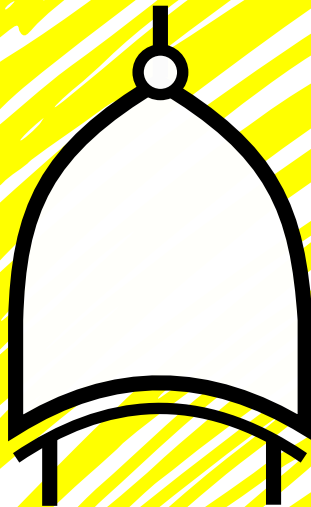


XOR



XOR

XNOR



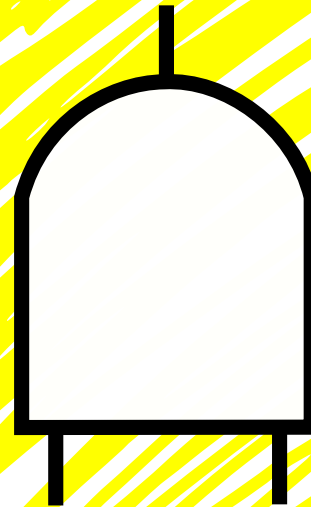
XNOR

NOT



NOT

AND



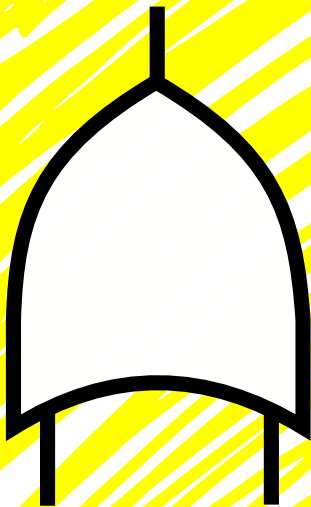
AND

1



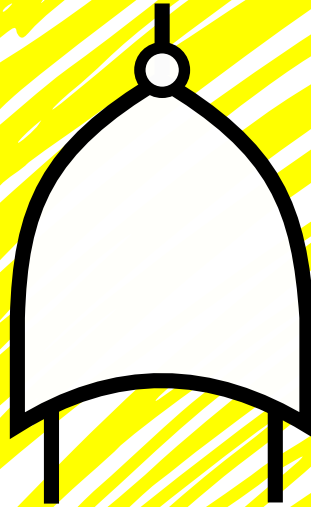
1

OR



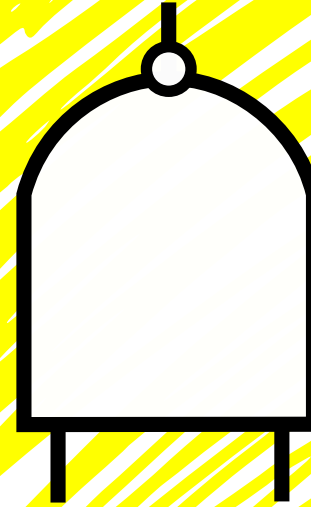
OR

NOR



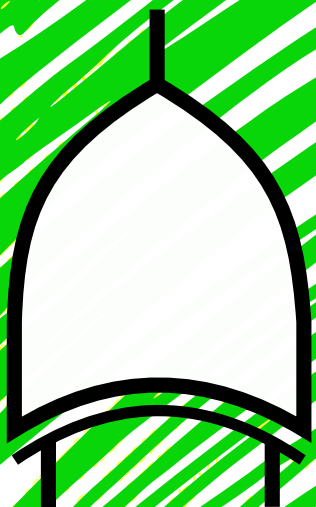
NOR

NAND



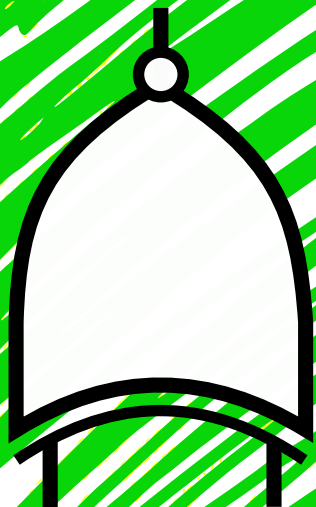
NAND

XOR



XOR

XNOR



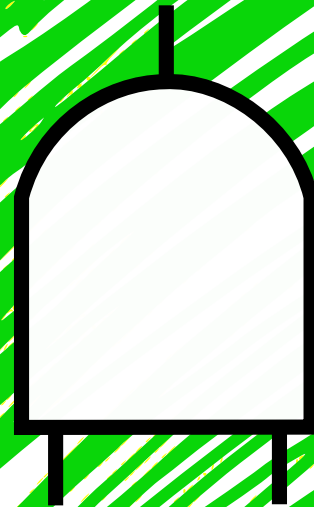
XNOR

NOT



NOT

AND



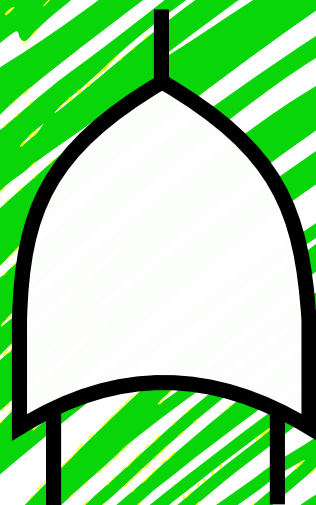
AND

0



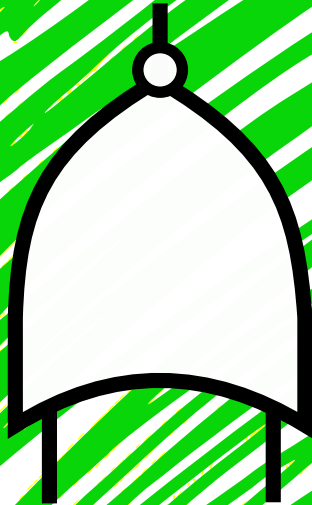
0

OR



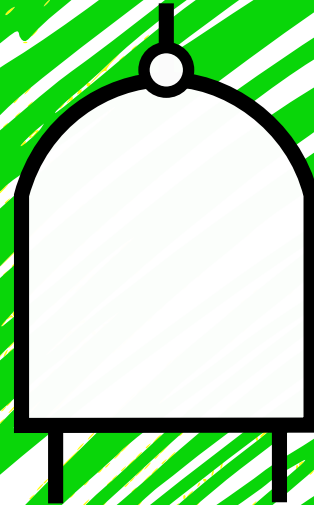
OR

NOR



NOR

NAND



NAND

