



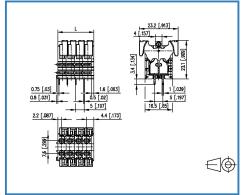
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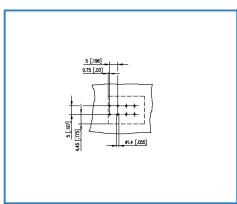
## **Data Sheet** ST125xxVDNN

### Illustration











see enlarged drawing at the end of the document

### **Product specification**

- spring clamp terminal block, solderable, double solder pins
- centerline 5.00 mm, direction of connection vertical 0°
- color gray
- finger push-button, double solder pins
- Connection data





1 mm<sup>2</sup> 250 V / 13.5 A / T60









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# **Data Sheet** ST125xxVDNN

| Technical Data            |         |        |       |  |
|---------------------------|---------|--------|-------|--|
| General Data              |         |        |       |  |
| Solder pin length         | 3.4 mm  |        |       |  |
| min. number of poles      | 2       |        |       |  |
| max. number of poles      | 24      |        |       |  |
| Insulating material class | CTI 600 |        |       |  |
| clearance/creepage dist.  | 4.5 mm  |        |       |  |
| protection category       | IP 20   |        |       |  |
| Overvoltage category      | III     | III    | II    |  |
| Pollution degree          | 3       | 2      | 2     |  |
| Rated voltage             | 320 V   | 800 V  | 800 V |  |
| Rated test voltage        | 4 kV    | 4.0 kV | 4 kV  |  |
| Min. insul. strip length  | 15 mm   |        |       |  |

#### **Connection data**

| CSUUS V / A / AWG | 300 / 15 / 28 - 12   |
|-------------------|----------------------|
| 1 mm²             | 250 V / 13.5 A / T60 |

| Material |
|----------|
| wateriai |

| insulating material | PA66         |  |
|---------------------|--------------|--|
| flammability class  | V0           |  |
| spring material     | Spring steel |  |
| contact material    | CuSn         |  |
| contact surface     | Sn           |  |

### **Climatic properties**

| upper limit temperature | 105 °C |
|-------------------------|--------|
| lower limit temperature | -40 °C |

#### **General**

| solderability | Acc. to JEDEC JESD22-B102E 245°C/5s |
|---------------|-------------------------------------|
| Tolerance     | ISO 2768 -mH                        |



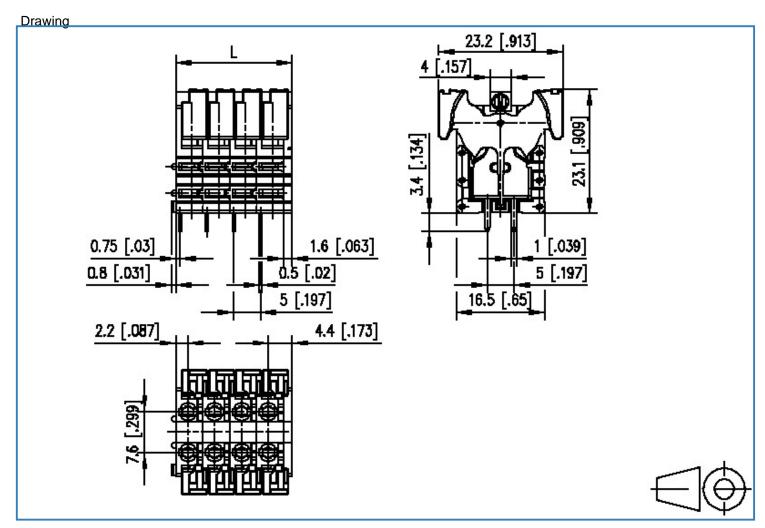




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# **Data Sheet** ST125xxVDNN

### Illustration



L=(pole size - 1) x centerline + 6.6 [0.26]









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# **Data Sheet** ST125xxVDNN

PCB Layout

