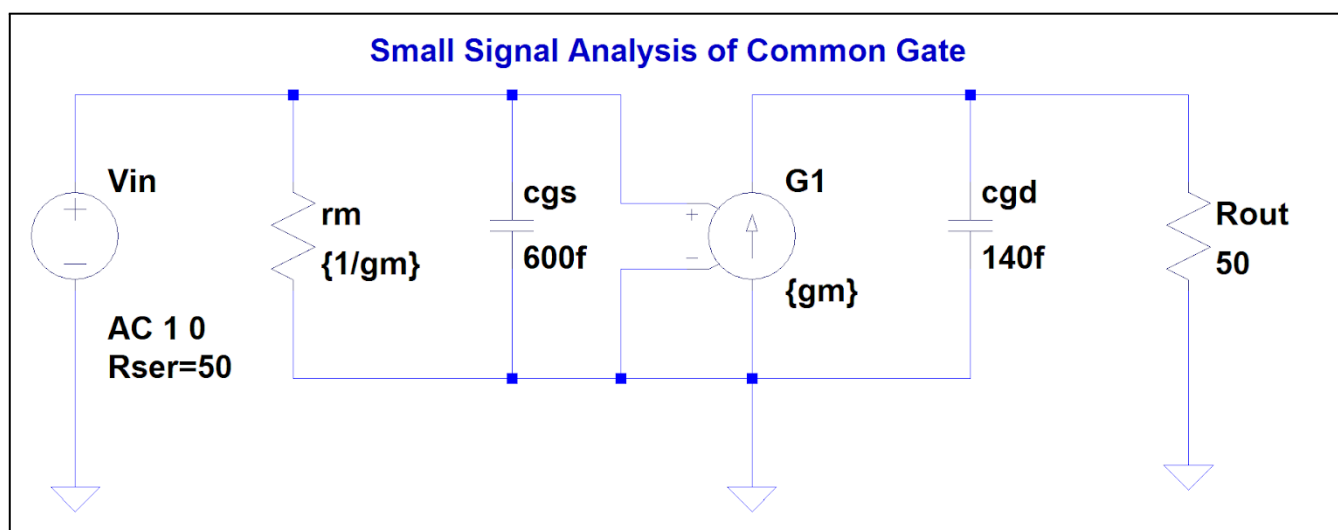
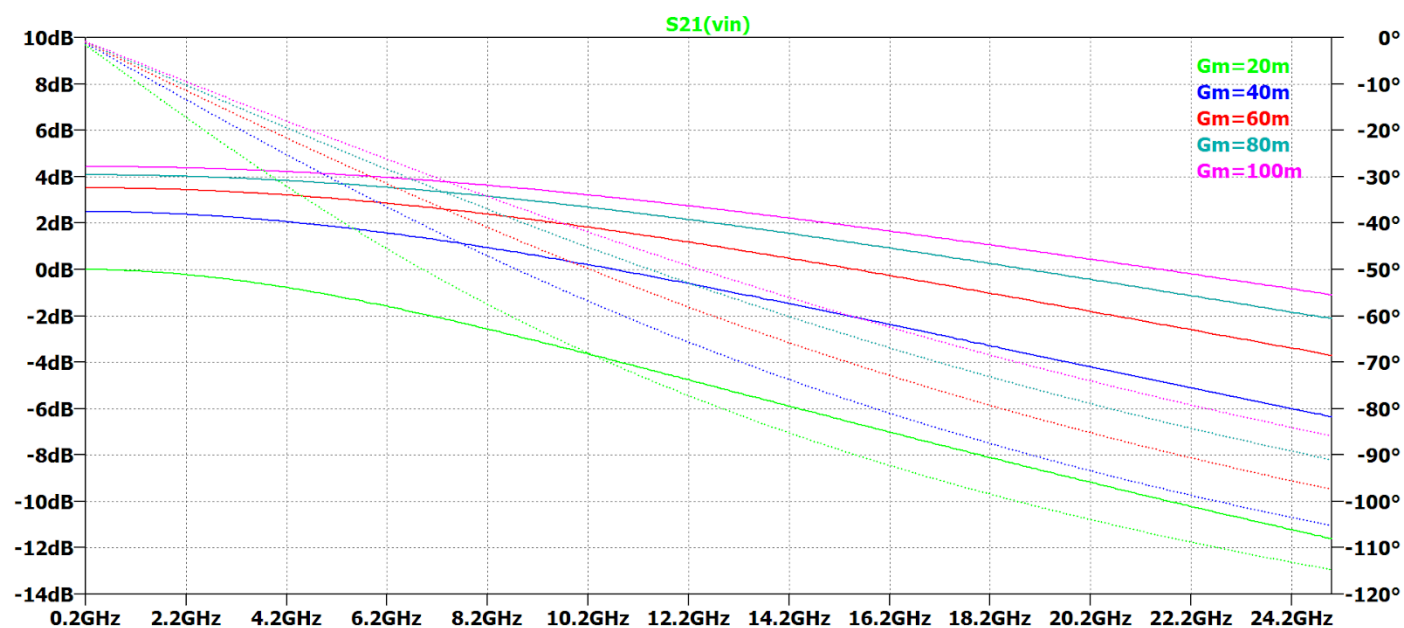


Small Signal Analysis of Common Gate Circuit:

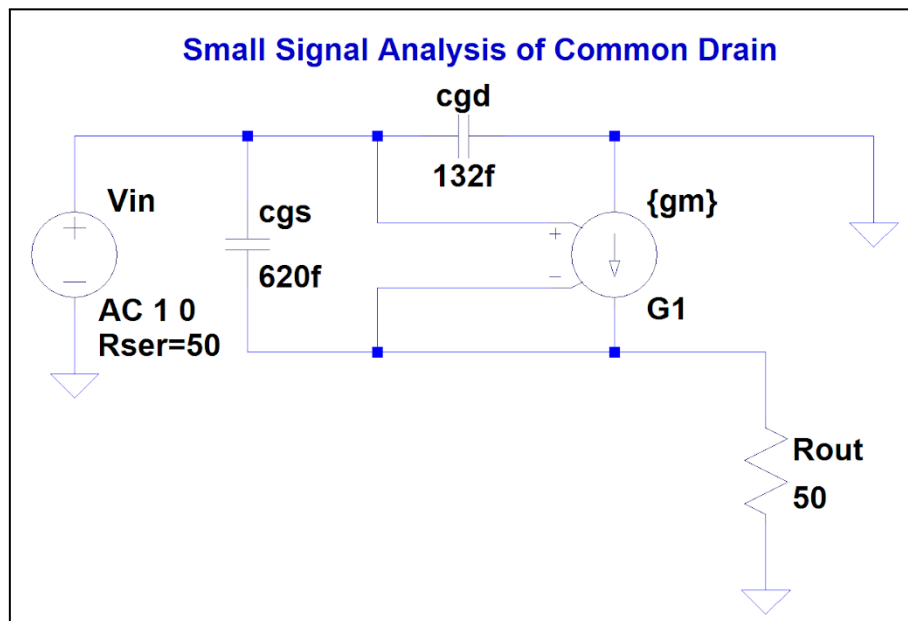


S-parameter Simulation:

S21 plot:



Small Signal Analysis of Common Drain Circuit:



S-parameter Simulation:

S21 plot:

