ENTRANCE EXAMINATION-2017

M.Tech. (Computer Engineering) [Set A]

		$ \ \ $		ROLL NO.
	╛	\Box		
Signature of Invigilator			-	

Time: 2 Hours

Total Marks: 85

Instructions to Candidates

- Do not write your name or put any other mark of identification anywhere in the OMR Answer Sheet. IF ANY MARK OF IDENTIFICATIONS IS DISCOVERED ANYWHERE IN OMR ANSWER SHEET, the OMR sheet will be cancelled, and will not be evaluated.
- This Question Booklet contains this cover page and a total of <u>85 Multiple Choice Questions of 1mark.</u> Space for rough work
 has been provided at the beginning and end. Available space on each page may also be used for rough work.
- 3. Each correct answer carries one mark.
- There is negative marking in Multiple Choice Questions. For each wrong answer 0.25 marks will be deducted.
- 5. USE OF CALCULATOR IS NOT PERMITTED.
- 6. USE/POSSESSION OF ELECTRONIC GADGETS LIKE MOBILE PHONE, iPhone, iPad, pager ETC. is not permitted.
- Candidate should check the serial order of questions at the beginning of the test. If any question is found missing in the serial
 order, it should be immediately brought to the notice of the Invigilator. No pages should be torn out from this question booklet.
- Answers must be marked in the OMR answer sheet which is provided separately. OMR answer sheet must be handed over to the invigilator before you leave the seat.
- 9. The OMR answer sheet should not be folded or wrinkled. The folded or wrinkled OMR/Answer Sheet will not be evaluated.
- Write your Roll Number in the appropriate space (above) and on the OMR Answer Sheet. Any other details, if asked for, should be written only in the space provided.
- 11. There are four alternative answers to each question marked A, B, C and D. Select one of the A swers you consider most appropriate and fill up the corresponding oval/circle in the OMR Answer Sheet provided to you. Correct procedure for filling up the OMR Answer Sheet is mentioned below.
- 12. Use Black or Blue Ball Pen only for filling the ovals/circles in OMR Answer Sheet while answering the Questions. For your Choice of answers darken the correct oval/circle completely. If the correct answer is 'B', the corresponding oval/circle should be completely filled and darkened as shown below.

CORRECT METHOD

-		SEA -A	
	1.	The output of initial permutation in I	
		0x00020000000000001.	DES, if the input
	-	A. 0x0000060000000020	
	-	C. 0x000006000000002	B. 0x00000080000000020
		0.0000000000000000000000000000000000000	D. 0x0000008000000002
	2.	Which of the fellowing	
		Which of the following is not used for the prims A. Naïve method	ility test?
		Traire method	B. Miller Rabin
		C. Divisibility	D. Rabin Karp
	3.	CATTO	,
	٠.	GAIT is a type of authentication	method.
		A. Text password	B. Biometric authentication
		 C. Authentication token 	D. Certificate based authentication
_	-		
	4.	A hashing function for digital signature	
		(1) Must give a hashed message which is shorter	than the original message
		(ii) Must be hardware implementable	
		(iii) Two different messages should not give the	same hashed message
	1	(iv) Is not essential for implementing digital sign	ature
		A. i and iii	B. i and ii
	.	C. ii and iii	D. iii and iv
			Dr. III dild 17
	5.	WPA2 is used for security in	
	- 1	A. Ethernet	B. Bluetooth
		C. Wi-Fi	D. ZigBee
			D. Elgbee
	6.	The value of 23 ⁻¹ mod 100 is:	
		A. 97	B. 77
		C. 87	D. 46
	7.	An IP packet has arrived with the	first few hexadecimal digits as:
		45000028000100000102, How many ho	ops can this packet travel before being
		dropped?	, and a second come
	9	A. One	B. Two
		C. Three	D. Four
	8.	The Loopback address in IPV6 is	
		A. ::/127	B. ::/128
		C. ::/120	D. ::/124
ı	9.	GRANT command of SQL is a:	
	2.	A. Data query language	P. P.
		C. Data definition language	 B. Data control language
			 D. Data manipulation language
1	10.	Which of the following transformation is refer	red as commutative of SELECTION in
		DBMS?	or believing in
		A. $\sigma_{c1} \wedge \sigma_{c2}$ $\sigma_{Cn}(R) \equiv \sigma_{c1}(\sigma_{c2}((\sigma_{Cn}(R)))$	R)))
		B. $\sigma_{c1}(\sigma_{c2}(R)) \equiv \sigma_{c2}(\sigma_{c1}(R))$	
		C. $\pi_L \pi_M - \pi_N (R) = \pi_L$	
		D. $\pi_{A1,A2}$ $\pi_{AN}(\sigma_c(R)) \equiv (\sigma_C(\pi_{A1,A2})$	AN(R))
	44		
	11.	For the renaming purpose, the symbol used in rela	
		Α. ρ	В. о
		С. п	D, Υ

ENTRANCE EXAMINATION-2017

12.	In an RDBMS a relation R(A, B) currently	has tuples {(1, 2), (1, 3), (3, 4)} and relation Consider the following two SOL queries SOL			
	and SO2:	has tuples {(1, 2), (1, 3), (3, 4)} and relation Consider the following two SQL queries SQ1			
*	SQ1 : Select *	consider the following two SQL queries SQ1			
	From R Full Join S				
	On R.B=S.B;				
	SQ2 : Select *				
Ì	From R Inner Join S				
	On R.B=S.B;				
	The numbers of tuples in the result of the	SQL query SQI and the SQL query SQ2 are			
	given by:	SQL query SQ1 and the SQL query SQ2 are			
	A. 2 and 6 respectively	B. 6 and 2 respectively			
	C. 4 and 2 respectively	D. 2 and 4 respectively			
13.	The term arounds it	* *			
	The term granularity used in DBMS relates to A. The size of the table				
	C. The size of the tuple	B. The size of the data item			
	the size of the tuple	D. The size of primary key			
14.	The following functional dependencies are gi	ven:			
	$AB \rightarrow CD$, $AF \rightarrow D$, $DE \rightarrow F$, $C \rightarrow G$, $F \rightarrow F$	E, G→ A, Which of the following option is			
	Turse:	option is			
	$A.\{CF\} += \{ACDEFG\}$	$B. \{BG\} + = \{ABCDG\}$			
	$C. \{AF\} + = \{ACDEFG\}$	D. $\{AB\}$ + = $\{ABCDFG\}$			
15.	A clustering index is defined on the fields wh	ich are of type			
	A. Non-key and ordering	B. Non-key and non-ordering			
	C. Key and ordering	D. Key and non-ordering			
-					
16.	The function f: $R-\{2\} \rightarrow R$ defined by $f(x) = ($				
	A. one-one and onto	B. one-one but not onto			
	C. Neither one-one nor onto	D. Not one-one but onto			
17.	Suppose that the relation R on a set is represe	nted by the matrix			
	$(i \cdot i \cdot i)$	K is a			
	$\begin{bmatrix} 1 & 1 & 0 \end{bmatrix}$				
- 1	$M_R = \begin{bmatrix} 1 & 1 & 1 \end{bmatrix}$				
	(0 1 1)				
	The above matrix is:				
	A. Reflexive, antisymmetric	B. Reflexive, symmetric			
	C. Reflexive, transitive	D. Equivalence relation			
18.	What is the solution of the linear non homoge	PAGNA TO			
	What is the solution of the linear non-homogeneous recurrence relation $a_n-6a_{n-1}+8a_{n-2}=3^n$, where $a_0=3$, $a_1=7$?				
	A. $a_n = (7)(2^n) + (5)(4(2) + (-9)(3^n)$	B. $a_n = (5)(2^n) + (7)(4^n) + (2)(2^n)$			
	C. $a_n = (7)(2^n) + (5)(4^n) + (9)(3^n)$	B. $a_n = (5)(2^n) + (7)(4^n) + (-9)(3^n)$ D. $a_n = (5)(2^n) + (7)(4^n) + (3^{n+2})$			
10		(7/4)+(3)			
19.	Which of the following poset is a lattice? A.1	Land and the same of the same			
	C. 3	B. 2			
		D. 4			

ENTRANCE EXAMINATION-2017

	A. 2 C. 4	B. 3 D. 6
0.	62,123,142 are inserted in the table, in what	s linear probing to resolve collisions. The key in used is key %10. If the values 43, 165, t location would the key value 142 be inserted?
	C1, 0, 1	D. 1, 0, 1
	A.1, 2, 1	B1, 2, -1
29.	The balance factor of height balance tree is	
	C. (n-1)/2	D. (2n-1)/3
	A. n/2	B.(n-1)/3
	children is:	ee of n nodes, with each node having 0 or 3
28.	The number of leaf nodes in a second	
	C. (23, 17, 14, 7, 13, 10, 1, 5, 6, 12)	D. (23, 17, 14, 7, 13, 10, 1, 12, 5, 7)
	A. (23, 17, 14, 6, 13, 10, 1, 12, 7, 5)	B. (23, 17, 14, 6, 13, 10, 1, 5, 7, 12)
27.	Which of the following sequences of array	elements forms a hear?
	C. (8,3)	D. (3,8)
	A.(4,7)	B. (7,4)
	right subtree of the root, respectively is:	er. The number of nodes in the left subtree and
26.	A binary search tree is generated by inserti 50, 15, 62, 5, 20, 58, 91, 3, 8, 37, 60, 60	ing the following integers in the order: 24. The number of nodes in the left subtree and
26	1	
	C. $ab+c*d-e\wedge f\wedge$	D. $-+*$ be $\wedge \wedge$ def
	A. abc * + def ^ ^ -	b * c − d ∧ e ∧ f is: B. abc * + de ∧ f ∧ -
	corresponding to the infix expression a +	h*c - d A e A fier
Arci e	order of precedence (from highest to	e left associative and '^' is right associative. The owest) is ^, *, +, The postfix expression
25.	Assume that the operators (1) (1) (1)	laß annual attended to the
	C. a∨b → b∨c	$D. a \wedge b \rightarrow b \wedge c$
	A. a V b → b A c	B. a ∧ b → b ∨ c
24.	Which of the following is tautology?	
		ner nergon was
	C. Regular	D. Bipartite
	A. Simple	B. Planar
23.	If the maximum degrees of a graph is equis called:	al to minimum degrees of graph, then the grap
22	16 th a market of the second	the discount of the same
	C.	D. 5
	A.2	B. 3
22.	How many different non-isomorphic abel	ian groups of order 4 are there?
	o. Path	D. Cycle
	Minimal spanning tree Path	B. Loop
21.	Kruskal's algorithm is used for finding:	B. Lann
	C. [E]	D. 2[E]
	A. 2 V	B. V ²

	In tree construction, which one will be s A. Queue	unable and efficient data structure?
	C. Heap	B. Linked list
		D. String
32.	The best data structure to all 1	
	parentheses is:	hether an arithmetic expression has balance
	A. Queue	
	C. Tree	B. Linked list
		D. Stack
33.	After evaluating the following postfix ex	
	10 5 + 60 / * 8	pression the result will be:
	A.284	B. 213
	C. 142	D. 71
34.	What will be the output of the program v	vritten in C?
	#include <stdio.h></stdio.h>	
	void main()	
	printf/190/22 1 cc4)	
	printf("%x", -1<<4);	
	A. fff0	D 6000
	C1	B. ffff D. 0
	The state of the s	D. 0
35.	What will be the output of the program w	ritten in C?
	#include <stdio.h></stdio.h>	
	int main()	
	{	8 4
	char *str;	
-	str = "%s";	
	printf(str, "K\n");	
	return 0;	
	A. Error in the program	B. No output
	C. K	D. %s
	C. K	D. 703
36.	Runtime polymorphism in object oriented	programming is achieved by
-	A. Friend function	B. Operator overloading
	C. Virtual function	D. Function overloading
37.	Which of the statements are true?	11 - 41
	Function overloading is done at com Protected members are accessible to	the member of derived class
	 II. Protected members are accessible to III. A derived class inherits constructors 	
,	IV. A friend function can be called like	
	V. Nested class is a derived class.	a normal varieties.
	A T II III	B. II, III, V
	C. III, IV, V	D. I, II, IV
20	If we areate a Clark Community	the default made of the file is
38.	If we create a file by 'ifstream', then	B. ios :: binary
	A. ios :: app C. ios :: out	D. ios :: in
	C. IOS OUL	D. 105 III

30	Mil. 1 64 6 H 1 1 1 0		
39.	Which of the following is correct? A. Constructors return values		
	B. Constructors can't be overloaded		
	C. Destructors do not have return values.		
	D. There can be any number of constructors	and destructors.	
			:Cthe emerates
40.	The order in which operands are evaluated	in an expression is predictable	if the operator
	is:		
	A.*	B. + D. &&	
	C. %	D. &&	
41.	What is the difference between overloaded	functions and overridden function	ons?
44.	A. Overloading is a dynamic or run-time	e binding and Overriding is stat	ic or compile-
	time binding		
	B. Redefining a function in a friend class	is called function overriding w	hile redefining
	B. Redefining a function in a friend class	on overloaded function.	
	a function in a derived class is called a	no binding and Overriding is di	mamic or run-
	C. Overloading is a static or compile-tir	ne omaing and Overriding is dy	
	time binding		looding while
	D. Redefining a function in a friend	class is called function over	loading wiffle
	redefining a function in a derived class	s is called as overridden function	
42.	Assume that we have constructor function	on for both Base and Derived	classes. Now
42.	consider the declaration:		
	main ()		
	Base *p = new Derived;	3	
	base p new Delived,	,	
	7 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1		
	In what sequence, the constructor will be ex		
	In what sequence, the constructor will be ex A. Derived class constructor is followed by		
		Base class constructor.	
	A. Derived class constructor is followed by	Base class constructor.	
	A. Derived class constructor is followed by B. Base class constructor is followed by De	Base class constructor.	
	A. Derived class constructor is followed by B. Base class constructor is followed by De C. Base class constructor is never called.	Base class constructor.	
43.	A. Derived class constructor is followed by B. Base class constructor is followed by De C. Base class constructor is never called.	Base class constructor.	Which of the
43.	 A. Derived class constructor is followed by B. Base class constructor is followed by De C. Base class constructor is never called. D. Derived class constructor is never called. If f(n)= n²log n and g(n)= n(log n)¹0 be following statement is correct? 	Base class constructor. rived class constructor. the two positive functions of n.	
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45.	What is the height of n-node, k-ary heap	9
- 1	A. $\Theta(\log_k n)$	B. \(\Theta(nk)\)
	C. $\Theta(k \log_2 n)$	D. Θ(n/k)
34		D. O(WK)
46.	The recurrence equation	
	T(1)=1	
	T(n) = 2T(n-1) + n, n >= 2	
	evaluates to:	
	A.2 ⁿ⁺¹ - n -2	B. 2 ⁿ - n
	C. 2 ⁿ⁺¹ -2n-2	$D. 2^n + n$
47	What is the minimum to 6	
47.	the worst case?	required to sort n elements using selection sort in
- 1		B O/ L
	A. $\Theta(n^2)$	B. $\Theta(n \log n)$
150	C. Θ(n)	D. $\Theta(n^2 \log n)$
48.	To find all pairs of shortest distance in a	graph, we can use:
	A. Recursion approach	B. Bidirectional search approach
	C. Merging approach	D. Dynamic programming approach
	3 3 11	,
49.	The number of spanning trees for a comp	
- 1	A.5 ⁷	B. 2 ⁵
	C. 7 ⁷	D. 7 ⁵
		The state of the s
50	A sorting technique is called stable if:	
50.	A sorting technique is called stable if:	
50.	A. It takes O (n log n) times.	propos pon dictinat alamenta
50.	A. It takes O (n log n) times. B. It maintains the relative order of occu	arrence non-distinct elements.
50.	A. It takes O (n log n) times. B. It maintains the relative order of occu C. It uses divide and conquer paradigm	arrence non-distinct elements.
50.	A. It takes O (n log n) times. B. It maintains the relative order of occu	arrence non-distinct elements.
	A. It takes O (n log n) times. B. It maintains the relative order of occu C. It uses divide and conquer paradigm D. It takes o (n) space.	
50.	A. It takes O (n log n) times. B. It maintains the relative order of occu C. It uses divide and conquer paradigm D. It takes o (n) space. Are we building the right product? This	statement refers to:
	A. It takes O (n log n) times. B. It maintains the relative order of occu C. It uses divide and conquer paradigm D. It takes o (n) space. Are we building the right product? This A. Testing	statement refers to: B. Software quality assurance
	A. It takes O (n log n) times. B. It maintains the relative order of occu C. It uses divide and conquer paradigm D. It takes o (n) space. Are we building the right product? This	statement refers to:
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51.	A. It takes O (n log n) times. B. It maintains the relative order of occu C. It uses divide and conquer paradigm D. It takes o (n) space. Are we building the right product? This A. Testing C. Validation	statement refers to: B. Software quality assurance
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ENTRANCE EXAMINATION-2017

+ Week

55.	Let $f(w,x,y,z) = \Sigma$ (0,4,5,7,8,9,13,15). Which	ch of the following expression are most
	equivalent to 1?	Those was
- 1	1. $x'y'z'+w'xy'+wy'z+xz$	4.
1	II. w'y'z'+wx'y'+xz	•
- 1	III. $x'y'z'+wx'y'+xyz+xy'z$	
	IV. $x'y'z' + wx'y' + w'y$	
1	A. i only	B. ii and iv
-	C. iii and iv	D. iv only
56.	How many 3 to 8 line decoders with an enable	e input are needed to construct a 6 to 64 line
	decoder without using any other logic gates?	
-	A.7	B. 8
	C. 9	D. 10
		I want representation of P is (F87B). The
57.	If P is a 16 bit signed integer. The 2's comp	dement representation of F is (167B)16. The
	2's complement representation of 8xP is:	D (E079).
	A.(987B) ₁₆	B. (F878) ₁₆ D. (C3D8) ₁₆
	C. (187B) ₁₆	D. (C3D8)16
50	Which one of the following expression doesn	't represent exclusive NOR of x and y?
58.	A. x ' XOR y '	B. x XOR y'
	C. x 'XOR y	D. xy XOR x ' y '
	C.X AORY	2.1., 2.0.1.
59.	A half adder is also known as:	30%
	A. NOR circuit	B. XNOR circuit
	C. NAND circuit	D. XOR circuit
		The state of the s
60.		lication of two 8 bit unsigned integers. The
	size of ROM required is:	5 (4) 0
	A. 256k x 16	B. 64k x 8
	C. 4k x 16	D. 64k x 16
(1	The contents of the flag register after eve	ecution of the following program by 8085
61.	microprocessor will be:	cution of the following program by over
	A. (54) _H	B. (45) _H
	C. (00) _H	D. (01) _H
	C. (00)H	D. (01)H
62.	Which of the following addressing modes are	suitable for program relocation at run time?
	I. Absolute addressing	II. Based addressing
	III. Relative addressing	IV. Indirect addressing
5		
	A. I and IV	B. I and II
	C. II and III	D. I, II and IV
63.	Consider a 4-way set-associative cache con	nsisting of 128 lines with a line size of 64
N Model	words. The CPU generates a 20-bit address	of a word in main memory. The number of
	bits in the TAG, LINE and WORD fields are	
P-1		
5	A. 9, 6, 5	B. 7, 7, 6

ENTRANCE EXAMINATION-2017

M.Tech. (C.E)

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64.	The term "cycle stealing" refers to:				
	A. Interrupt based data transfer C. DMA based data transfer	B. Polling mode data transfer D. Clock cycle overriding			
65.	Which of the following configuration	ns for CPU is in decreasing order of operating			
	specis?				
	 A. Hardwired control, vertical micropro 	ogramming, and horizontal microprogramming.			
	25. Franciwited control, norizontal micro	programming, and vertical microprogramming.			
	C. Horizontal microprogramming, verti	ical microprogramming, hardwired control.			
	D. Vertical microprogramming, horizon	ntal microprogramming, hardwired control.			
66.	The correct matching for the following				
	a. DMA I/O	1. High speed RAM			
	b. Cache	2. Disk			
	c. Interrupt I/O	3. Printer			
	d. Condition code registers	4. ALU			
	A. $a \rightarrow 4$, $b \rightarrow 3$, $c \rightarrow 1$, $d \rightarrow 2$	B. $a \rightarrow 4$, $b \rightarrow 3$, $c \rightarrow 2$, $d \rightarrow 1$			
	C. $a \rightarrow 2$, $b \rightarrow 3$, $c \rightarrow 4$, $d \rightarrow 1$	D. $a\rightarrow 2$, $b\rightarrow 1$, $c\rightarrow 3$, $d\rightarrow 4$			
67.	The language generated by the following	ng grammar is S→ aSa bSb ∈			
	A. $a^m b^n n >= 0, m>= 0$	B. a ⁿ b ^m n >=1, m>= 1			
	C. Odd length palindrome	D. Even length palindrome			
68.	Given the following grammars:				
	G₁: S→AB aaB				
	of. 5 AD and				
	A→aA €				
	B→bB €				
	$G_2: S \rightarrow A \mid B$				
	Annahlah				
	A→aAb ab B→abB €				
-	B→a0B €				
	Which of the following is correct?	*			
	A. G ₁ is ambiguous and G ₂ is unambigu				
	B. G ₁ is unambiguous and G ₂ is ambigu				
	C. Both G ₁ and G ₂ are ambiguous gram				
	D. Both G ₁ and G ₂ are unambiguous gra	animars			
69.	Which of the following four regular exp	pressions are equivalent?			
	I. (00) * (∈ + 0)	II. (00)*			
,	III. 0*	IV. 0(00)*			
TO I					
	A. I and II	B. II and III			
	C. I and III	D. III and IV			
70.	Let L be a set of all binary strings who	ose last two symbols are the same. The number of			
Code de la constante de la con	states in the minimum state determinist	ic finite state automata accepting L is:			
	A. 5	B. 6			

Which of the following statement is true? A. SLR parser is more powerful than LALR B. LALR parser is more powerful than canoni C. The parsers SLR, canonical LR and LALR D. Canonical LR parser is more powerful than Consider the grammar shown below: S→ CC C→ Cc d The grammar is: A. LL(1) C. LALR(1) but not SLR(1) Which of the following is a top down parser? A. Operator precedence parser C. Recursive decent parser Consider the following grammar S→ aB aAb A→ bAb a B→ Ab ∈ How many backtracks are required to generate a.1 a.3 Consider the grammar E → TE' A→ E/+TE' A→ FT' A→ E/+FT' A→ A/{E} A A A A A A A A A A A A A A A A A A A	al LR parser have the same power. LALR parser. B. SLR(1) but not LL(1) D. LR(1) but not LALR(1) B. An LR(k) parser D. An LALR(k) parser
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Transfer turi	
D. Set of all languages over Σ accepted by turn	ig machine.
C. Set of all languages over Σ	
B. Set of all regular languages over Σ	
oct of all strings over 2.	not countable?
Given $\Sigma = \{a, b\}$ which are after fall.	
C. Context free	D. Recursive
A. Context sensitive	B. Regular
A language accepted by pushdown automata):
11	
C. II, III and IV	D. I, III and IV
A. I, II and III	B. I, II and IV
III. baaaaabaaaab	V. Baaaaaba
Given the language L= {ab, aa, baa}, which of I. abaabaaabaa	II. aaaabaaaa
	Tr. L.

M.Tech. (C.E)

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79,	A process executar it	
1	A process executes the following code for (i=0: i=n: i++). for the following code	
	for (i=0; i <n; ();<="" fork="" i++)="" td=""><td></td></n;>	
1	The total number of child process cres	ited to:
- 1		B. 2"s1
	C. 2"	D. 2*1-1
		D. 2****4
80.	Where does the swap space resides?	
	A. RAM	
	C. ROM	B. On chip cache
		D. Disk
81.	The working set work it	
X	A. Segmentation	nemory management to implement the concept of
	C. Paging	D. Principle of locality
	Co. Faging	D. Thrashing
82.	A sehectule in the	
G.a.	A scheduling algorithm assigns prior	ity proportional to the waiting time of a process.
		The perticular to the waiting time of a process.
	process starts with priority zero	(the lowest priority). The scheduler re-evaluates the
	process priorities every T time units a	(the lowest priority). The scheduler re-evaluates the
	process priorities every T time units a of the following is TRUE if the proc	(the lowest priority). The scheduler re-evaluates the
	process priorities every T time units a	(the lowest priority). The scheduler re-evaluates the
	process priorities every T time units a of the following is TRUE if the proc zero?	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time
	process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the processor.	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm
	process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the B. This algorithm is equivalent to the following is the processor.	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm
	process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the B. This algorithm is equivalent to the C. This algorithm is equivalent to the second control of t	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm
	process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the B. This algorithm is equivalent to the following is the processor.	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm
83.	process states with priority zero process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the B. This algorithm is equivalent to the C. This algorithm is equivalent to the S. D. This algorithm is equivalent to the s.	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm.
83.	process states with priority zero process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the B. This algorithm is equivalent to the C. This algorithm is equivalent to the S. D. This algorithm is equivalent to the S. Consider a disk system with 100 cylin	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm shortest-remaining-time-first algorithm anders, the requests to access the cylinders occur in
83.	process states with priority zero process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the second to the	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm shortest-remaining-time-first algorithm anders, the requests to access the cylinders occur in 9, 73, 2, 15, 6, 20
83.	process states with priority zero process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the second to the	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm shortest-remaining-time-first algorithm unders. the requests to access the cylinders occur in 9, 73, 2, 15, 6, 20 at cylinder 50. What is the time taken to satisfy all
83.	process states with priority zero process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the set. This algorithm is equivalent to the set	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm shortest-remaining-time-first algorithm anders, the requests to access the cylinders occur in 9, 73, 2, 15, 6, 20
83.	process states with priority zero process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the B. This algorithm is equivalent to the C. This algorithm is equivalent to the D. This algorithm is equivalent to the S. D. This algorithm is equivalent to the S. Consider a disk system with 100 cylis the following sequence: 4, 34, 10, 7, 19. Assuming that the head is currently a request if it takes 1 ms to move from first policy is used?	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm shortest-remaining-time-first algorithm unders. the requests to access the cylinders occur in 9, 73, 2, 15, 6, 20 at cylinder 50. What is the time taken to satisfy all one cylinder to adjacent one and shortest seek time
83.	process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the B. This algorithm is equivalent to the B. This algorithm is equivalent to the B. This algorithm is equivalent to the S. D. This algorithm is equivalent to the S. D. This algorithm is equivalent to the S. D. This algorithm is equivalent to the S. Consider a disk system with 100 cylisthe following sequence: 4, 34, 10, 7, 19. Assuming that the head is currently a request if it takes 1 ms to move from first policy is used? A. 95 ms	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm shortest-remaining-time-first algorithm nders. the requests to access the cylinders occur in 9, 73, 2, 15, 6, 20 at cylinder 50. What is the time taken to satisfy all one cylinder to adjacent one and shortest seek time B. 110 ms
83.	process states with priority zero process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the B. This algorithm is equivalent to the C. This algorithm is equivalent to the D. This algorithm is equivalent to the S. D. This algorithm is equivalent to the S. Consider a disk system with 100 cylis the following sequence: 4, 34, 10, 7, 19. Assuming that the head is currently a request if it takes 1 ms to move from first policy is used?	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm shortest-remaining-time-first algorithm unders. the requests to access the cylinders occur in 9, 73, 2, 15, 6, 20 at cylinder 50. What is the time taken to satisfy all one cylinder to adjacent one and shortest seek time
	process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the set. This algorithm is equivalent to	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm shortest-remaining-time-first algorithm nders. the requests to access the cylinders occur in 9, 73, 2, 15, 6, 20 at cylinder 50. What is the time taken to satisfy all one cylinder to adjacent one and shortest seek time B. 110 ms
83.	process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the B. This algorithm is equivalent to the C. This algorithm is equivalent to the S. D. This algorithm is equivalent to the S. D. This algorithm is equivalent to the S. Consider a disk system with 100 cylist the following sequence: 4, 34, 10, 7, 15. Assuming that the head is currently a request if it takes 1 ms to move from first policy is used? A. 95 ms C. 119 ms Banker's algorithm is applied for:	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm shortest-remaining-time-first algorithm nders. the requests to access the cylinders occur in 19, 73, 2, 15, 6, 20 at cylinder 50. What is the time taken to satisfy all one cylinder to adjacent one and shortest seek time 110 ms D. 276 ms
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	process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the B. This algorithm is equivalent to the C. This algorithm is equivalent to the S. D. This algorithm is equivalent to the S. D. This algorithm is equivalent to the S. Consider a disk system with 100 cylist the following sequence: 4, 34, 10, 7, 15. Assuming that the head is currently a request if it takes 1 ms to move from first policy is used? A. 95 ms C. 119 ms Banker's algorithm is applied for:	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm shortest-remaining-time-first algorithm nders. the requests to access the cylinders occur in 19, 73, 2, 15, 6, 20 at cylinder 50. What is the time taken to satisfy all one cylinder to adjacent one and shortest seek time 110 ms D. 276 ms
	process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the B. This algo	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm shortest-remaining-time-first algorithm anders. the requests to access the cylinders occur in 9, 73, 2, 15, 6, 20 at cylinder 50. What is the time taken to satisfy all one cylinder to adjacent one and shortest seek time B. 110 ms D. 276 ms B. Deadlock detection D. Both A and C
84.	process priorities every T time units a of the following is TRUE if the processor? A. This algorithm is equivalent to the B. This algo	(the lowest priority). The scheduler re-evaluates the nd decides the next process to schedule. Which one esses have no I/O operations and all arrive at time round-robin algorithm first-come-first-serve algorithm shortest-job-first algorithm shortest-remaining-time-first algorithm nders. the requests to access the cylinders occur in 9, 73, 2, 15, 6, 20 at cylinder 50. What is the time taken to satisfy all one cylinder to adjacent one and shortest seek time B. 110 ms D. 276 ms B. Deadlock detection D. Both A and C
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ENTRANCE EXAMINATION-201

Paper Code No- BM51

Question Booklet No. 651462

ENTRANCE EXAMINATION-2018

M.Tech. (Computer Engineering)
[Set B]

ROLL NO.					
	_	 	_		_

Signature of Invigilator

Time: 2 Hours

Total Marks: 85

Instructions to Candidates

- Do not write your name or put any other mark of identification anywhere in the OMR Response Sheet. IF ANY MARK OF IDENTIFICATIONS IS DISCOVERED ANYWHERE IN OMR RESPONSE SHEET, the OMR sheet will be cancelled, and will not be evaluated.
- 2. This Question Booklet contains the cover page and a total of 85 Multiple Choice Questions of 1mark each.
- Space for rough work has been provided at the beginning and end. Available space on each page may also be used for rough work.
- 4. There is negative marking in Multiple Choice Questions. For each wrong answer, 0.25 marks will be deducted.
- 5. USE OF CALCULATOR IS NOT PERMITTED.
- 6. USE/POSSESSION OF ELECTRONIC GADGETS LIKE MOBILE PHONE, iPhone, iPad, pager ETC. is strictly PROHIBITED.
- Candidate should check the serial order of questions at the beginning of the test. If any question is found missing in the serial
 order, it should be immediately brought to the notice of the Invigilator. No pages should be torn out from this question
 booklet.
- 8. Answers must be marked in the OMR response sheet which is provided separately. OMR Response sheet must be handed over to the invigilator before you leave the seat.
- The OMR response sheet should not be folded or wrinkled. The folded or wrinkled OMR/response Sheet will not be evaluated.
- 10. Write your Roll Number in the appropriate space (above) and on the OMR Response Sheet. Any other details, if asked for, should be written only in the space provided.
- 11. There are four options to each question marked A, B, C and D. Select one of the most appropriate option and fill up the corresponding oval/circle in the OMR Response Sheet provided to you. The correct procedure for filling up the OMR Response Sheet is mentioned below.
- 12. Use Black or Blue Ball Pen only for filling the ovals/circles in OMR Response Sheet. Darken the selected oval/circle completely. If the correct answer is 'B', the corresponding oval/circle should be completely filled and darkened as shown

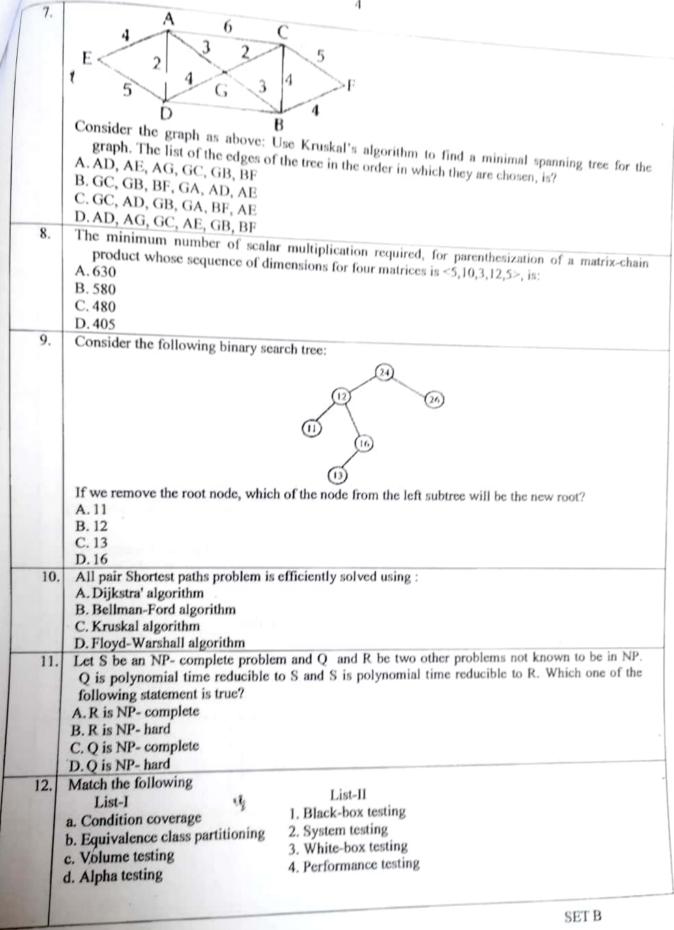
below.



WRONG METHOD

A X C D A Y C D A D C D A D C D A D C D

```
SET-B
     A process executes the code
      fork ();
     fork ();
      fork ();
      The total number of child processes created is:
     B. 4
     C. 7
     D. 8
2.
     If no exception is thrown:
     A. a catch block will cause an error
     B, the first catch block coded will execute
     C, the last catch block coded will execute
     D, any catch blocks coded will be bypassed
     What is the time complexity of Bellman-Ford single-source shortest path algorithm on a complete
        graph of n vertices?
     A.\Theta(n^2)
     B. \Theta(n^2 \log n)
     C.\Theta(n^3)
     D. O(n log n)
     Match the following
     List-I
    a. Prim's algorithm for minimum spanning tree
    b. Floyd-Warshall algorithm for all pairs shortest paths
    c. Merge sort
    d. Hamiltonian circuit
    List-II
    1. Backtracking
    2. Greed method
   3. Dynamic programming
   4. Divide and conquer
   Codes:
   B.
   C.
          2
  Consider the following functions:
  f(n) = 2^n
  g(n) = n!
  h(n) = n^{\log n}
  Which of the following statements about the asymptotic behavior of f(n), g(n) and h(n) is true?
  A. f(n) = O(g(n)); g(n) = O(h(n))
  B. f(n) = \Omega(g(n)); f(n) = O(h(n))
  C. g(n)=O(f(n)); h(n)=O(f(n))
  D. h(n) = O(f(n)); g(n) = \Omega(h(n))
  Suppose T(n)=2T(n/2)+n, Y(0)=1. Which one of the following is False?
  A. T(n) = O(n^2)
  B. T(n) = \theta(n \log n)
  C. T(n) = \Omega(n^2)
  D. T(n) = O(n \log n)
   M.Tech. (C.E)
                                                                                         SET-B
```



	Codes:
	a b c d A. 2 3 1 4
1	
	D a
13.	
	A. Re-constructing the original source code from the existing machine code program and modifying
	it to make it more user friendly
	B. Scrapping the source code of a software and re-writing it entirely from scratch
	C. Re-organizing and modifying existing software systems to make them more maintainable
	D. Translating source code of an existing software to a new machine language.
	source code of an existing software to a new machine language.
14.	testing testing testing testing testing testing testing the software products full confective after the
	changes during maintenance?
	A. Path testing
	B. Integration testing
	C. Unit testing
	D. Regression testing
15.	Which one of the following is not a software myth?
-	A. Once we write the program and get it to work, our job is done
	B. Project requirement continually changes, but change can be easily accommodated because
	software is flexible
	C. If we get behind schedule, we can add more programmers and catch up
	D.If an organization does not understand how to control software project internally, it will
	invariably struggle when it outsources software projects
16.	58000 LOC gaming software is developed with effort of three person year. What is the productivity of person-month? A. 1.0 KLOC B. 1.6 KLOC C. 4.8 KLOC D. 4.2 KLOC
17.	For a well understood data processing application. It is best to use the:
	A. Waterfall model
	B. Prototyping model
	C. Evolutionary model
	D. Spiral model
18.	The above synchronous sequential circuit built using JK flip-flops is initialized with Q ₂ Q ₁ Q ₀ = 000. The state sequence for this circuit for the next 3 clock cycles is: A. 001, 010, 011 B. 111, 110, 101 C. 100, 110, 111 D. 100, 011, 001
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	M.Tech. (C.E)
2.50	

	A RAM chip has a capacity of 10 with enable line needed to const	24 words	of 8 bit	s each (1	V = 0) T	
	with enable line needed to const	ruct a 16	K ×16 R	AM from	1K × 8	he number of 2×4 decoders
THE	B. 5					IXAIVI IS;
	c .6					
	D. 7					
20.	Consider a full adder with the follo					
	(a) x=1, y=0 and Ci(carry input)	wing inpi	ut values	:		
	(b) $x=0$, $y=1$ and $C_i = 1$					
	Compute the values of S(sum) a	nd C0 (ca	rry outp	ut) for the	ahous :	
			, cutp	at) for the	above	nput values:
	B. S=0, C0= 0 and S=1, C0= 1					
	C. S=1, C0=1 and S=0, C0=0 D. S=0, C0=1 and S=1, C0=0					
21.		statemen	t?			
	A. In memory - mapped I/O, the CF	U can ma	anipulate	: I/O data	residing	in interface registers that are
	B. The isolated I/O method isolates affected by interface address assi	memory	and I/O	addresses	s so that	memory address range is not
	C. In asynchronous serial transfer o	f data the	two unit	e chara a	00mmor	alast
	D. In synchronous serial transmission	on of data	the two	is share a units hav	e differe	nt clocks
22.	AB			unito nu t	o differe	in clocks.
	CD /	00	01.	11	10	
	00	0	1	1	0	
	01	0	1	1	1	
	11	1	1	1	1	
	10	0	1	1	0	
	The Boolean function for the above	Karnaugl	n map is:			•
	A.(A+C).D+B					
	B. (A + B).C + D C. (A + D).C + B					
	D. (A + C).B + D					
22		n all its in	nnut are	logic '0'.	The gate	e is either:
23.			uput aic			
23.	A. A NAND or an EX-OR gate		iiput aic i			
23.	B. A NOT or an EX-NOR gate		apur are i			
	B. A NOT or an EX-NOR gate C. An OR or an EX-NOR gate		input are			
	B. A NOT or an EX-NOR gate C. An OR or an EX-NOR gate D. An AND or an EX-OR gate				ment nu	mbars. Their product in 2's
	B. A NOT or an EX-NOR gate C. An OR or an EX-NOR gate D. An AND or an EX-OR gate Let A = 11111010 and B = 000010				ment nu	umbers. Their product in 2's
24.	B. A NOT or an EX-NOR gate C. An OR or an EX-NOR gate D. An AND or an EX-OR gate Let A = 11111010 and B = 000010 complement is:				ement nu	mbers. Their product in 2's
24.	B. A NOT or an EX-NOR gate C. An OR or an EX-NOR gate D. An AND or an EX-OR gate Let A = 11111010 and B = 000010 complement is: A. 11000100				ement nu	mbers. Their product in 2's
24.	B. A NOT or an EX-NOR gate C. An OR or an EX-NOR gate D. An AND or an EX-OR gate Let A = 11111010 and B = 000010 complement is: A. 11000100 B. 10011100				ement nu	mbers. Their product in 2's
24.	B. A NOT or an EX-NOR gate C. An OR or an EX-NOR gate D. An AND or an EX-OR gate Let A = 11111010 and B = 000010 complement is: A. 11000100 B. 10011100 C. 10100101 D. 11010101	10 be two			ement nu	ambers. Their product in 2's
24.	B. A NOT or an EX-NOR gate C. An OR or an EX-NOR gate D. An AND or an EX-OR gate Let A = 11111010 and B = 000010 complement is: A. 11000100 B. 10011100	10 be two			ement nu	mbers. Their product in 2's
24.	B. A NOT or an EX-NOR gate C. An OR or an EX-NOR gate D. An AND or an EX-OR gate Let A = 11111010 and B = 000010 complement is: A. 11000100 B. 10011100 C. 10100101 D. 11010101	10 be two			ement nu	mbers. Their product in 2's
24.	B. A NOT or an EX-NOR gate C. An OR or an EX-NOR gate D. An AND or an EX-OR gate Let A = 11111010 and B = 000010 complement is: A. 11000100 B. 10011100 C. 10100101 D. 11010101 A positive AND gate is also a negati A. NAND gate B. NOR gate	10 be two			ement nu	ambers. Their product in 2's
24.	B. A NOT or an EX-NOR gate C. An OR or an EX-NOR gate D. An AND or an EX-OR gate Let A = 11111010 and B = 000010 complement is: A. 11000100 B. 10011100 C. 10100101 D. 11010101 A positive AND gate is also a negation.	10 be two			ement nu	ambers. Their product in 2's

26	and of the following circuit can be used as parallel to serial converter:
	A. Multiplexer
	B. Demultiplexer
1	C. Decoder
1	D. Digital counter
27	Task.
1 21	and data blocks into the physical main memory when
1	dicy are required for execution, are called:
	A. Associative Memory Mapping techniques
	B. Main Memory techniques
	C. Virtual Memory techniques D. Cache Memory techniques
	b. Cache Memory techniques
28.	The contents of the flag register after execution of the full size of the first size of the flag register after execution
	The contents of the flag register after execution of the following program by 8085 microprocessor will be:
	SUB A
	MVI B, (01) _H
	DCR B
	HLT
	A. (54) _H
	B. (00) _H
	C. (01) _H
	D. (45) _H
20	
29.	Which of the following addressing modes are suitable for program relocation at run time?
	I. Absolute addressing II. Base addressing
	III. Relative addressing IV. Indirect addressing
	A. I and IV
	B. I and II
	C. II and III
	D. I, II and IV
30.	Which of the following and associated the following associated a
30.	Which of the following code occupies more memory and worst performance in Booth's algorithm? A. 000111
20	B. 111000
-	C. 101010
	D. 110011
	D. 110011
31.	The minimum number of D flip flops needed to design a mod-258 counter, is:
	A.9
	B. 8
	C. 512
- 1	D. 258
	For computers based on three-address instruction formats, each address field can be used to specify
	which of the followings
	S1: A memory operand
	S2: A processor register
	S3: An implied accumulator register
1000	A. Either S1 or S2
	B. Either S2 or S3
	C. Only S2 and S3
_	D. All S1, S2 and S3 SET B
743 3	M.Tech. (C.E)

Consider the grammar shown below. C → c C d The grammar is: A. LL(1) B. SLR(1) but not SLR(1) D. LR(1) but not SLR(1) D. LR(1) but not SLR(1) D. LR(1) but not LL(1) C. LALR(1) but not LL(1) C. LALR(1) but not LL(1) C. LALR(1) but not LL(1) D. LR(1) but not LL(1) C. LALR(1) but not LL(1) C. LALR(1) but not LL(1) D. LR(1) but not LL(1) C. LALR(1) but not LL(1) A. Ambiguous B. Left-recursive C. Right-recursive D. An operator-grammar 48. A system uses 3 page frames for storing process pages in main memory. It uses the Least Recently used (LRU) page replacement policy. Assume that all the page frames are initially empty. What below? A. 4 4, 7, 6, 1, 7, 6, 1, 2, 7, 2 B. 5 C. 6 D. 7 49. An operating system uses shortest remaining time first scheduling algorithm for pre-emptive scheduling of processes. Consider the following set of processes with their arrival times and CPU burst times (in milliseconds): Process Arrival Burst time Pl 0 12 P2 2 4 P3 3 6 P4 8 5 The average waiting time (in milliseconds) of the processes is———— A. 4.5 B. 5.5 C. 6.5 D. 7.5 50. Consider a main memory with five page frames and the following sequence of page references: 3, 8, 2, 3, 9, 1, 6, 3, 8, 9, 3, 6, 2, 1, 3. Which one of the following is true with respect to page replacement policies First-In-First Out (FiFO) and Least Recently Used (LRU)? A. Both incur the same number of page faults than LRU C. LRU incurs 2 more page faults than LRU C. LRU incurs 2 more page faults than LRU C. IRU incurs 2 more page faults than LRU C. RU incurs 2 more page faults than LRU S. FIFO incurs 1 more page faults than LRU A. RR B. SIF C. FCFS D. Priority	46. Consider the grammar shown below. C → c C A ← C ∈ C A ← C ∈ C ∈ C ← C ∈ C ∈ C ← C ∈ C ∈ C ∈ C				10			
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48. A system uses 3 page frames for storing process pages in main memory. It uses the Least Recently Used (LRU) page replacement policy. Assume that all the page frames are initially empty. What below? A. 4 4, 7, 6, 1, 7, 6, 1, 2, 7, 2 B. 5 C. 6 D. 7 49. An operating system uses shortest remaining time first scheduling algorithm for pre-emptive scheduling of processes. Consider the following set of processes with their arrival times and CPU burst times (in milliseconds): Process Arrival Burst time Pl 0 12 P2 2 4 4 P3 3 6 6 P4 8 5 5 The average waiting time (in milliseconds) of the processes is————————————————————————————————	As system uses 3 page frames for storing process pages in main memory. It uses the Least Recently Used (LRU) page replacement policy. Assume that all the page frames are initially empty. What below? A. 4	1						
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The average waiting time (in milliseconds) of the processes is————————————————————————————————	The average waiting time (in milliseconds) of the processes is————————————————————————————————			P2	2			
The average waiting time (in milliseconds) of the processes is————————————————————————————————	The average waiting time (in milliseconds) of the processes is————————————————————————————————			P3	3	6		
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51. Which scheduling algorithm gives a minimum average waiting time? A. RR B. SJF C. FCFS	Mhich scheduling algorithm gives a minimum average waiting time? A. RR B. SJF C. FCFS D. Priority							
A. RR B. SJF C. FCFS	A. RR B. SJF C. FCFS D. Priority			14				
A. RR B. SJF C. FCFS	A. RR B. SJF C. FCFS D. Priority	51.	Which scheduling algo	rithm gives a mini	imum average v	vaiting time?		
B. SJF C. FCFS	B. SJF C. FCFS D. Priority			B. 40 4 111111		many mile.		
C. FCFS	C. FCFS D. Priority							
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D. Priority	NT LOD							
	M.Tech. (C.E) SET B		D. Priority					
	M.Tech. (C.E)	2	THE SHAPE OF STREET					

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In which of the following page replacement policies, Belady's anomaly may occur?
 A. FIFO
 B. Optimal
 C. LRU
D. MRU
An operating system contains three user processes each requiring 2 units of resource R. The
   minimum number of units of R such that no deadlocks will ever arise, is:
 B. 4
 C. 5
 D. 6
Relation R has eight attributes ABCDEFGH. Fields of R contain only atomic values.
 F = \{CH \rightarrow G, A \rightarrow BC, B \rightarrow CFH, E \rightarrow A, F \rightarrow EG\} is a set of functional dependencies (FDs) so
 that F is exactly the set of FDs that hold for R. How many candidate keys does the relation R
A. 3
B. 4
C. 5
D. 6
Which of the following is TRUE?
A. Every relation is 3NF is also in BCNF
B. A relation R is in 3NF if every non-prime attribute of R is fully functionally dependent on every
   key of R
C. Every relation in BCNF is also in 3NF
D. No relation can be in both BCNF and 3NF
Consider the following transactions with data items P and Q initialized to zero:
T1: read (P);
read (Q);
 if P = 0 then Q := Q + 1;
write (Q).
T2: read (Q);
read (P) if Q = 0 then P := P + 1;
write (P).
Any non-serial interleaving of T1 and T2 for concurrent execution leads to:
A. A serializable schedule
B. A schedule that is not conflict serializable
C. A conflict serializable schedule
D. A schedule for which precedence graph cannot be drawn
Which one of the following is not a part of the ACID properties of database transactions?
A. Atomicity
B. Consistency
C. Isolation
D. Deadlock
Which of the following concurrency control protocols ensure both conflict serializability and
   freedom from deadlock?
                                  II. Timestamping ordering
I. Two phase locking
A. I only
B. II only
C. Both I and II
D. Neither I nor II
                                                                                     SET B
```

	12	
59.	Which of the following is/are example(s) of stateful application layer protocol: II. FTP II. FTP	
	I. HTTP	0
	III. TCP	87
1		
	20.1 and 11 only	
	B. II and III only	
	C. II and IV only	
	D.IV only	
60.	Which one of the following fields of an IP header is NOT modified by a typical A. Checksum	
	A. Checksum	d IP router?
	B. Source address	
	C. Time To Live	
	D. Length	
61.		
0.1.	The transport layer protocols used for real time multimedia, file transfer	er, DNS and email,
	respectively are.	
	A. TCP, UDP, UDP and TCP	
	B. UDP, TCP, TCP and UDP	
	C. UDP, TCP, UDP and TCP	
-	D, TCP, UDP, TCP and UDP	
62.	The first the first has a subject mask of 2555.255.246.0, then	what is the maximum
	number of hosts per subnet?	
	A. 1022	
	B. 1023	
	C. 2046	
	D.2047	
63.	One of the following is not the type of the active attack.	
	A. Denial of Service	
	B. Masquerades	
	C. Fabrication	
	D. Traffic analysis	
64.	What is the value of 3 ¹² mod 11.	
	A.9	
	B. 10	
	C. 11	
	D. 12	
	D. 12	
65.	Digital signature is signed by a sender using:	
	A. Sender public key	
	B. Sender private key	
	C. Receiver's public key	
	D. Receiver's private key	
1		
66.	Consider the following statements: P: Good Laptops are not cheap	
	Q: Cheap Laptops are not good	
	L: P implies Q	
	M: Q implies P	
	N: P is equivalent to O	
	Which one of the following about L, M, and N is correct?	
- 3	A. Only L is true.	
	B. Only M is true.	
	C. Only N is true.	
	D. L, M and N are true.	4
HE	M.Tech. (C.E)	
	(C.L)	SET B

SET B

67	13
	Consider the baring the consideration of the consid
i	Consider the basic block given below. a=b+c
	a-b+c
	c=a+d
	d = b + c
	e=d-b
	a = e - b
	The minimum number of nodes and edges present in the DAG representation of the above basic
	block respectively area.
	A. 6 and 6
	B. 8 and 10
	C. 9 and 12
	D. 4 and 4
	or and 4
68	A grouph with
00	Stupii Willi Ulic Verrey and no adose in
	A. Multigraph
	B. Digraph
	C. Isolated graph
	D. Trivial graph
69.	Total
	10. V d(Kaniy (d) //~ Cold (d))
	$B. \forall d(\sim Rainy(d) \rightarrow Cold(d))$
	C. $\exists d(\sim Rainy(d) \rightarrow Cold(d))$
	D. $\exists d(\sim Rainy(d) \land \sim Cold(d))$
	(2) (1)
70.	Let an represent the number of bit strings of length n containing two consecutive 1s. What is the
	recurrence relation for a _n ?
	A. $a_{n-2} + a_{n-1} + 2^{n-2}$
	B. $a_{n-2} + 2a_{n-1} + 2^{n-2}$
	C. $2a_{n-2} + a_{n-1} + 2^{n-2}$
	$C \cdot 2a_{n-2} + a_{n-1} + 2$
	$D. 2a_{n-2} + 2a_{n-1} + 2^{n-2}$
71	The worst case supplied times of Incertion cort. Marge cort and Quick cort. respectively, are:
71.	The worst case running times of Insertion sort, Merge sort and Quick sort, respectively, are:
71.	A. $\Theta(n \log n)$, $\Theta(n \log n)$ and $\Theta(n^2)$
71.	A. $\Theta(n \log n)$, $\Theta(n \log n)$ and $\Theta(n^2)$ B. $\Theta(n^2)$, $\Theta(n^2)$ and $\Theta(n \log n)$
71.	A. $\Theta(n \log n)$, $\Theta(n \log n)$ and $\Theta(n^2)$ B. $\Theta(n^2)$, $\Theta(n^2)$ and $\Theta(n \log n)$ C. $\Theta(n^2)$, $\Theta(n \log n)$ and $\Theta(n \log n)$
71.	A. $\Theta(n \log n)$, $\Theta(n \log n)$ and $\Theta(n^2)$ B. $\Theta(n^2)$, $\Theta(n^2)$ and $\Theta(n \log n)$
71.	A. $\Theta(n \log n)$, $\Theta(n \log n)$ and $\Theta(n^2)$ B. $\Theta(n^2)$, $\Theta(n^2)$ and $\Theta(n \log n)$ C. $\Theta(n^2)$, $\Theta(n \log n)$ and $\Theta(n \log n)$ D. $\Theta(n^2)$, $\Theta(n \log n)$ and $\Theta(n^2)$
71.	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three,
	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three,
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	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three, then the number of edges in G is A. 24 B. 20
	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three, then the number of edges in G is A. 24 B. 20 C. 32
	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three, then the number of edges in G is A. 24 B. 20 C. 32 D. 64
72.	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three, then the number of edges in G is A. 24 B. 20 C. 32 D. 64 I. He and each student must choose 5 courses to put in his/her.
	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three, then the number of edges in G is A. 24 B. 20 C. 32 D. 64 It blee and each student must choose 5 courses to put in his/her.
72.	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three, then the number of edges in G is A. 24 B. 20 C. 32 D. 64 There are 8 different courses available, and each student must choose 5 courses to put in his/her available. There are 8 different courses available, and each students such that, no matter what they choose,
72.	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three, then the number of edges in G is A. 24 B. 20 C. 32 D. 64 There are 8 different courses available, and each student must choose 5 courses to put in his/her than the student what they choose, and the students such that, no matter what they choose, the students what they choose.
72.	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three, then the number of edges in G is A. 24 B. 20 C. 32 D. 64 There are 8 different courses available, and each student must choose 5 courses to put in his/her plan of studies. What is the minimum number of students such that, no matter what they choose, there will be at least 10 students with the same plan?
72.	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three, then the number of edges in G is A. 24 B. 20 C. 32 D. 64 There are 8 different courses available, and each student must choose 5 courses to put in his/her plan of studies. What is the minimum number of students such that, no matter what they choose, there will be at least 10 students with the same plan? A. 499
72.	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three, then the number of edges in G is A. 24 B. 20 C. 32 D. 64 There are 8 different courses available, and each student must choose 5 courses to put in his/her plan of studies. What is the minimum number of students such that, no matter what they choose, there will be at least 10 students with the same plan? A. 499 B. 500
72.	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three, then the number of edges in G is
72.	 A. Θ(n log n), Θ(n log n) and Θ(n²) B. Θ(n²), Θ(n²) and Θ(n Log n) C. Θ(n²), Θ(n log n) and Θ(n log n) D. Θ(n²), Θ(n log n) and Θ(n²) Let G be a connected planar graph with 10 vertices. If the number of edges on each face is three, then the number of edges in G is A. 24 B. 20 C. 32 D. 64 There are 8 different courses available, and each student must choose 5 courses to put in his/her plan of studies. What is the minimum number of students such that, no matter what they choose, there will be at least 10 students with the same plan? A. 499 B. 500

74.	The preorder traversal sequence of a binary search tree is 30, 20, 10, 15, 25, 23, 39, 35, 42. Which A. 10,20,15,23,25,35,42,39,30 B. 15,10,25,23,20,42,35,39,30 C. 15,00,25,23,20,42,35,39,30
1	one of the following sequence of a binary see
	one of the following is the postorder traversal sequence of the same tree? A. 10,20,15,23,25,35,42,39,30 B. 15,10,25,23,20,42,35,39,30 C. 15 20 10 23,20,42,35,39,30
	B. 15.10.25.22.20, 35,42,39,30 Which
1	B. 15,10,25,23,20,42,35,39,30 C. 15,20,10,23,25,42,35,39,30 D. 15,10,23,25,23,25,42,35,39,30
	D 15 10 22 25,42,35,39,30
	D. 15,10,23,25,42,35,39,30
75.	You have
	You have an array of n elements. Suppose you implement quick sort by always choosing the central is: A O(n ²)
	element of the array as the pivot. Then the tightest upper bound for the worst case performance A. O(n ²)
	is: Then the tightest upper bound for the worst case and
	A. O(II)
2	B. O (n log n)
	$C. \theta(n \log n)$
	$D. \theta(n^2)$
76.	Which of the following statements is/are TRUE for undirected graphs? P: Number of odd degree vertices is even
	P: Number of odd degree vertices is even. O: Sum of degrees of the statements is/are TRUE for undirected graphs?
	Q: Sum of degrees of all vertices is even.
	A. P only
	B. Q only
	C. Both P and Q
	D. Neither P nor Q
	B. Reidler F nor Q
77.	Consider a may been assessed the
100	Consider a max heap, represented by the array: 40, 30, 20, 10, 15, 16, 17, 8, 4. Now consider that a
	and 33 is miscred into this near, After insertion, the new hear is
	A. 40, 30, 20, 10, 15, 16, 17, 8, 4, 35
	B. 40, 35, 20, 10, 30, 16, 17, 8, 4, 15
	C. 40, 30, 20, 10, 35, 16, 17, 8, 4, 15
70	D. 40, 35, 20, 10, 15, 16, 17, 8, 4, 30
78.	The number of integers between 1 and 500 (both inclusive) that are divisible by 3 or 5 or 7 is:
	A.269
	B. 270
	C.271
	D.272
70	Consider a hash table of size 11 that uses open addressing with linear probing. Let h(k) = k mod 11
79.	
	be the hash function used. A sequence of records with keys: 43 36 92 87 11 4 71 13 14
	43 30 92 67 11 4 71 13 14
	are inserted into an initially empty hash table, the bins of which are indexed from zero to ten. What
	is the index of the bin into which the last record is inserted?
	A.3
	B.4
	C. 6
	D.7
	tions gearch tree nodes containing the key values 10,
80.	When searching from the key value 60 in a binary search tree, nodes containing the key values 10, 20, 40, 50, 70, 80, 90 are traversed, not necessarily in the order given. How many different orders are possible in which these key values can occur on the search path from the root to the node containing the value 60?
	A.35
	B. 64
117	C. 128
	D. 196
	M.Tech (C.F.)

81.	The following sequence of operation is performed on stack: PUSH(10), PUSH(20), POP, PUSH(20), POP, POP, POP, POP, POP, POP, POP, PO
	sequence of values popped out is:
	A. 20,10,20,10,20
	B. 20,20,10,10,20
	C. 10,20,20,10,20
1	D. 20,10,10,20,10
82.	Runtime polymorphism is achieved by:
	A. Friend function
	B. Virtual function
	C. Operator overloading
	D. Function overloading
83.	
	I. Function overloading is done at compile time.
	II. Protected members are accessible to the member of derived class.
	III. A derived class inherits constructors and destructors.
	IV. A friend function can be called like a normal function.
1 =	V. Nested class is a derived class.
	A. I, II, III
	B. II, III, IV
	C. III, IV, V
	D.I, II IV
84.	
	A>
	B. =
	C. () D. *
05	Member functions, when defined within the class specification:
85.	A. Are always inline
	B. Are not inline
	C. Are inline by default, unless they are too big or too complicated
	D. Are not inline by default
100	D. Fue not mine of warmen

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