Wallace Multiplier 4 Bit						
S.No.	Syn/Imp	#LUTs	Slack	Power	Frequency	
1	Synthesis	17	1.928ns	4.282W	1/9 ns-1	
1	Implementation	20	0.211ns	4.267W	1/9115-1	
2	Synthesis	17	2.928ns	4.282W	1/10 ns-1	
2	Implementation	18	0.845ns	4.248W		
3	Synthesis	17	3.928ns	4.282W	1/11 ns-1	
3	Implementation	17	1.736ns	4.242W		
4	Synthesis	17	4.928ns	4.282W	1/12 ns-1	
4	Implementation	17	2.833ns	4.242W		
5	Synthesis	17	5.928ns	4.282W	1/13 ns-1	
3	Implementation	17	3.565ns	4.240W	1/13/15-1	

Vedic Multiplier 4 Bit						
S.No.	Syn/Imp	#LUTs	Slack	Power	Frequency	
1	Synthesis	22	2.745ns	3.759W	1/9 ns-1	
1	Implementation	22	0.846ns	3.868W	1/9115-1	
2	Synthesis	22	3.745ns	3.759W	1/10 ns-1	
2	Implementation	22	1.853ns	3.851W		
3	Synthesis	22	4.745ns	3.759W	1/11 ns-1	
3	Implementation	22	2.962ns	3.851W		
4	Synthesis	22	5.754ns	3.759W	1/12 ns-1	
4	Implementation	22	3.682ns	3.85W		
5	Synthesis	22	6.754ns	3.759W	1/13 ns-1	
	Implementation	22	4.870ns	3.847W	1/13 [15-1	

Booth Multiplier 4 Bit						
S.No.	Syn/Imp	#LUTs	Slack	Power	Frequency	
1	Synthesis	13	0.071ns	0.116W	1/3 ns-1	
1	Implementation	13	0.630ns	0.115W	1/3/115-1	
2	Synthesis	13	1.071ns	0.113W	1/4 ns-1	
2	Implementation	13	1.463ns	0.112W		
3	Synthesis	13	2.071ns	0.111W	1/5 ns-1	
3	Implementation	13	2.376ns	0.111W		
4	Synthesis	13	3.071ns	0.110W	1/6 ns-1	
4	Implementation	13	3.194ns	0.110W		
5	Synthesis	13	4.071ns	0.109W	1/7 ns-1	
	Implementation	13	4.194ns	0.109W	1// NS-1	

Wallace Multiplier 8 Bit						
S.No.	Syn/Imp	#LUTs	Slack	Power	Frequency	
1	Synthesis	94	3.929ns	14.926W	1/14 ns-1	
1	Implementation	94	0.859ns	14.887W	1/14 115-1	
2	Synthesis	94	4.929ns	14.926W	1/15 ns-1	
2	Implementation	93	2.083ns	14.846W		
3	Synthesis	94	5.929ns	14.926W	1/16 ns-1	
3	Implementation	93	2.817ns	14.856W		
4	Synthesis	94	6.929ns	14.926W	1/17 ns-1	
4	Implementation	93	3.674ns	14.73W		
5	Synthesis	94	7.929ns	14.926W	1/18 ns-1	
	Implementation	93	4.650ns	14.678W	1/10 112-1	

Vedic Multiplier 8 Bit						
S.No.	Syn/Imp	#LUTs	Slack	Power	Frequency	
1	Synthesis	106	3.311ns	15.137W	1/15 ns-1	
1	Implementation	107	0.900ns	14.941W	1/13 115-1	
2	Synthesis	106	4.311ns	15.137W	1/16 ns-1	
2	Implementation	106	1.356ns	14.944W		
3	Synthesis	106	5.311ns	15.137W	1/17 ns-1	
	Implementation	106	2.377ns	14.872W		
4	Synthesis	106	6.311ns	15.137W	1/18 ns-1	
	Implementation	106	3.482ns	14.923W	1/10 115-1	
5	Synthesis	106	7.311ns	15.137W	1/19 ns-1	
	Implementation	106	4.230ns	14.876W	1/13/115-1	

Booth Multiplier 8 Bit						
S.No.	Syn/Imp	#LUTs	Slack	Power	Frequency	
1	Synthesis					
1	Implementation					
2	Synthesis					
۷	Implementation					
3	Synthesis					
3	Implementation					
4	Synthesis					
4	Implementation					
5	Synthesis					
	Implementation					