Considerare la seguente architectura MIPS64:

|  |  |  |
| --- | --- | --- |
| * + Integer ALU: 1 clock cycle   + Data memory: 1 clock cycle   + FP multiplier unit: pipelined 7 stages | * + FP arithmetic unit: pipelined 2 stages   + FP divider unit: not pipelined unit that requires 8 clock cycles   + branch delay slot: 1 clock cycle, and the branch delay slot disabled | * + forwarding enabled   + è possibile completare lo stage EXE di una istruzion in modo out-of-order. |

* Facendo riferimento al frammento di codice riportato, si mostrino le tempistiche relative all’esecuzione ciascuna istruzione e si calcoli il numero totale di clock cycles necessari per eseguire completamente il programma:

for (i = 0; i < 100; i++) {

v5[i] = ((v1[i]/v2[i]) + v3[i]);

v6[i] = ((v3[i]/v4[i]) + v1[i]\*v2[i]);

}

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- | --- |
| .data |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | Clock  cycles |
| V1: .double “100 values” |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| V2: .double “100 values” |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| V3: .double “100 values”  …  V5: .double “100 zeros” |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| V4: .double “100 values” |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| V5: .double “100 values” |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| V6: .double “100 values” |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| .text |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| main: daddui r1,r0,0 | F | D | E | M | W |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | 5 |
| daddui r2,r0,100 |  | F | D | E | M | W |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | 1 |
| loop: l.d f1,v1(r1) |  |  | F | D | E | M | W |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | 1 |
| l.d f2,v2(r1) |  |  |  | F | D | E | M | W |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | 1 |
| div.d f5,f1,f2 |  |  |  |  | F | D | s | / | / | / | / | / | / | / | / | M | W |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | 9 |
| l.d f3,v3(r1) |  |  |  |  |  | F | S | D | E | M | W |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | 0 |
| add.d f5, f5,f3 |  |  |  |  |  |  |  | F | D | s | s | s | s | s | s | + | + | M | W |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | 2 |
| l.d f4, v4(r1) |  |  |  |  |  |  |  |  | F | S | S | S | S | S | S | D | E | S | M | W |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | 1 |
| div.d f6,f3,f4 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | F | D | s | s | / | / | / | / | / | / | / | / | M | W |  |  |  |  |  |  |  |  |  |  | 9 |
| mul.d f1,f1,f2 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | F | s | s | D | \* | \* | \* | \* | \* | \* | \* | S | M | W |  |  |  |  |  |  |  |  |  | 1 |
| add.d f6,f6,f1 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | F | D | s | s | s | s | s | s | s | + | + | M | W |  |  |  |  |  |  |  | 2 |
| s.d f5,v5(r1) |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | F | s | s | s | s | s | s | s | D | E | S | M | W |  |  |  |  |  |  | 1 |
| s.d f6,v6(r1) |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | F | D | S | E | M | W |  |  |  |  |  | 1 |
| daddui r1,r1,8 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | F | S | D | E | M | W |  |  |  |  | 1 |
| daddi r2,r2,-1 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | F | D | E | M | W |  |  |  | 1 |
| bnez r2,loop |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | F | s | D | E | M | W |  | 2 |
| halt |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **F** | **-** | **-** | **-** | **-** | 1 |
| Total | 6+33\*100 | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | 3306 |

Considerando il programma precedente, lo si ottimizzi in modo da eliminare per quanto possibile gli stalli del programma usando le tecniche note come rescheduling e register renaming. Si calcoli il tempo di esecuzione del nuovo programma nella stessa architettura evidenziando il miglioramento ottenuto.

|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **Clock  cycles** |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| main: daddui r1,r0,0 | **F** | **D** | **E** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **5** |
| daddui r2,r0,100 |  | **F** | **D** | **E** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **1** |
| loop: l.d f1,v1(r1) |  |  | **F** | **D** | **E** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **1** |
| l.d f2,v2(r1) |  |  |  | **F** | **D** | **E** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **1** |
| div.d f5,f1,f2 |  |  |  |  | **F** | **D** | **s** | **/** | **/** | **/** | **/** | **/** | **/** | **/** | **/** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **9** |
| l.d f3,v3(r1) |  |  |  |  |  | **F** | **s** | **D** | **E** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **0** |
| l.d f4, v4(r1) |  |  |  |  |  |  |  | **F** | **D** | **E** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **0** |
| mul.d f1,f1,f2 |  |  |  |  |  |  |  |  | **F** | **D** | **\*** | **\*** | **\*** | **\*** | **\*** | **\*** | **\*** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **2** |
| daddi r2,r2,-1 |  |  |  |  |  |  |  |  |  | **F** | **D** | **E** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **0** |
| div.d f6,f3,f4 |  |  |  |  |  |  |  |  |  |  | **F** | **D** | **s** | **s** | **s** | **/** | **/** | **/** | **/** | **/** | **/** | **/** | **/** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **6** |
| add.d f5, f5,f3 |  |  |  |  |  |  |  |  |  |  |  | **F** | **s** | **s** | **s** | **D** | **+** | **+** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **0** |
| s.d f5,v5(r1) |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **F** | **D** | **s** | **E** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **0** |
| add.d f6,f6,f1 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **F** | **s** | **D** | **s** | **s** | **s** | **s** | **+** | **+** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  |  | **2** |
| s.d f6,v6(r1) |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **F** | **s** | **s** | **s** | **s** | **D** | **s** | **E** | **M** | **W** |  |  |  |  |  |  |  |  |  |  |  | **1** |
| daddui r1,r1,8 |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **F** | **s** | **D** | **E** | **M** | **W** |  |  |  |  |  |  |  |  |  |  | **1** |
| bnez r2,loop |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **F** | **D** | **E** | **M** | **W** |  |  |  |  |  |  |  |  |  | **1** |
| halt |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  | **F** | **-** | **-** | **-** | **-** |  |  |  |  |  |  |  |  | **1** |
|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
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|  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |  |
| **Total** | **6+25\*100** | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | | **2506** |

SPEEDUP = 3306/2506 = 1.319

FRACTION\_ENHANCED = (+peso vecchio delle istruzioni cambiate) / totale vecchio = 100\*(1+1+1+9+2+1+1)/3306 = 0.4235

SPEEDUP\_ENHANCED = clock vecchi / clock nuovi di tutte le istruzioni cambiate = (1+1+1+9+2+1+2)/(0+2+0+6+0+0+1) = 1.8889

SPEEDUP\_AMHDAL = 1/((1-FRACTION\_ENHANCED)+(FRACTION\_ENHANCED/SPEEDUP\_ENHANCED)) = 1.25