ARMv7 Processor rev 5 (v7l) 4.0.8-3-rt6-ARCH #1 SMP PREEMPT RT Tue Aug 11 14:47:44 CEST 2015 armv7 1x10<sup>8</sup> CPU 1 1x10<sup>7</sup> CPU<sub>2</sub> CPU 3 1x10<sup>6</sup> CPU 4 100000 10000 1000 100 10 0.1 100 200 300 400 500 600

Preemption Time (us)

Number of Samples