Course Structure

1 lecture every teaching week on Tuesday starting at 10:20AM
1 Lab every teaching week

Course Textbooks

ARM system-on-chip architecture.
S. Furber. ISBN 0-201-67519-6

Open Office Hours: 9:30 – 11:30AM, on Wednesday of every teaching week
at my office (521 Southwest Wing, South Tower, CoE Building)

Module Assessment

- Final Exam. 2 hours, 40%
- Lab, 30%
- In-class Assignments, 30%
  - In-class assignments may be given during a lecture, and answers will be collected at the end of class.
    - Do not do the assignments for other students. If caught, first time, 0 point for the In-class Assignments part; second time, 0 point for the whole course;
    - No excuse will be accepted for not attending classes unless I have been notified by an official permission for absence at the university level in advance.
  - Marking scheme for each in-class assignment:
    - Same mark as the actual mark if above 60;
    - 60, if 60 or below or you are absent with an accepted permission;
    - 0, if absence.

Contents

- Mnemonics
- Flags and Representations
- Subroutines
- Interrupt
- Pipeline
- Encoding
- Memory
- Input/output
- Architecture and Components
- Processor Modes and Thumb Code
- Serial Port Interfacing