COMPLEMENTARY 100V ENHANCEMENT MODE MOSFET H-BRIDGE

SUMMARY

N-Channel = $V_{\text{(BR)DSS}}$ = 100V : $R_{\text{DS(on)}}$ = 0.7 Ω ; I_{D} = 1.4A P-Channel = $V_{\text{(BR)DSS}}$ = -100V : $R_{\text{DS(on)}}$ = 1.0 Ω ; I_{D} = -1.3A



DESCRIPTION

This new generation of trench MOSFETs from Zetex utilizes a unique structure that combines the benefits of low on-resistance with fast switching speed. This makes them ideal for high efficiency, low voltage, power management applications.

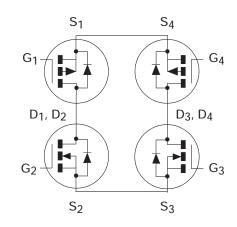
SM8

FEATURES

- · Low on-resistance
- · Fast switching speed
- Low threshold
- Low gate drive
- Single SM-8 Surface Mount Package

APPLICATIONS

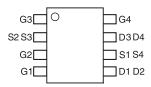
• Single Phase DC Fan Motor Drive



ORDERING INFORMATION

DEVICE	REEL SIZE	TAPE WIDTH	QUANTITY PER REEL
ZXMHC10A07T8TA	7″	12mm	1000 units
ZXMHC10A07T8TC	13″	12mm	4000 units

PINOUT



DEVICE MARKING

 ZXMH C10A7



ABSOLUTE MAXIMUM RATINGS

PARAMETER	SYMBOL	N-channel	P-channel	UNIT
Drain-Source Voltage	V _{DSS}	100	-100	V
Gate-Source Voltage	V_{GS}	±20	±20	V
Continuous Drain Current @ V _{GS} =10V; T _A =25°C (b) (d)	I _D	1.1	-0.9	А
$@V_{GS}=10V; T_A=70^{\circ}C^{(b)}(d)$ $@V_{GS}=10V; T_A=25^{\circ}C^{(a)}(d)$		0.9	-0.8	Α
@ V _{GS} =10V; T _A =25°C ^{(a) (d)}		1.0	-0.8	Α
Pulsed Drain Current ^(c)	I _{DM}	5.2	-4.5	А
Continuous Source Current (Body Diode) (b)	I _S	2.3	-2.2	А
Pulsed Source Current (Body Diode) (c)	I _{SM}	5.2	-4.5	А
Power Dissipation at T _A =25°C ^{(a) (d)}	P _D	1.3		W
Linear Derating Factor		10.4 r		mW/°C
Power Dissipation at T _A =25°C ^{(b) (d)}	P _D	1.3		W
Linear Derating Factor		10.4 n		mW/°C
Operating and Storage Temperature Range	T _j , T _{stg}	-55 to +150		°C

THERMAL RESISTANCE

PARAMETER	SYMBOL	VALUE	UNIT
Junction to Ambient ^{(a) (d)}	$R_{\Theta JA}$	94.5	°C/W
Junction to Ambient ^{(b) (d)}	$R_{\Theta JA}$	73.3	°C/W

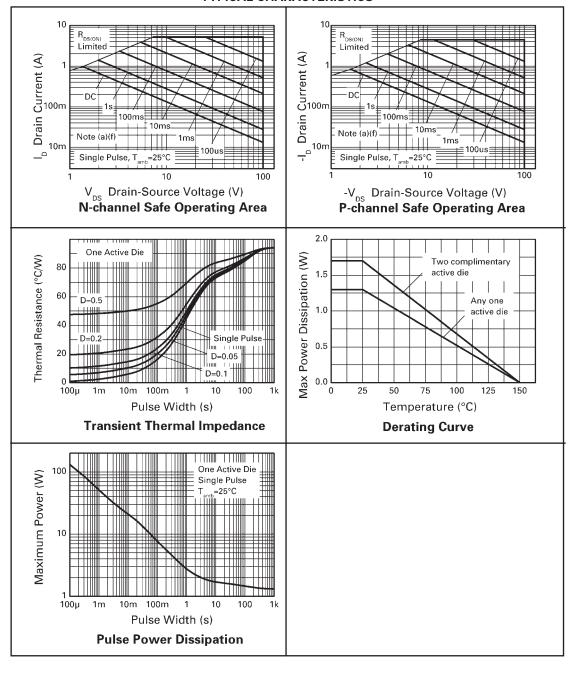


 ⁽a) For a device surface mounted on 50mm x 1.6mm FR4 PCB with high coverage of single sided 2oz copper, in still air conditions, with the heat sink split into two equal areas one for each drain connection.
 (b) For a device surface mounted on FR4 PCB measured at t ≤ 10 sec.

⁽c) Repetitive rating on 50mm x 50mm x 1.6mm FR4 PCB, D= 0.02, pulse width = 300 μs - pulse width limited by maximum junction temperature. Refer to transiennt thermal impedance graph.

⁽d) For device with one active die.

TYPICAL CHARACTERISTICS





N-Channel

ELECTRICAL CHARACTERISTICS (at $T_{amb} = 25$ °C unless otherwise stated)

PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS	
STATIC	•		•	•	•	•	
Drain-Source Breakdown Voltage	V _{(BR)DSS}	100			V	I _D = 250μA, V _{GS} =0V	
Zero Gate Voltage Drain Current	I _{DSS}			1	μΑ	V _{DS} =100V, V _{GS} =0V	
Gate-Body Leakage	I _{GSS}			100	nA	V _{GS} =±20V, V _{DS} =0V	
Gate-Source Threshold Voltage	V _{GS(th)}	2.0		4.0	V	I _D = 250μA, V _{DS} =V _{GS}	
Static Drain-Source On-State Resistance ⁽¹⁾	R _{DS(on)}			0.7 0.9	Ω	V _{GS} = 10V, I _D = 1.5A V _{GS} = 6V, I _D = 1.0A	
Forward Transconductance (1) (3)	g _{fs}		1.6	0.7	S	$V_{DS} = 15V, I_{D} = 1.0A$	
DYNAMIC (3)	1-10	-			-		
Input Capacitance	C _{iss}		138		pF	V (0V V 0V	
Output Capacitance	C _{oss}		12		pF	V _{DS} = 60V, V _{GS} =0V f=1MHz	
Reverse Transfer Capacitance	C _{rss}		6		pF		
SWITCHING ^{(2) (3)}							
Turn-On-Delay Time	t _{d(on)}		1.8		ns		
Rise Time	t _r		1.5		ns	V _{DD} = 50V, I _D = 1.0A	
Turn-Off Delay Time	t _{d(off)}		4.1		ns	$R_G \cong 6.0\Omega$, $V_{GS} = 10V$	
Fall Time	t _f		2.1		ns		
Total Gate Charge	Qg		2.9		nC	V _{DS} = 50V, V _{GS} = 10V	
Gate-Source Charge	Q _{gs}		0.7		nC	I _D = 1.0A	
Gate Drain Charge	Q _{gd}		1.0		nC	11D= 1.0A	
SOURCE-DRAIN DIODE		•	•				
Diode Forward Voltage (1)	V _{SD}			0.95	V	T _j =25°C, I _S = 1.5A, V _{GS} =0V	
Reverse Recovery Time (3)	t _{rr}		27		ns	T _j =25°C, I _S = 1.8A,	
Reverse Recovery Charge (3)	Q _{rr}		12		nC	di/dt=100A/μs	

NOTES

- (1) Measured under pulsed conditions. Pulse width $\leq 300 \mu s;$ duty cycle $\leq 2\%.$
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.



P-Channel

ELECTRICAL CHARACTERISTICS (at $T_{amb} = 25$ °C unless otherwise stated)

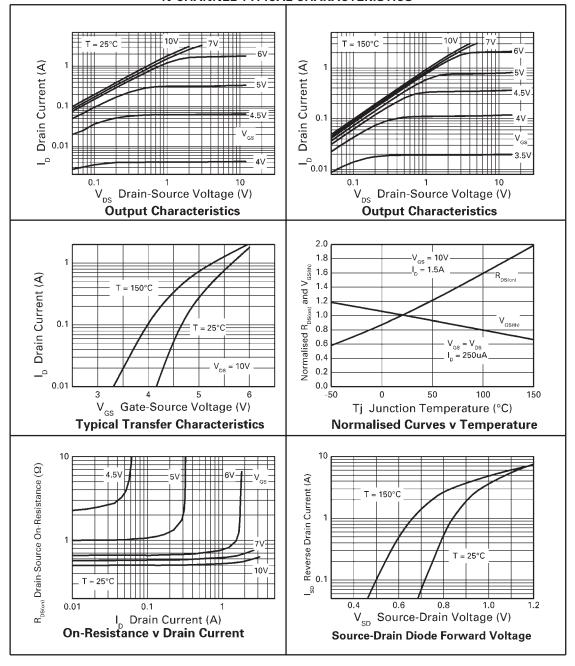
PARAMETER	SYMBOL	MIN.	TYP.	MAX.	UNIT	CONDITIONS	
STATIC	'	1					
Drain-Source Breakdown Voltage	V _{(BR)DSS}	-100			V	I _D = -250μA, V _{GS} =0V	
Zero Gate Voltage Drain Current	I _{DSS}			-1.0	μΑ	V _{DS} = -100V, V _{GS} =0V	
Gate-Body Leakage	I _{GSS}			100	nA	V _{GS} =±20V, V _{DS} =0V	
Gate-Source Threshold Voltage	V _{GS(th)}	-2.0		-4.0	V	I _D = -250μA, V _{DS} =V _{GS}	
Static Drain-Source On-State Resistance ⁽¹⁾	R _{DS(on)}			1 1.45	Ω	V_{GS} = -10V, I_{D} = - 0.6A V_{GS} = -6V, I_{D} = -0.5A	
Forward Transconductance (1) (3)	g _{fs}		1.2		S	V _{DS} = -15V, I _D = -0.6A	
DYNAMIC (3)		1	ı	ı	ı		
Input Capacitance	C _{iss}		141		pF	.,	
Output Capacitance	C _{oss}		13.1		pF	V _{DS} = -50V, V _{GS} =0V -f=1MHz	
Reverse Transfer Capacitance	C _{rss}		10.8		pF		
SWITCHING ^{(2) (3)}	1	1					
Turn-On-Delay Time	t _{d(on)}		1.6		ns		
Rise Time	t _r		2.1		ns	V _{DD} = -50V, I _D = -1A	
Turn-Off Delay Time	t _{d(off)}		5.9		ns	$R_G \cong 6.0\Omega$, $V_{GS} = -10V$	
Fall Time	t _f		3.3		ns		
Gate Charge	Qg		1.6		nC	$V_{DS} = -50V, V_{GS} = -5V$ $I_{D} = -0.6A$	
Total Gate Charge	Qg		3.5		nC	V F0V V 10V	
Gate-Source Charge	Q _{gs}		0.6		nC	$V_{DS} = -50V, V_{GS} = -10V$ $I_{D} = -0.6A$	
Gate Drain Charge	Q _{gd}		1.6		nC	11D0.0A	
SOURCE-DRAIN DIODE	<u> </u>	•	•	•	•	•	
Diode Forward Voltage ⁽¹⁾	V _{SD}		-0.85	-0.95	V	T _j =25°C, I _S = -0.75A, V _{GS} =0V	
Reverse Recovery Time (3)	t _{rr}		29		ns	T _i =25°C, I _S = -0.9A,	
Reverse Recovery Charge (3)	Q _{rr}		31		nC	di/dt=100A/μs	

NOTES

- (1) Measured under pulsed conditions. Pulse width $\leq 300 \mu s$; duty cycle $\leq 2\%$.
- (2) Switching characteristics are independent of operating junction temperature.
- (3) For design aid only, not subject to production testing.

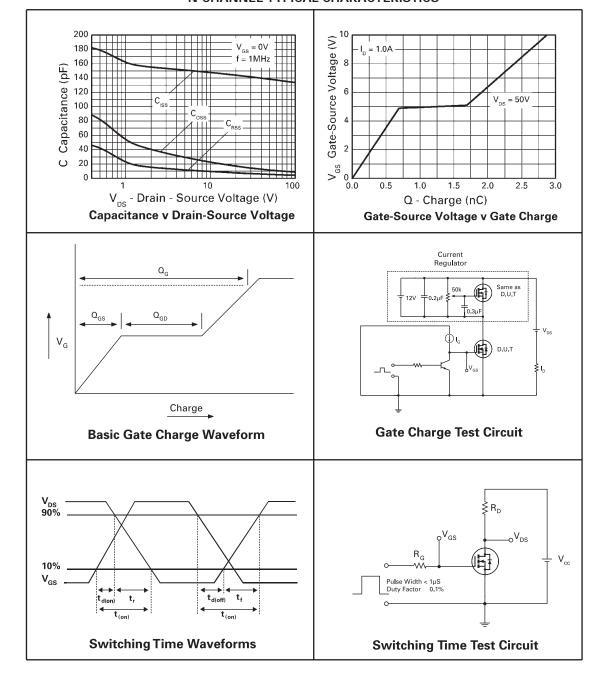


N-CHANNEL TYPICAL CHARACTERISTICS



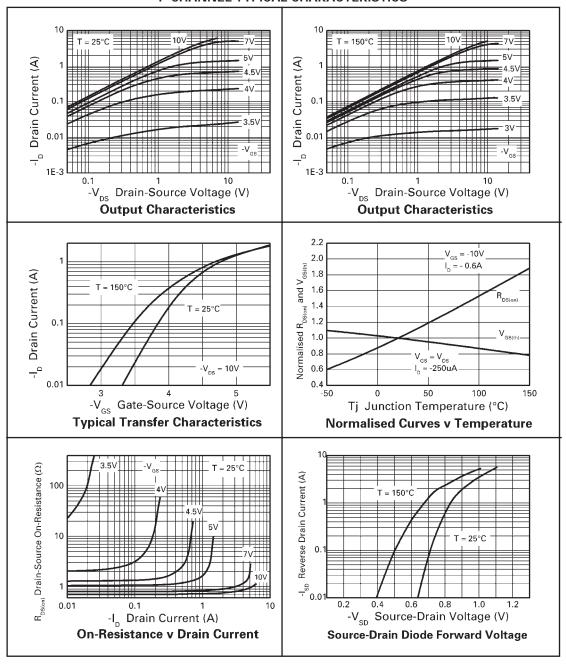


N-CHANNEL TYPICAL CHARACTERISTICS



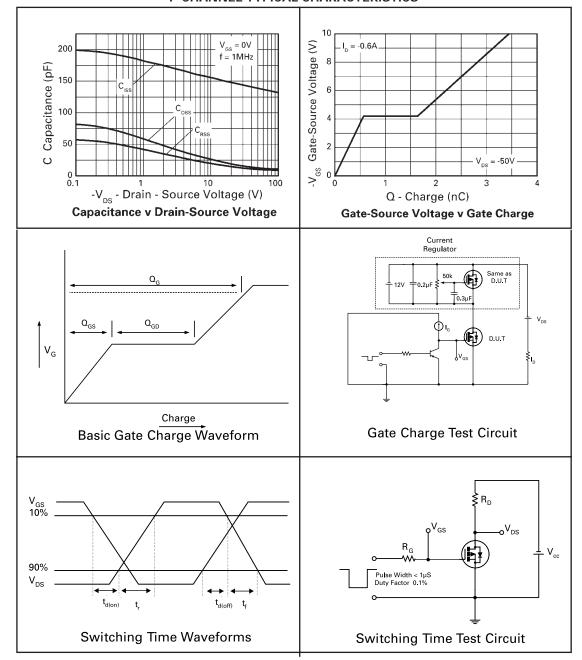


P-CHANNEL TYPICAL CHARACTERISTICS



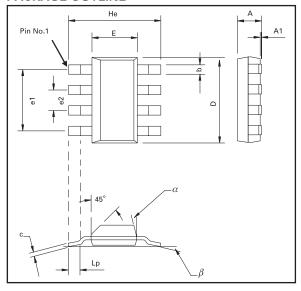


P-CHANNEL TYPICAL CHARACTERISTICS





PACKAGE OUTLINE



PACKAGE DIMENSIONS

DIM	Millimetres			Inches			
	MIN	TYP	MAX	MIN	TYP	MAX	
Α	-	1	1.7	-	1	0.067	
A1	0.02	ı	0.1	0.0008	ı	0.004	
b	_	0.7	_	_	0.028	-	
С	0.24	ı	0.32	0.009	ı	0.013	
D	6.3	ı	6.7	0.248	ı	0.264	
Е	3.3	1	3.7	0.130	ı	0.145	
e1	_	4.59	-	_	0.180	_	
e2	_	1.53	-	-	0.060	_	
Не	6.7	ı	7.3	0.264	ı	0.287	
Lp	0.9	ı	-	0.035	ı	_	
α	_	1	15°	_		15°	
β	_	10°	_	_	10°	-	

Controlling dimensions are in millimetres. Approximate conversions are given in inches

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ISSUE 2 - JUNE 2005