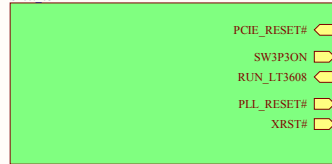




PCIEExpress-Interface / Power Supply 1
Sheet 02



Power Supply 2 / Reset
Sheet 03



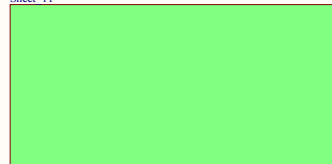
DDR2-SDRAM
Sheet 04



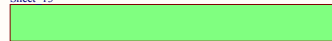
Power DDR2-SDRAM
Sheet 12



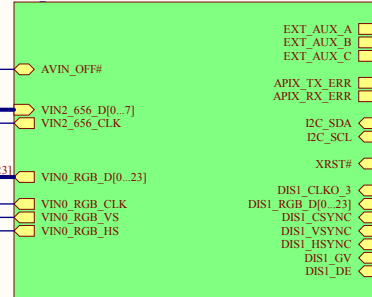
Power MB86298
Sheet 11



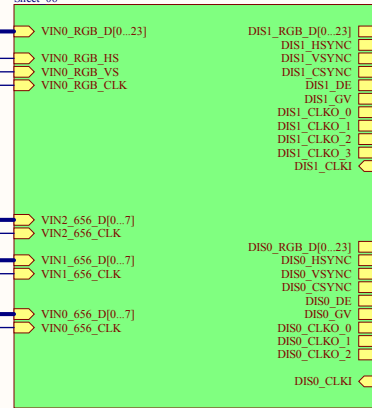
Revision History
Sheet 13



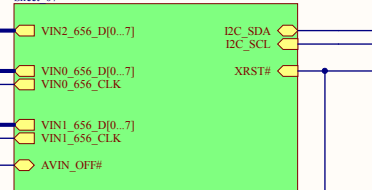
Extension IO
Sheet 10



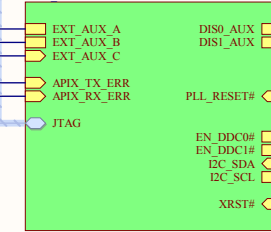
Ruby Video-CAP-DIS
Sheet 06



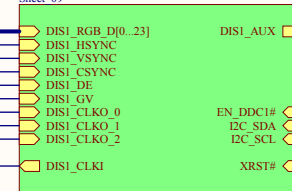
Video-IN
Sheet 07



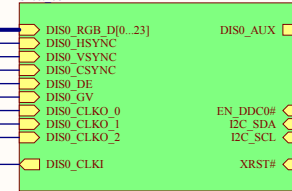
Clock-JTAG-I2C-SPI-GPIO
Sheet 05




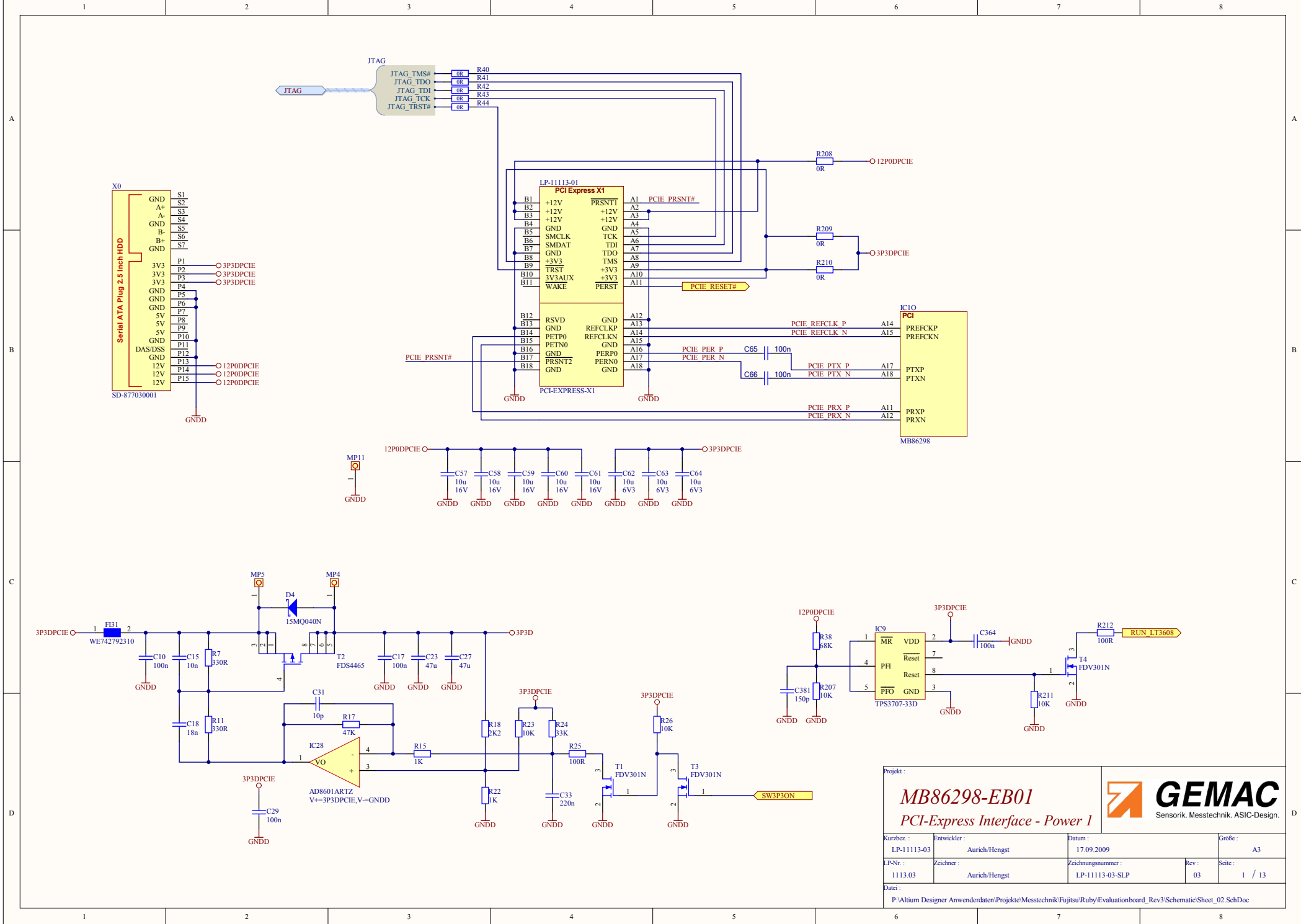
Display1-OUT
Sheet 09

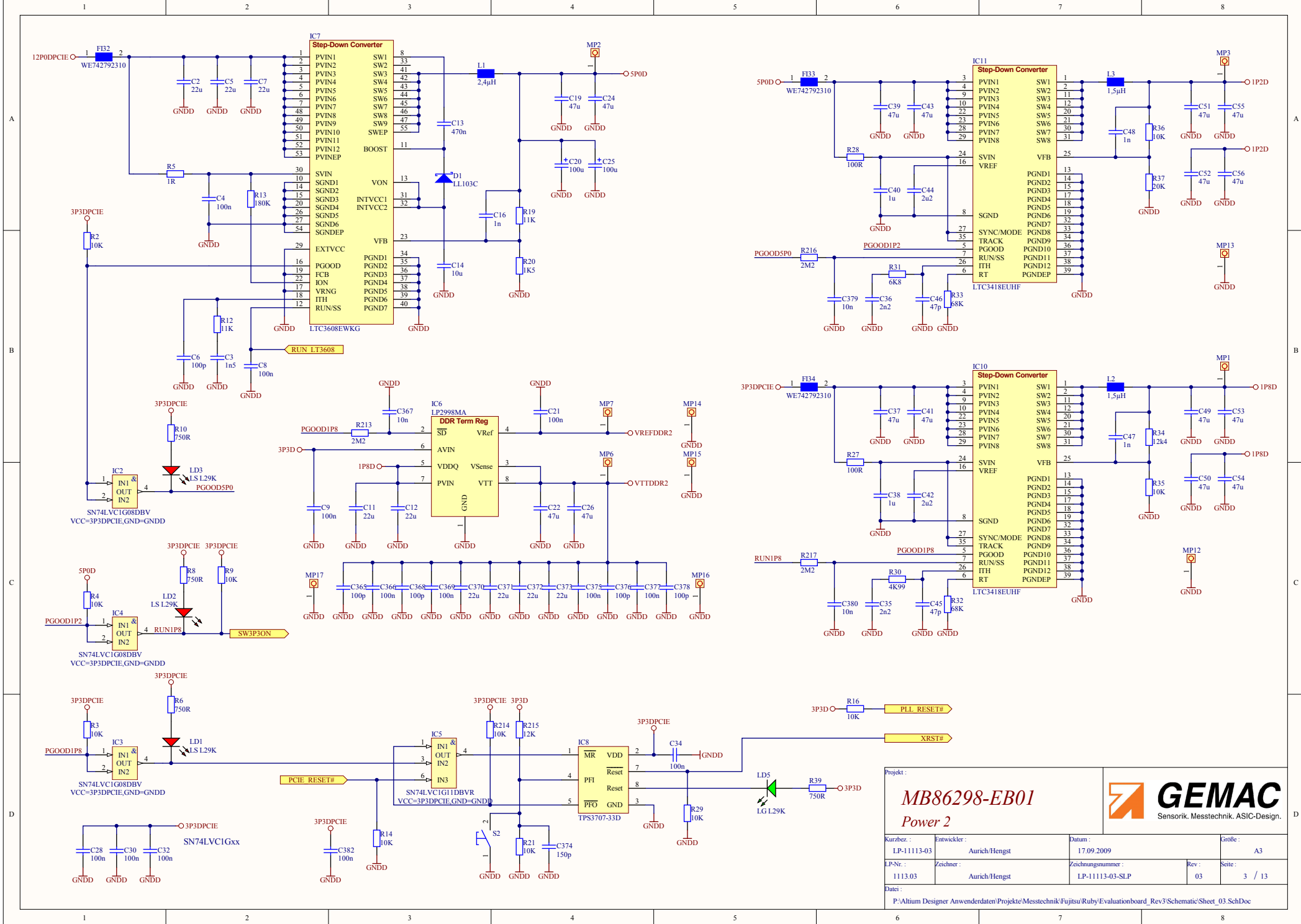


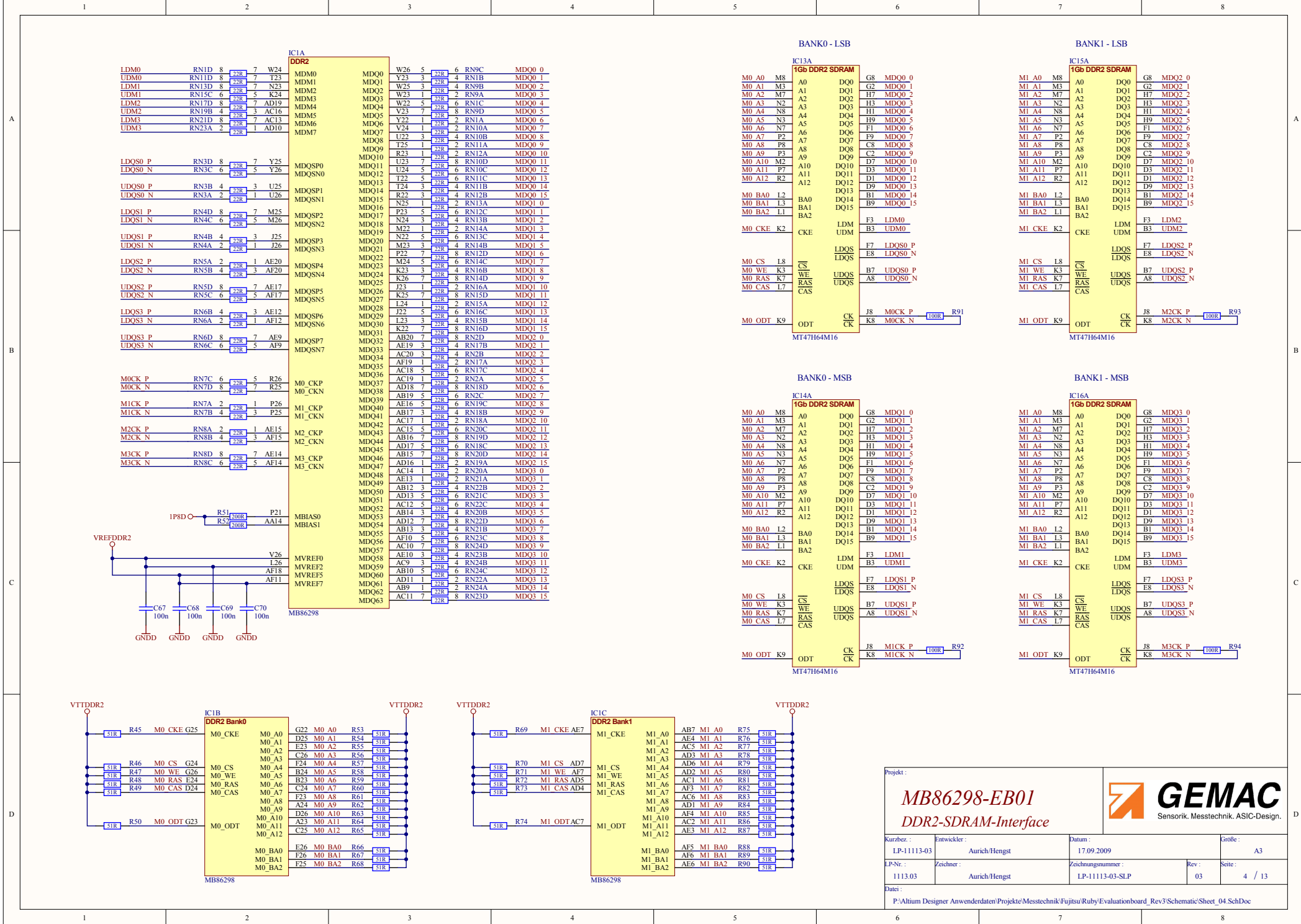
Display0-OUT
Sheet 08

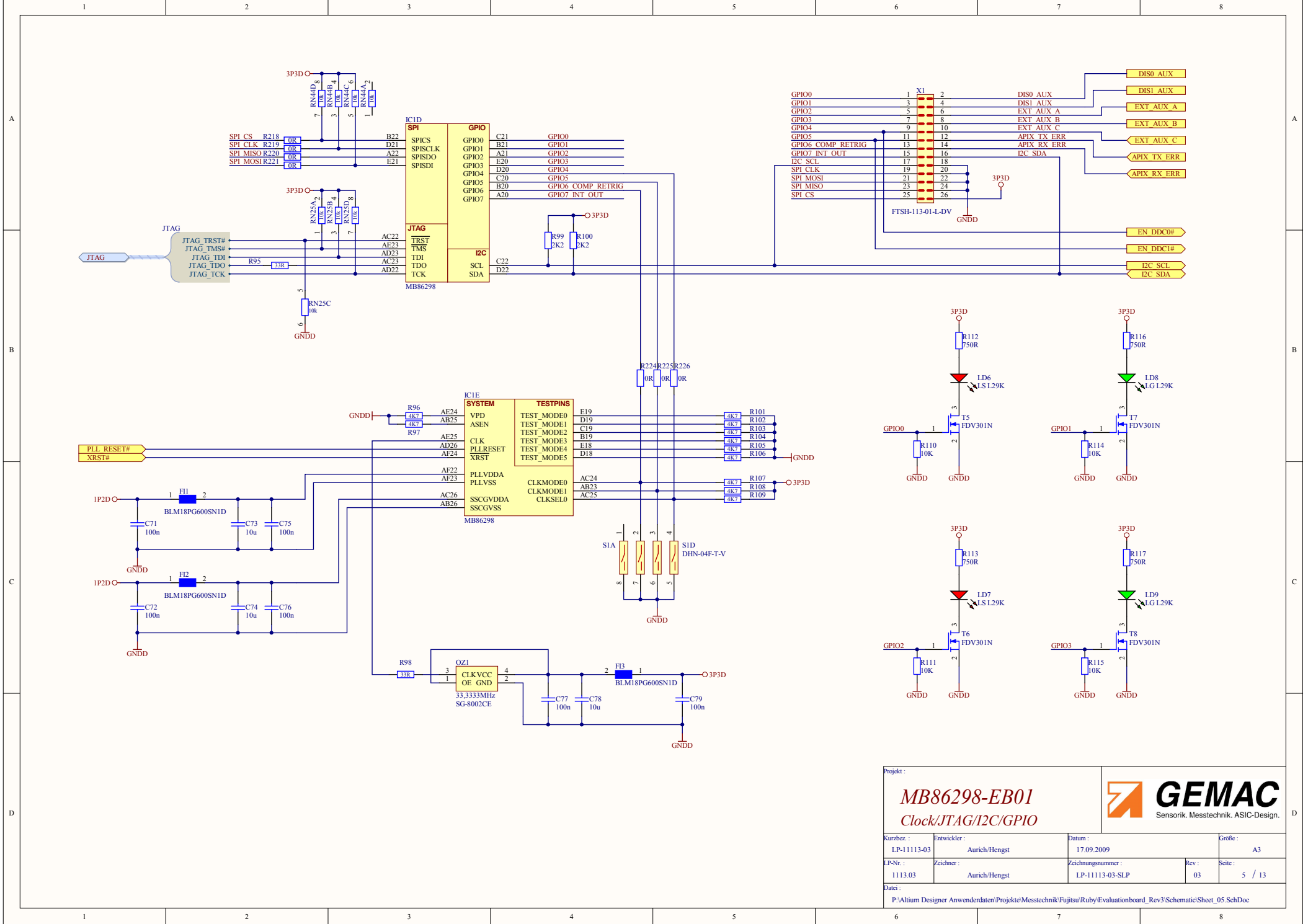


Projekt :		 GEMAC Sensorik. Messtechnik. ASIC-Design.		
<i>MB86298-EB01</i> <i>Overview</i>				
Kurzbez. :	Entwickler :	Datum :	Größe :	
LP-11113-03	Aurich/Hengst	17.09.2009	A3	
LP-Nr. :	Zeichner :	Zeichnungsnummer :	Rev. :	Seite :
1113.03	Aurich/Hengst	LP-11113-03-SLP	03	1 / 13
Datei :				
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A

A

B

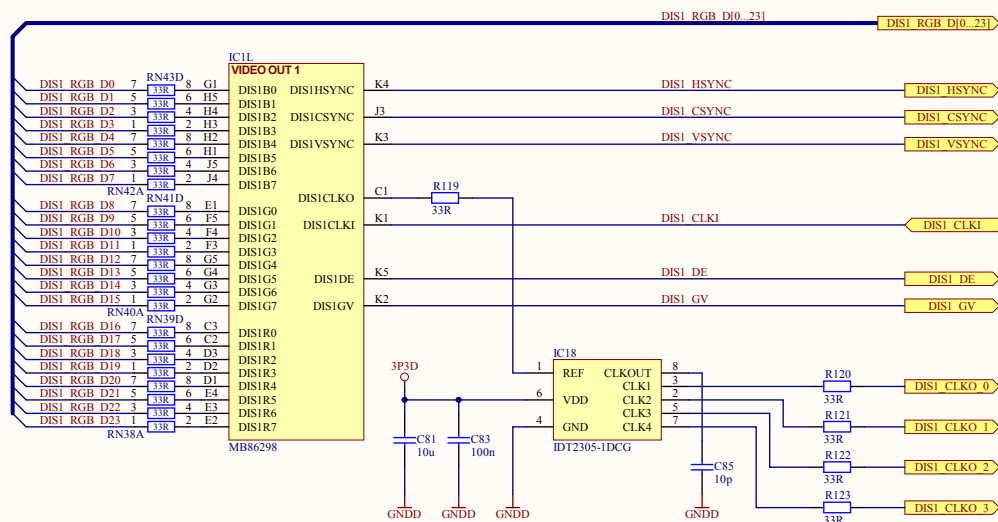
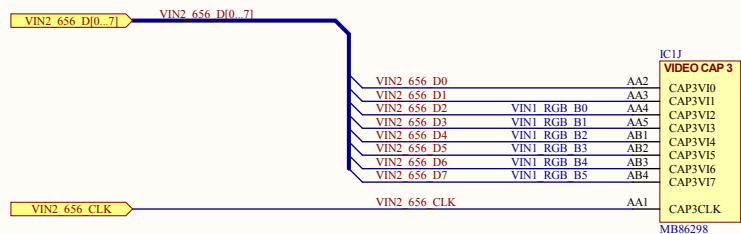
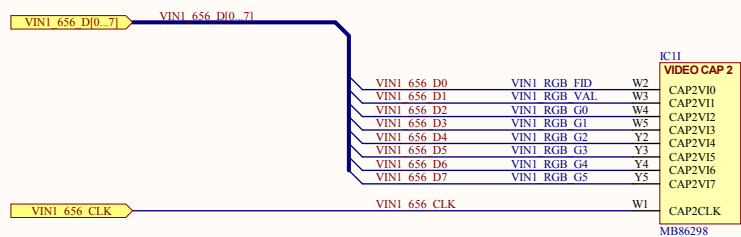
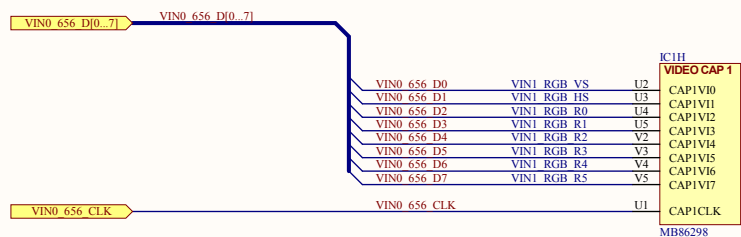
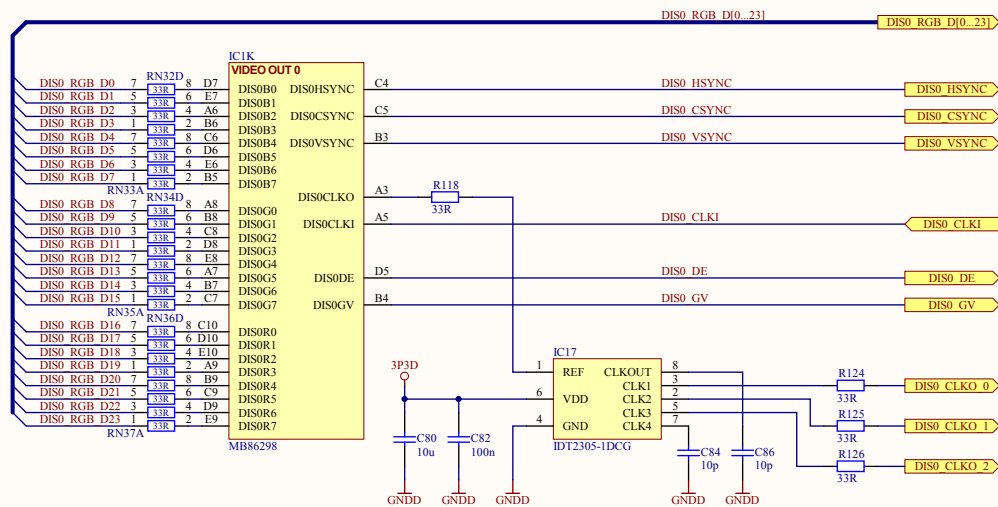
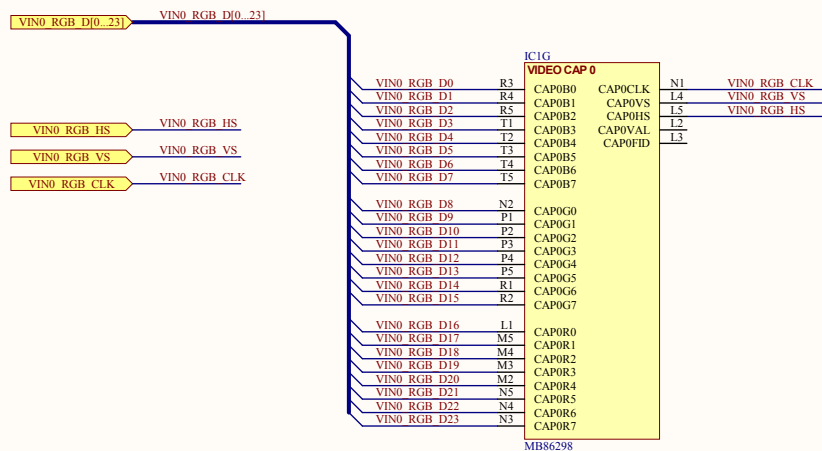
B

C

C

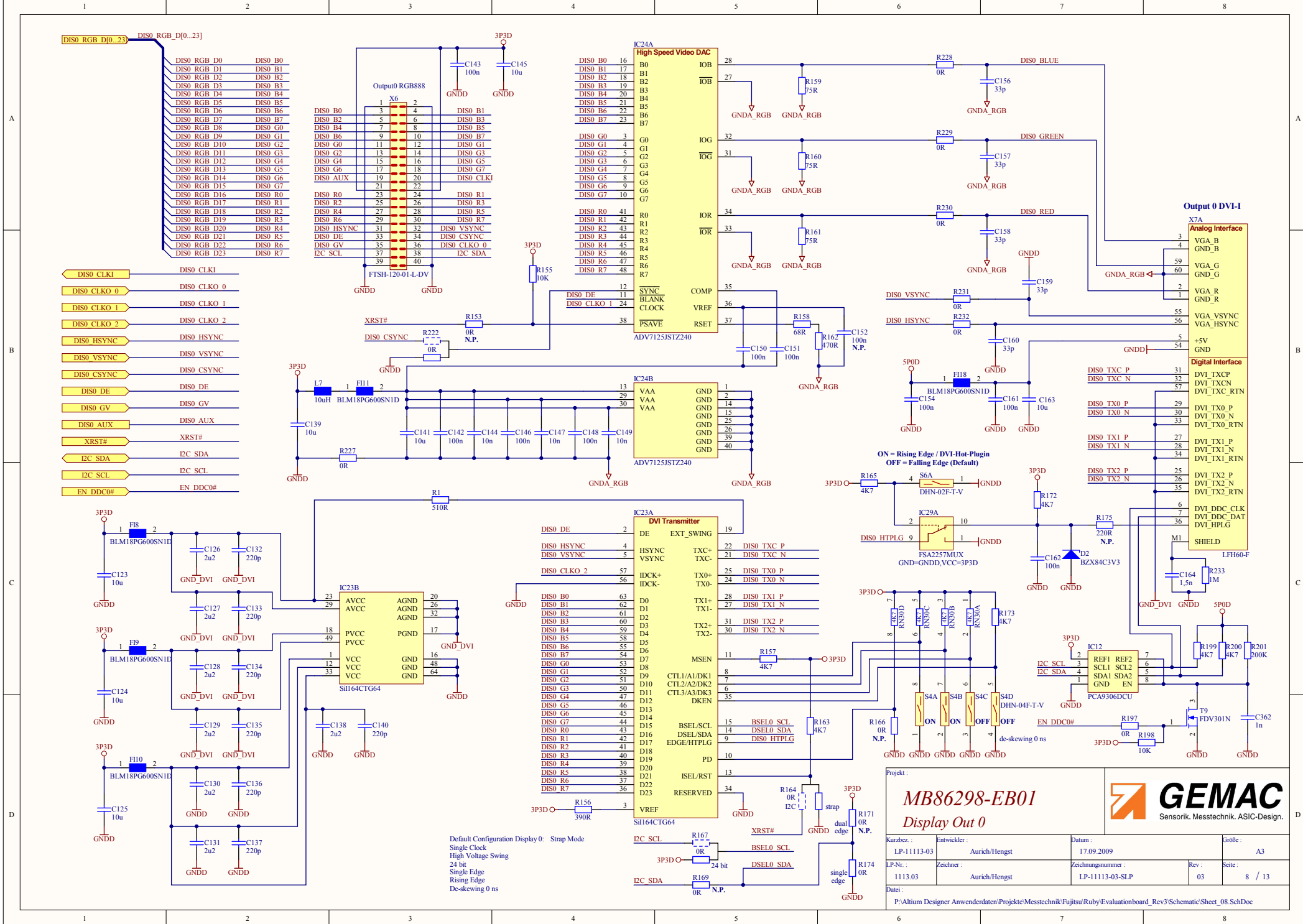
D

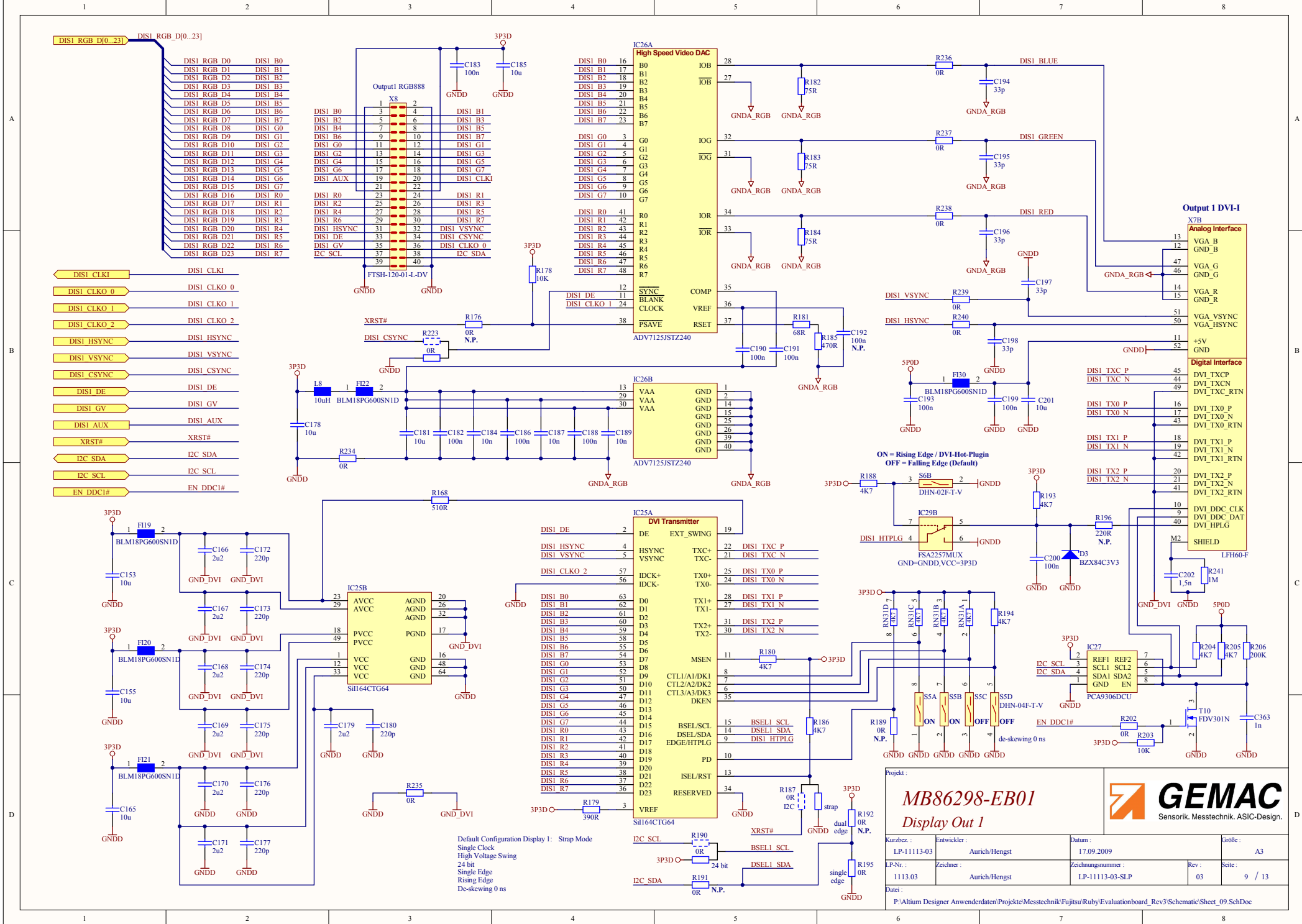
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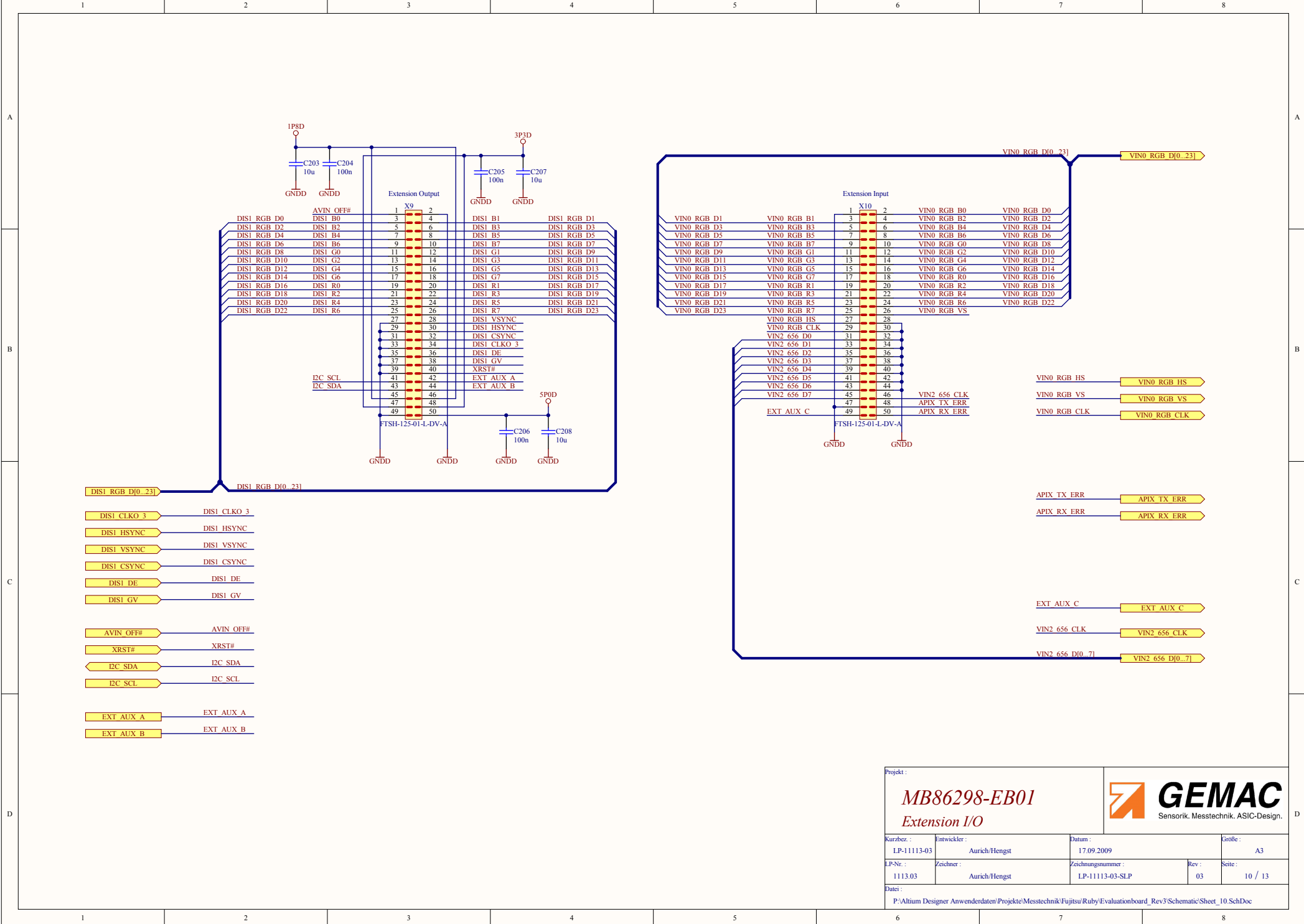


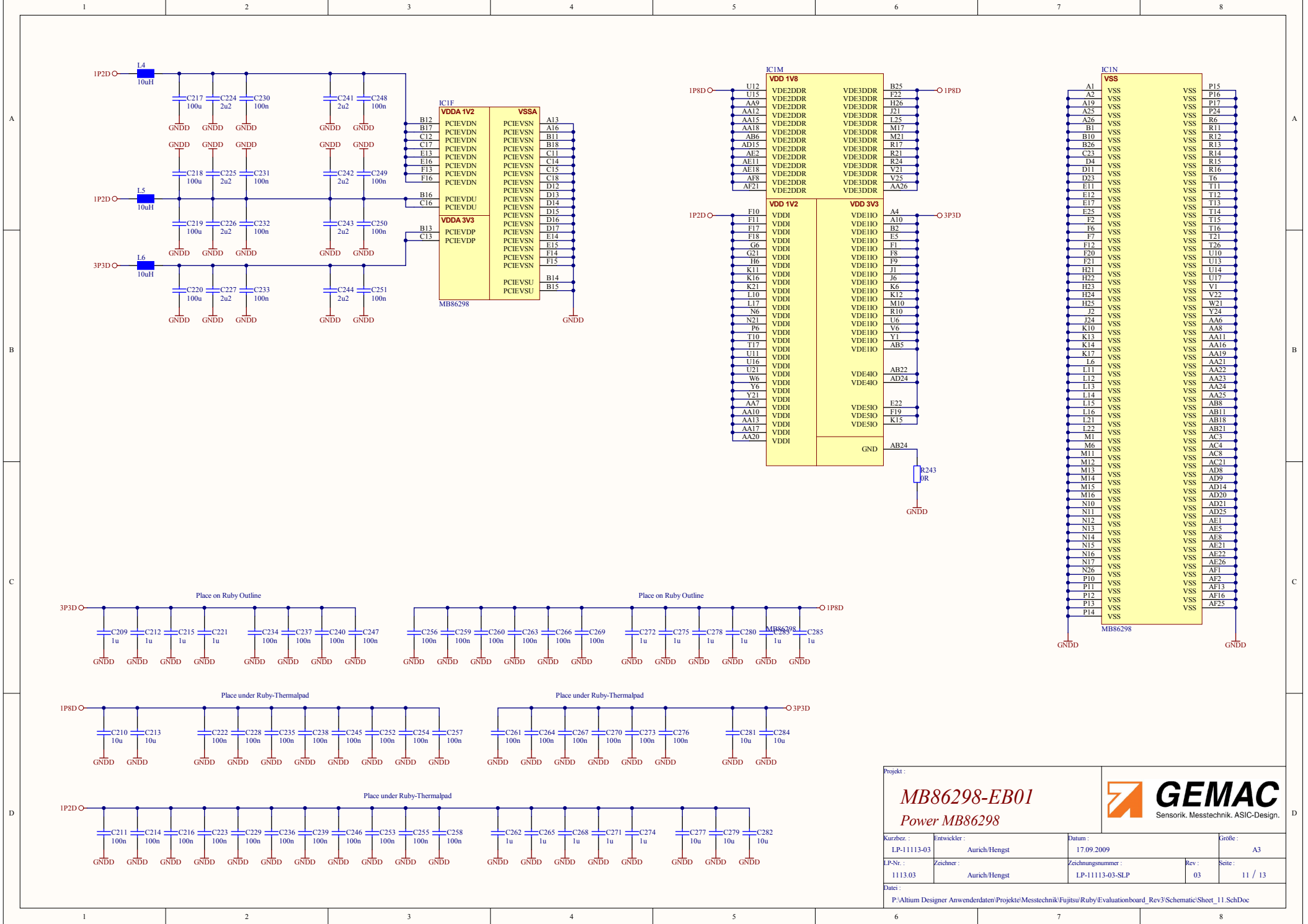
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Kurzbez. : LP-11113-03	Entwickler : Aurich/Hengst	Zeichner : Aurich/Hengst	Zeichnungsnummer : LP-11113-03-SLP	Rev. : 03	Seite : 6 / 13
Datei : P:\Altium Designer Anwenderdaten\Projekte\Messtechnik\Fujitsu\Ruby\Evaluationboard_Rev3\Schematic\Sheet_06.SchDoc					

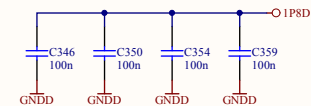
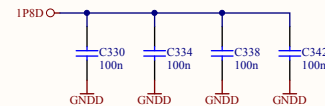
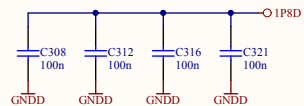
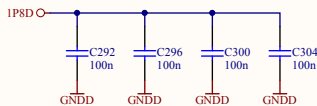
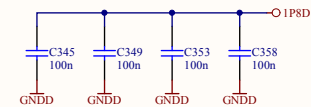
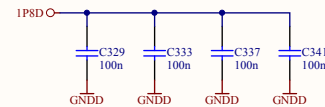
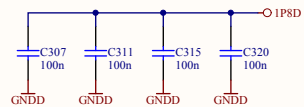
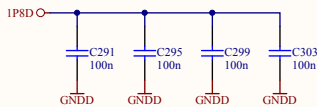
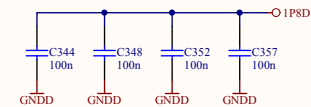
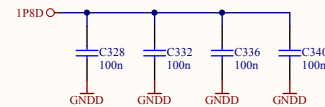
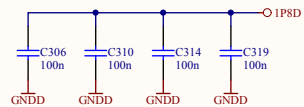
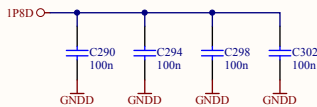
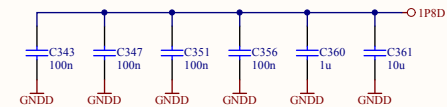
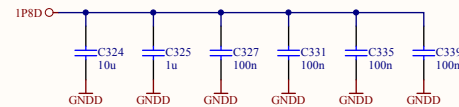
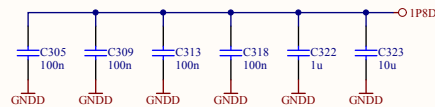
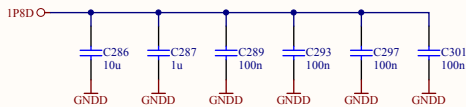
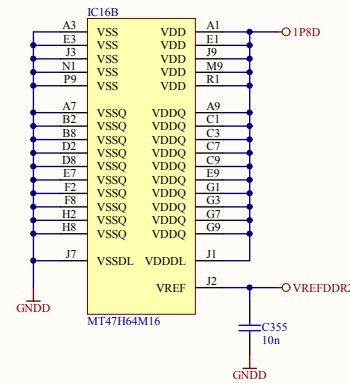
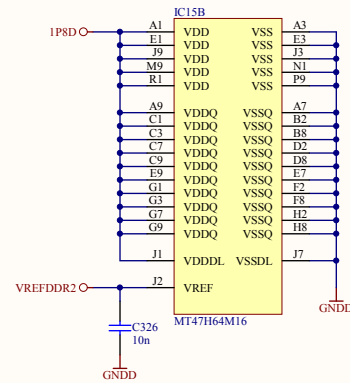
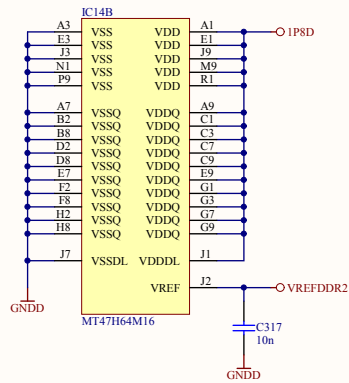
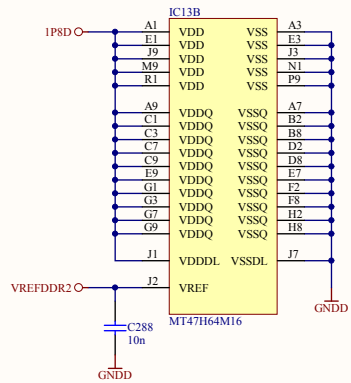












Projekt :		 GEMAC Sensorik, Messtechnik, ASIC-Design.	
<i>MB86298-EB01</i> <i>Power DDR2-SDRAM</i>			
Kurzbez. :	Entwickler :	Datum :	Größe :
LP-11113-03	Aurich/Hengst	17.09.2009	A3
LP-Nr. :	Zeichner :	Zeichnungsnummer :	Rev. :
1113.03	Aurich/Hengst	LP-11113-03-SLP	03
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Revision History:

LP-11113-01	09.02.2009	- Inertial Version
LP-11113-02	30.04.2009	- Major Changes in Power Supply - Added SATA-Power-Connector - Change R50, R74 to 51R (Sheet 4) - Connect Clockmode-Switches to Ruby GPIO-Pins (Sheet 5) - HW Configuration Options added for DVI-Transmitter (Sheet 8+9) - EMI-Filter added on Video DAC (Sheet 8+9) - 0R added on Ruby Pin AB24 (Sheet 11)
LP-11113-03	16.09.2009	- Changes on 5V-Buck-Converter Design for improved stability (Sheet 3) - Change Net 3P3DPCIE on R4 to SP0D (Reset Bug-Fix) - Change SATA-Connector as not placed Part - Connect Blank-Input on ADV7125 to Ruby Display Output Enable (Bug-Fix with Video Blanking, Sheet 8+9) - Change DVI Transmitter EDGE Configuration Option from 0R-Jumper to combination of Switch plus Analog-Mux for easier usability (Sheet 8+9)

Ref1 Ref2 Ref3 Ref4

Projekt :		 GEMAC Sensorik. Messtechnik. ASIC-Design.	
Kurzbez. :	Entwickler :	Datum :	Größe :
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LP-Nr. :	Zeichner :	Zeichnungsnummer :	Rev : Seite :
1113.03	Aurich/Hengst	LP-11113-03-SLP	03 13 / 13
Datei :			
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