

OptiMOS[™]3 Power-Transistor

Features

- Very low gate charge for high frequency applications
- Optimized for dc-dc conversion
- N-channel, normal level
- Excellent gate charge x R DS(on) product (FOM)
- Very low on-resistance $R_{\,\mathrm{DS(on)}}$
- 150 °C operating temperature
- Pb-free lead plating; RoHS compliant
- Qualified according to JEDEC¹⁾ for target application
- Halogen-free according to IEC61249-2-21

Product Summary

V _{DS}	100	٧
R _{DS(on),max}	6	mΩ
I _D	90	Α

PG-TDSON-8







Туре	Package	Marking	
BSC060N10NS3 G	PG-TDSON-8	060N10NS	



Maximum ratings, at T_i =25 °C, unless otherwise specified

Parameter	Symbol	Conditions	Value	Unit
Continuous drain current	I _D	T _C =25 °C	90	А
		T _C =100 °C	66	
		T _A =25 °C, R _{thJA} =50 K/W ²⁾	14.9	
Pulsed drain current ³⁾	I _{D,pulse}	T _C =25 °C	360	1
Avalanche energy, single pulse	E _{AS}	$I_{\rm D}$ =50 A, $R_{\rm GS}$ =25 Ω	230	mJ
Gate source voltage	V _{GS}		±20	V
Power dissipation	P _{tot}	T _C =25 °C	125	w
Operating and storage temperature	$T_{\rm j},T_{\rm stg}$	_	-55 150	°C
IEC climatic category; DIN IEC 68-1			55/150/56	



BSC060N10NS3 G

Parameter	Symbol	Conditions	Values		Unit	
			min.	typ.	max.	
Thermal characteristics						
Thermal resistance, junction - case	R _{thJC}		-	-	1	K/W
Thermal resistance,	R_{thJA}	minimal footprint	-	-	62	
junction - ambient		6 cm ² cooling area ²⁾	-	-	50	

Electrical characteristics, at T_j =25 °C, unless otherwise specified

Static characteristics

Drain-source breakdown voltage	V _{(BR)DSS}	V _{GS} =0 V, I _D =1 mA	100	-	-	V
Gate threshold voltage	$V_{\rm GS(th)}$	V _{DS} =V _{GS} , / _D =90 μA	2	2.7	3.5	
Zero gate voltage drain current	/ _{DSS}	V _{DS} =100 V, V _{GS} =0 V, T _j =25 °C	1	0.01	1	μA
		V _{DS} =100 V, V _{GS} =0 V, T _j =125 °C	1	10	100	
Gate-source leakage current	I _{GSS}	V _{GS} =20 V, V _{DS} =0 V	-	1	100	nA
Drain-source on-state resistance	R _{DS(on)}	V _{GS} =10 V, I _D =50 A	-	5.3	6	mΩ
		V _{GS} =6 V, I _D =25 A	-	6.6	11.5	
Gate resistance	R _G		-	1.6	-	Ω
Transconductance	g fs	V _{DS} >2 I _D R _{DS(on)max} , I _D =50 A	43	85	-	s

¹⁾J-STD20 and JESD22

 $^{^{2)}}$ Device on 40 mm x 40 mm x 1.5 mm epoxy PCB FR4 with 6 cm² (one layer, 70 μ m thick) copper area for drain connection. PCB is vertical in still air.

³⁾ see figure 3



Parameter	Symbol Conditions		Values			Unit
			min.	typ.	max.	
Dynamic characteristics						
Input capacitance	C iss		-	3700	4900	pF
Output capacitance	C oss	V _{GS} =0 V, V _{DS} =50 V, f=1 MHz	-	650	860	
Reverse transfer capacitance	C _{rss}		-	25	-	1
Turn-on delay time	$t_{d(on)}$		-	20	-	ns
Rise time	t _r	V _{DD} =50 V, V _{GS} =10 V,	-	16	-	
Turn-off delay time	$t_{d(off)}$	$I_{\rm D}$ =25 A, $R_{\rm G}$ =1.6 Ω	-	45	-	
Fall time	t _f	1	-	12	-	
Gate Charge Characteristics ⁴⁾						
Gate to source charge	Q _{gs}		-	15	-	nC
Gate to drain charge	Q_{gd}		1	9	1	
Switching charge	Q sw	V _{DD} =50 V, / _D =25 A, V _{GS} =0 to 10 V	-	13	-	
Gate charge total	Qg		1	51	68	
Gate plateau voltage	V _{plateau}		-	4.2	-	V
Output charge	Q _{oss}	V _{DD} =50 V, V _{GS} =0 V	-	68	91	nC
Reverse Diode						
Diode continous forward current	Is	T -25 °C	-	-	90	Α
Diode pulse current	/ _{S,pulse}	T _C =25 °C	-	-	360	1
Diode forward voltage	V _{SD}	V _{GS} =0 V, I _F =50 A, T _j =25 °C	-	1	1.2	V
Reverse recovery time	t rr	V _R =50 V, I _F =25 A,	-	61	-	ns
Reverse recovery charge	Q _{rr}	d <i>i</i> _F /d <i>t</i> =100 A/μs	-	109	-	nC

⁴⁾ See figure 16 for gate charge parameter definition



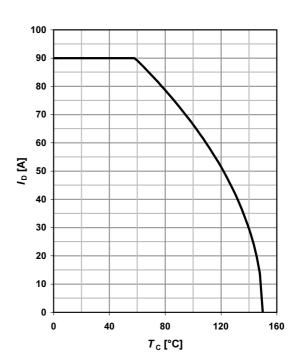
1 Power dissipation

P_{tot} =f(T_{C})

140 120 100 80 40 20 0 40 80 120 160 T_C [°C]

2 Drain current

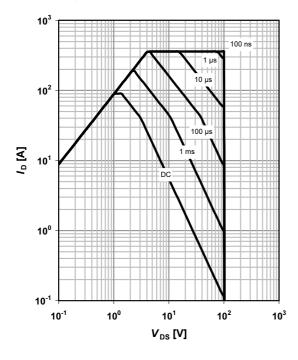
$$I_D = f(T_C); V_{GS} \ge 10 \text{ V}$$



3 Safe operating area

$$I_D$$
=f(V_{DS}); T_C =25 °C; D =0

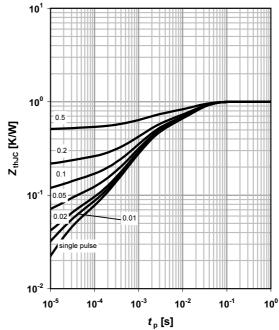
parameter: t_p



4 Max. transient thermal impedance

$$Z_{thJC}$$
=f(t_p)

parameter: $D = t_p/T$

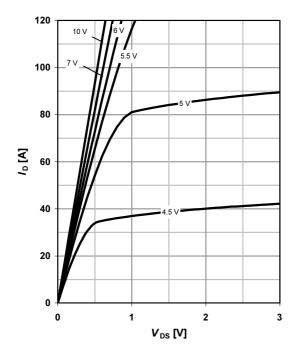




5 Typ. output characteristics

 $I_D = f(V_{DS}); T_j = 25 °C$

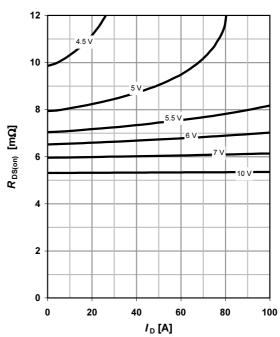
parameter: V_{GS}



6 Typ. drain-source on resistance

 $R_{DS(on)}$ =f(I_D); T_j =25 °C

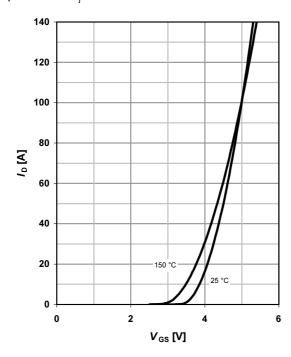
parameter: V_{GS}



7 Typ. transfer characteristics

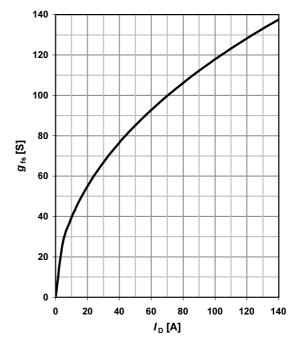
 I_{D} =f(V_{GS}); $|V_{DS}|$ >2 $|I_{D}|R_{DS(on)max}$

parameter: T_j



8 Typ. forward transconductance

 g_{fs} =f(I_D); T_j =25 °C





9 Drain-source on-state resistance

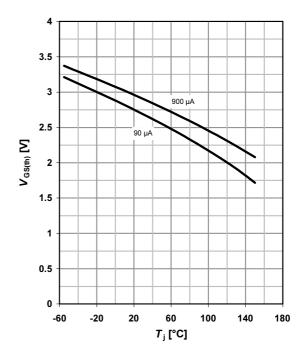
 $R_{DS(on)}$ =f(T_j); I_D =50 A; V_{GS} =10 V

12 10 8 R_{DS(on)} [mΩ] 2 0 20 -60 -20 140 180 *T*_j [°C]

10 Typ. gate threshold voltage

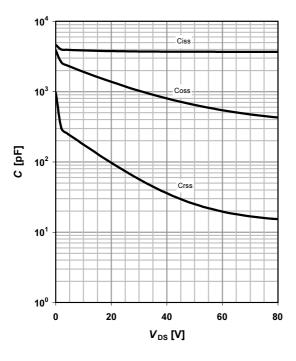
 $V_{GS(th)}$ =f(T_j); V_{GS} = V_{DS}

parameter: I_D



11 Typ. capacitances

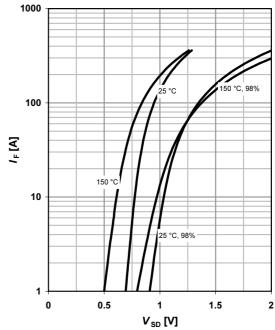
 $C = f(V_{DS}); V_{GS} = 0 V; f = 1 MHz$



12 Forward characteristics of reverse diode

 $I_{\mathsf{F}} = \mathsf{f}(V_{\mathsf{SD}})$

parameter: $T_{\rm j}$





13 Avalanche characteristics

 I_{AS} =f(t_{AV}); R_{GS} =25 Ω

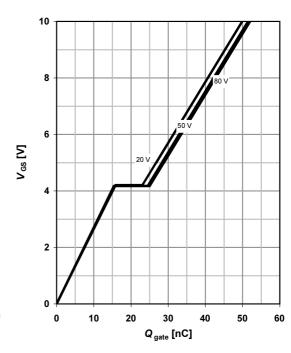
parameter: $T_{j(start)}$

100 25 °C 100 °C 25 °C 100 °C 125 °C 125 °C 100 °C 125 °C 125 °C 100 °C 125 °C

14 Typ. gate charge

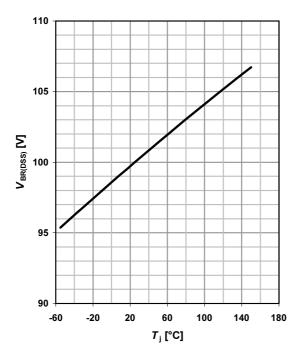
 $V_{\rm GS}$ =f(Q_{gate}); $I_{\rm D}$ =25 A pulsed

parameter: V_{DD}

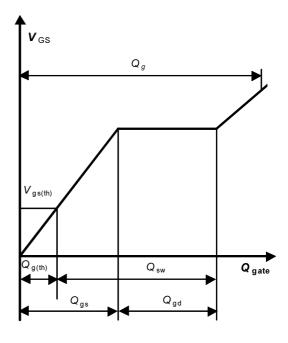


15 Drain-source breakdown voltage

 $V_{BR(DSS)}$ =f(T_j); I_D =1 mA

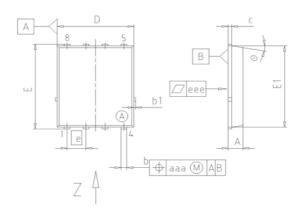


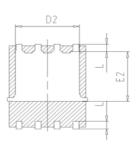
16 Gate charge waveforms

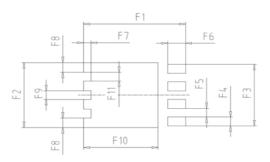


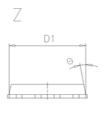


Package Outline: PG-TDSON-8

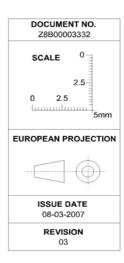




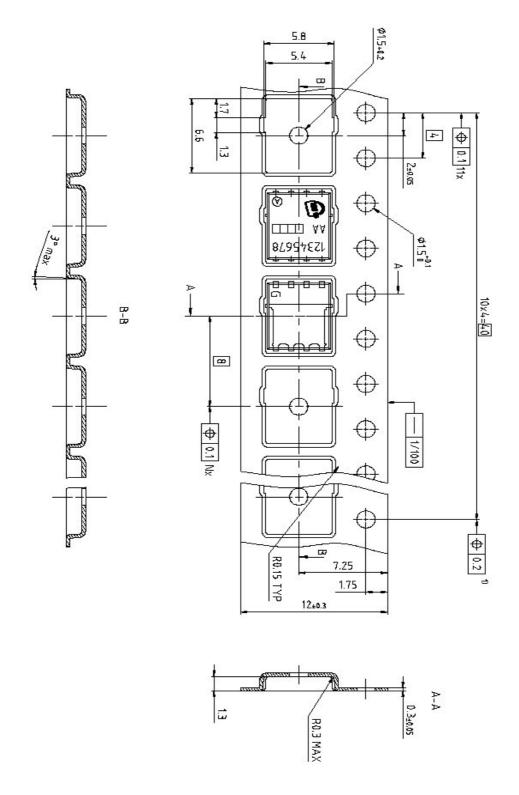




DIM	MILLIM	IETERS	INCHES		
DIW	MIN	MAX	MIN	MAX	
Α	0.90	1.10	0.035	0.043	
b	0.34	0.54	0.013	0.021	
b1	0.02	0.22	0.001	0.008	
С	0.15	0.35	0.006	0.014	
D=D1	4.95	5.35	0.195	0.211	
D2	4.20	4.40	0.165	0.173	
E	5.95	6.35	0.234	0.250	
E1	5.70	6.10	0.224	0.240	
E2	3.40	3.80	0.134	0.150	
е	1.2	27	0.050		
N	8		8		
L	0.45	0.65	0.018	0.026	
	8.5°	11.5°	8.5°	11.5°	
aaa	0.2	25	0.010		
eee	0.0)5	0.002		
F1	6.75	6.95	0.266	0.274	
F2	4.60	4.80	0.181	0.189	
F3	4.36	4.56	0.172	0.180	
F4	0.55	0.75	0.022	0.030	
F5	0.52	0.72	0.020	0.028	
F6	1.10	1.30	0.043	0.051	
F7	0.40	0.60	0.016	0.024	
F8	0.60	0.80	0.024	0.031	
F9	0.53	0.73	0.021	0.029	
F10	4.90	5.10	0.193	0.201	
F11	0.53	0.73	0.021	0.029	







Dimensions in mm



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