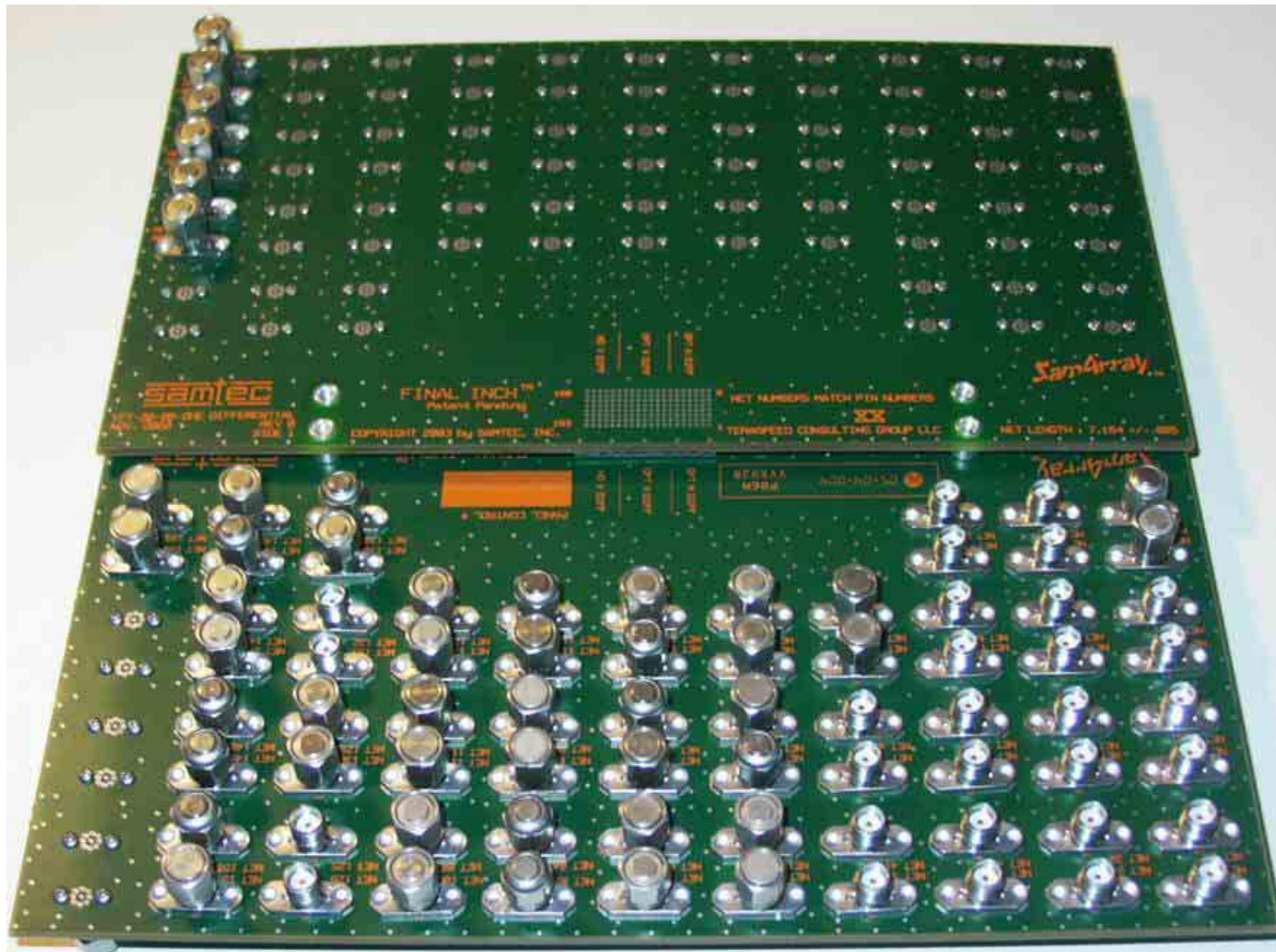


**Samtec Final Inch™  
SE Array® Connector (SEAM/SEAF)  
7 mm Stack Height  
Differential Channel Properties, High  
Density Vertical Test Case**

Scott McMorrow, Director of Engineering  
Jim Bell, Senior Signal Integrity Engineer

# SE Array<sup>®</sup> Differential Final Inch<sup>™</sup> Test & Evaluation Boards



# Documentation Goals

- Provide Information for :
  - Characterization of in-system differential performance of the SE Array<sup>®</sup> connector system.
  - Provide eye pattern simulations with real system layout and routing effects.
    - Eye simulations performed with worst case adjacent neighbor pairs switching at maximum bit rate and driven victim switching with a pseudo random pattern.
  - Provide frequency domain simulations of insertion loss, return loss and crosstalk.

# Introduction and Philosophy

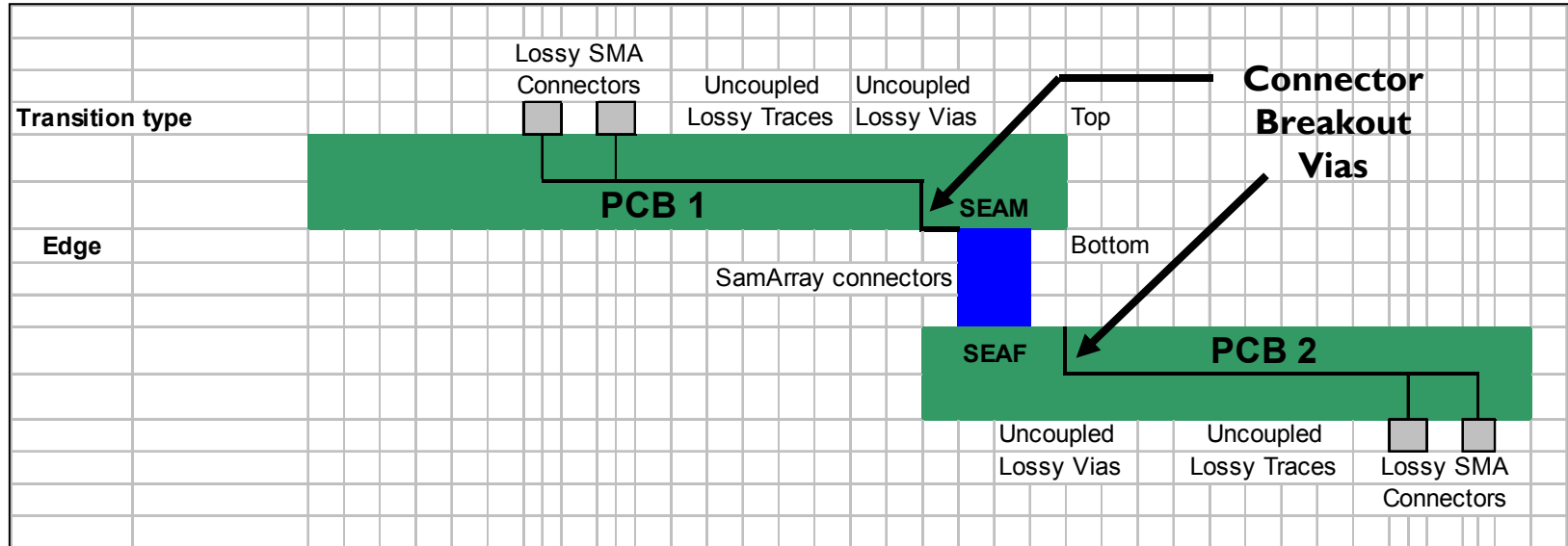
The “Final Inch™” is not complete without validation of the electrical system performance through signal integrity modeling, simulations and measurements.

- Advanced 2D and 3D modeling methods are used to provide accurate models for all sections of the Final Inch™ boards.
  - SE Array® connector models are developed through Samtec’s advanced modeling process, as found at [www.samtec.com](http://www.samtec.com).
  - Three dimensional PCB structures, such as SMA launches and vias, are modeled using a combination of CST Microwave Studio and Sigroty BroadBand SPICE.
  - Two dimensional PCB traces are modeled using Ansoft Maxwell 2D FEM Field Solver and Synopsis HSPICE w-element table models.
  - All modeled elements are combined and simulated using Synopsis HSPICE.

# PCB Stackup and Trace Configuration

- 12 layer 0.0862 construction
- Inner layer stripline routing.
- Outer layer escape and breakout
- 4.0 mil trace width, 6 mil separation
- 50 ohm characteristic impedance (single ended)
- 100 ohm differential impedance
- $\epsilon_r = 4.2$
- Loss Tangent = 0.0215

# PCB Stackup and Trace Configuration



# Eye Pattern Configuration

- Signal stimulus +/- 500 mV
- Driven victim and aggressor signals terminated differentially to 100 ohms.
- Eye simulations performed with worst case adjacent neighbor pairs switching at maximum bit rate and driven victim switching with a pseudo random pattern.

# Insertion and Return Loss Configuration

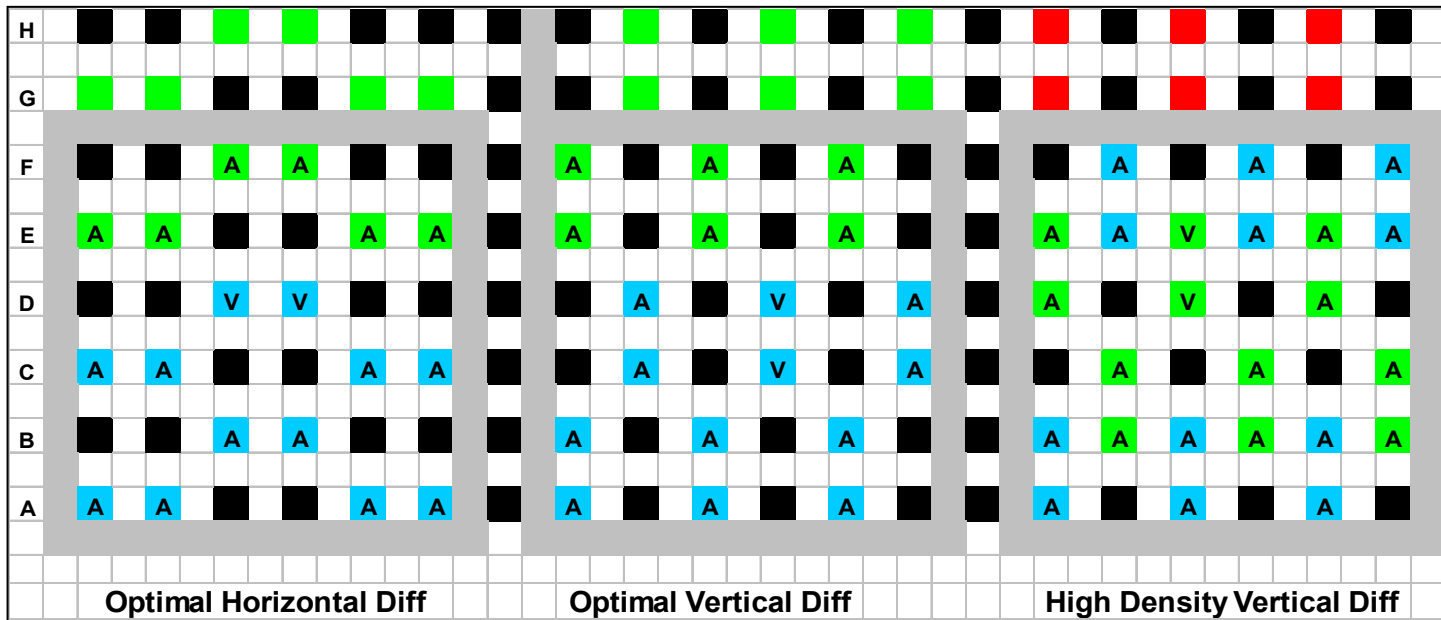
- Victim Input Stimulus - 1 Volt a.c., linearly swept from 100 MHz to 10 GHz
- Victim Signals terminated differentially at measurement point, 100 Ohms.
- Passive Signals terminated at both end points, 50 Ohms to Ground.



# Crosstalk Configuration

- Victim Input Stimulus - 1 Volt a.c., linearly swept from 100 MHz to 10 GHz
- Victim Signals terminated differentially at measurement point, 100 Ohms.
- Passive Signals terminated at both end points, 50 Ohms to Ground.

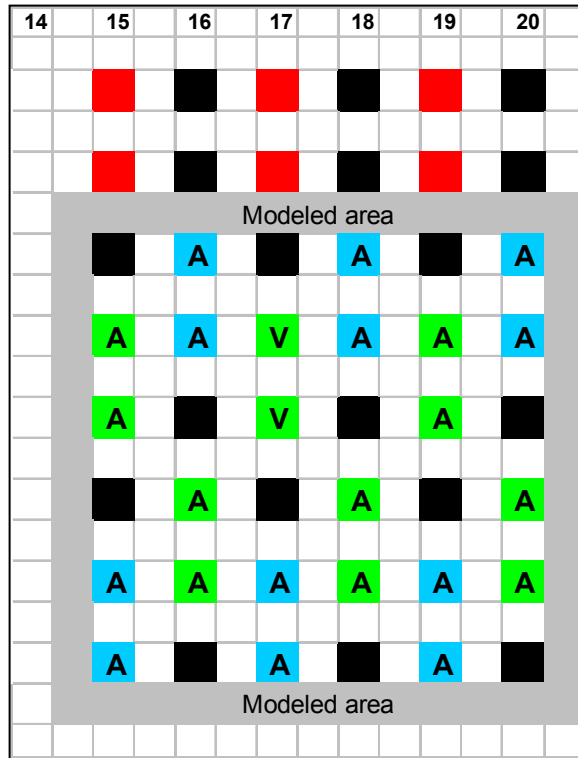
# Channel Eye Properties



■ = Signal Layer 10 (driven by Layer 1 SMAs)  
■ = Signal Layer 8 (driven by Layer 1 SMAs)  
■ = Signal Layer 5 (driven by Layer 12 SMAs)  
■ = Ground

**V = Victim pair**  
**A = Aggressor pairs**

# Channel Eye Properties, High Density Vertical Differential



■ = Signal Layer 10 (driven by Layer 1 SMAs)

■ = Signal Layer 8 (driven by Layer 1 SMAs)

■ = Ground

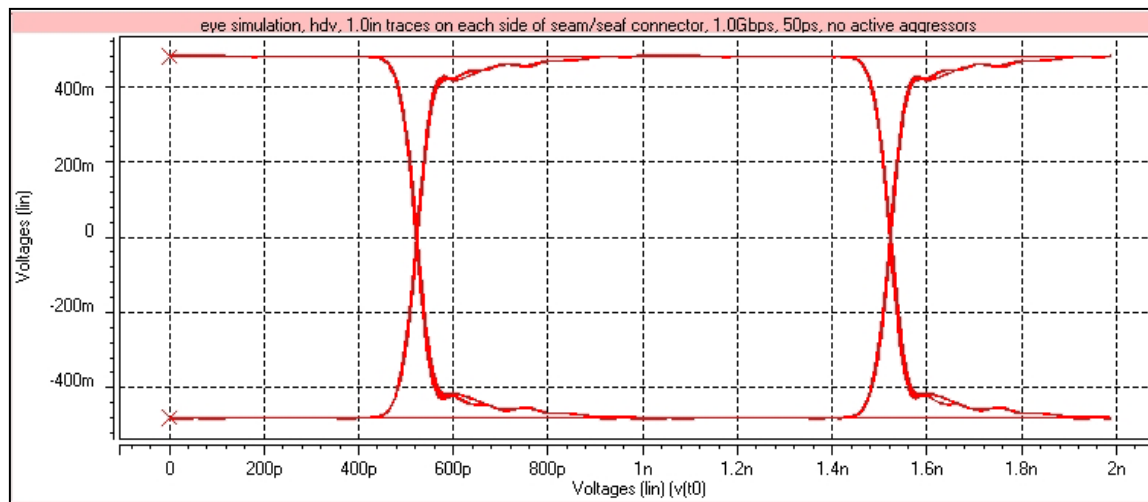
V = Victim pair

A = Aggressor pairs

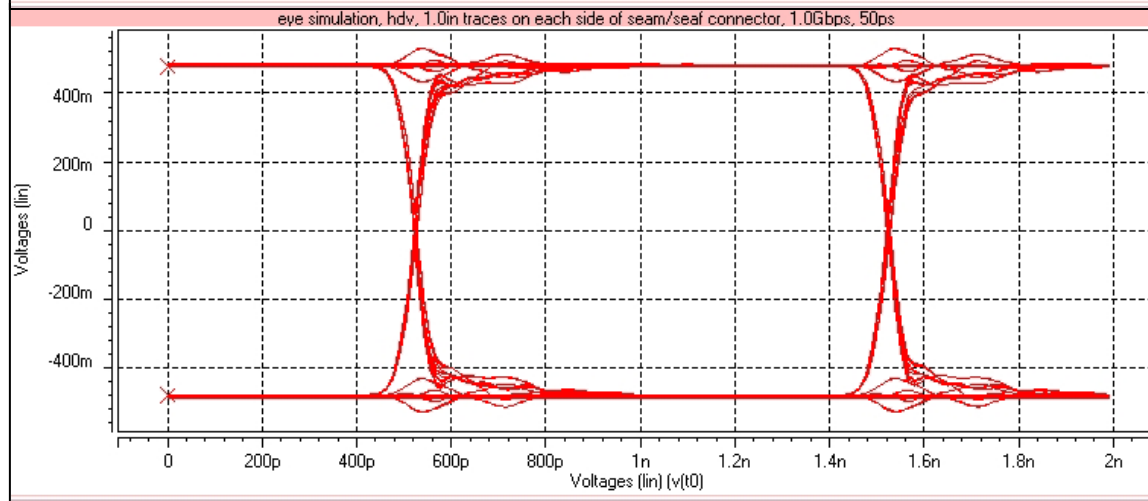
# Eye, High Density Vertical Differential - Victim pair, 1 Gbps, 50 psec. edges

1 inch Traces

No aggressor pairs active



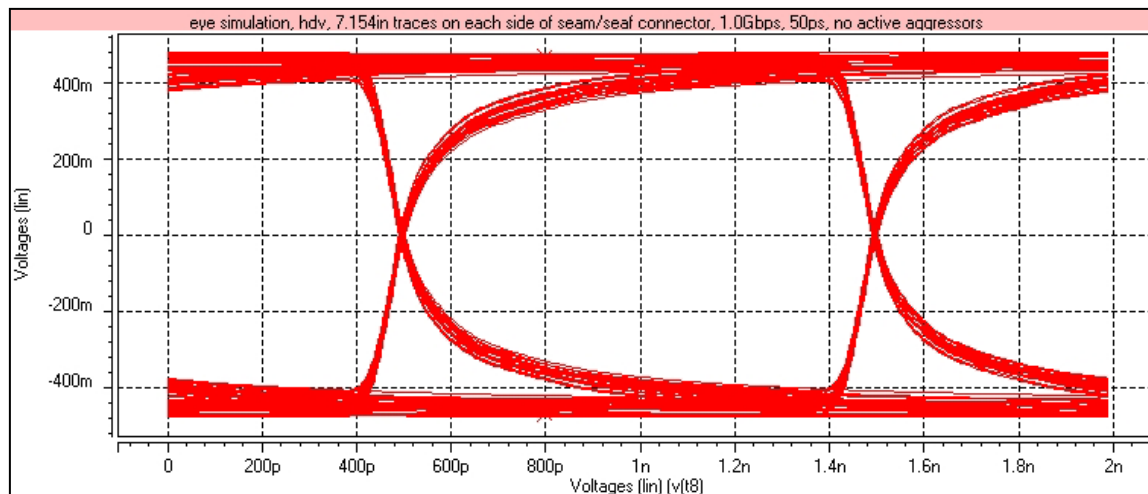
All aggressor pairs active



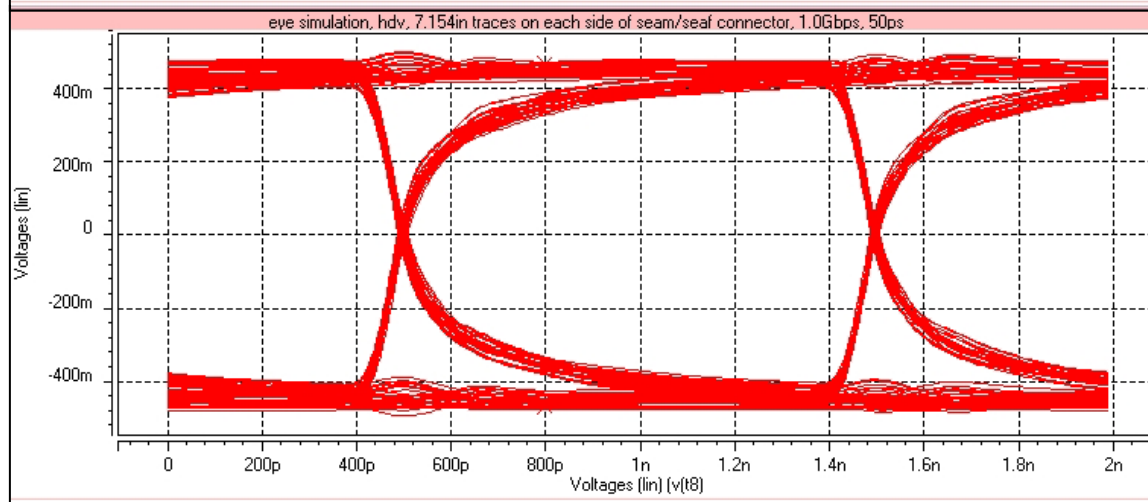
# Eye, High Density Vertical Differential - Victim pair, 1Gbps, 50 psec. edges

7.154 inch Traces

No aggressor pairs active



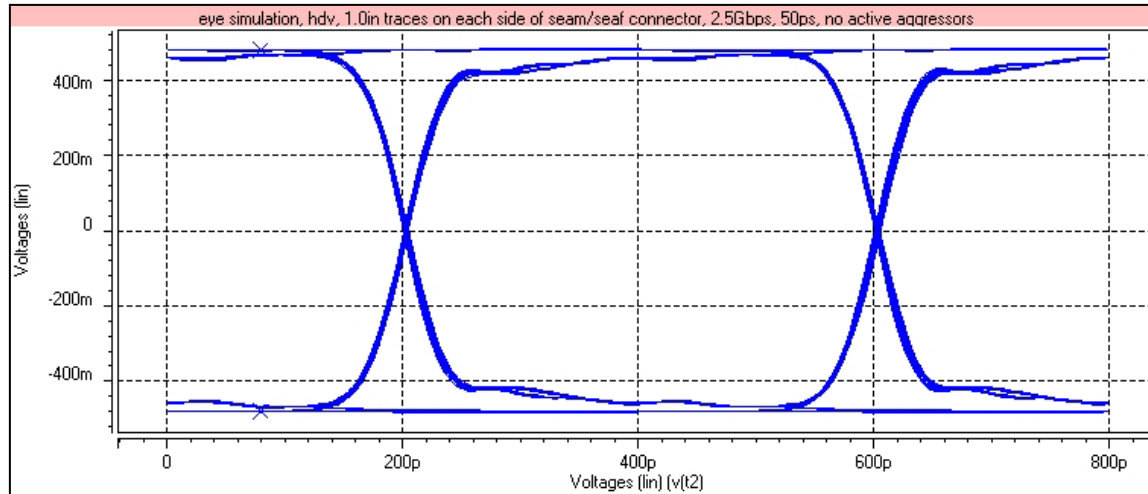
All aggressor pairs active



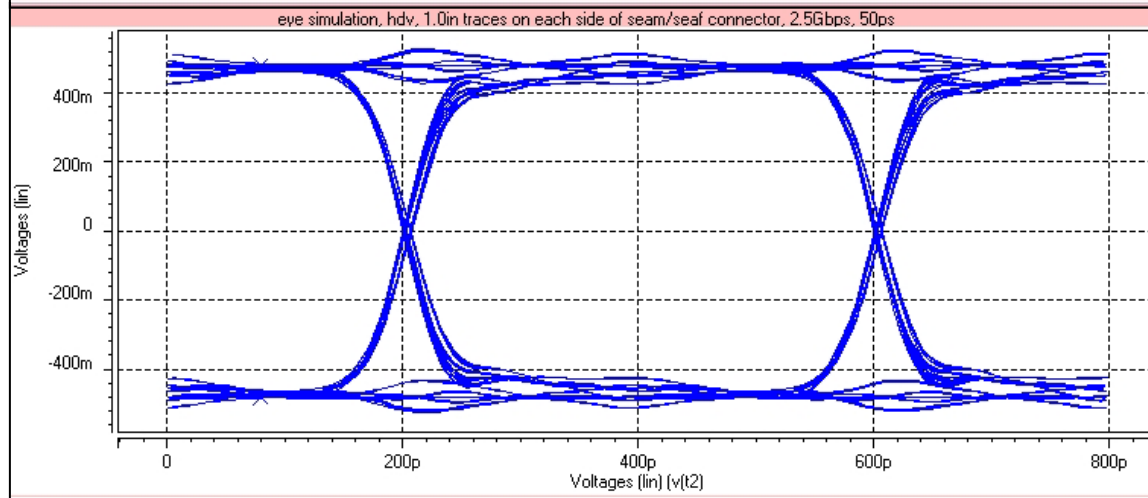
# Eye , High Density Vertical Differential - Victim pair, 2.5 Gbps, 50 psec. edges

1 inch Traces

No aggressor  
pairs active



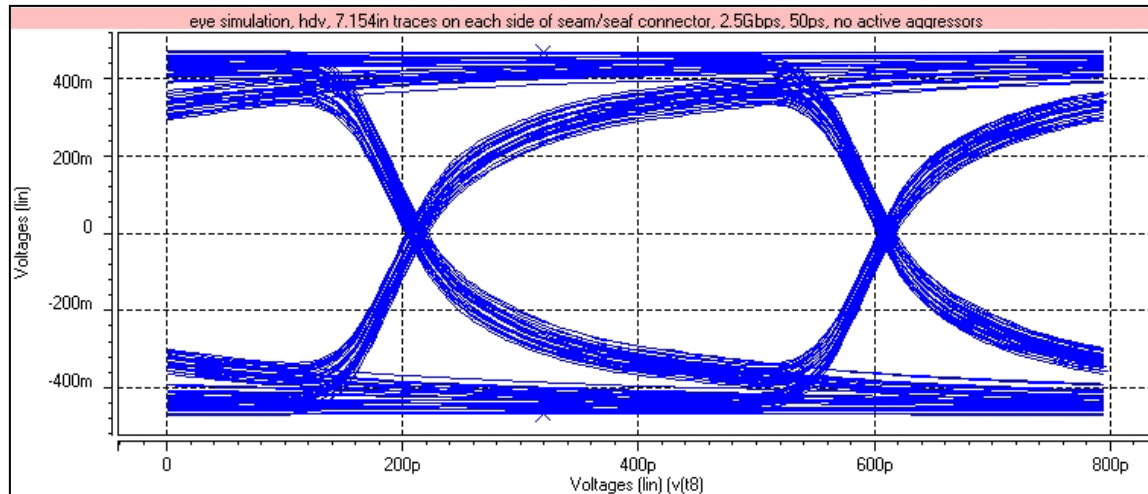
All aggressor  
pairs active



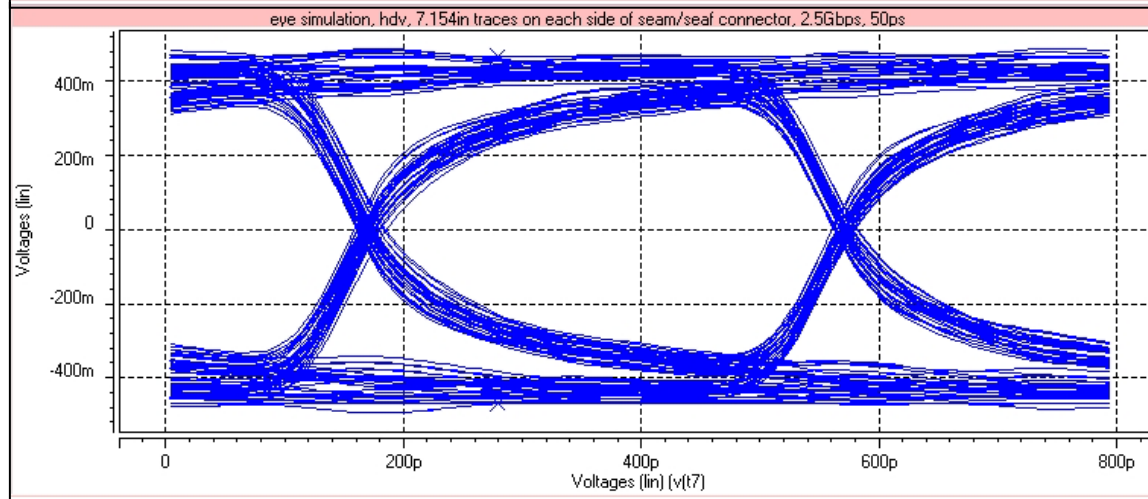
# Eye , High Density Vertical Differential - Victim pair, 2.5 Gbps, 50 psec. edges

7.154 inch Traces

No aggressor  
pairs active



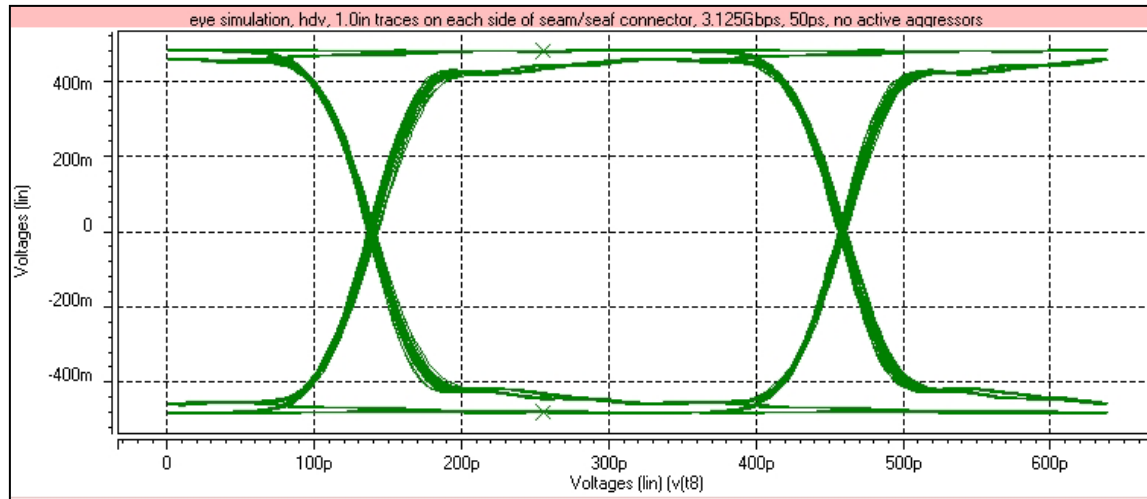
All aggressor  
pairs active



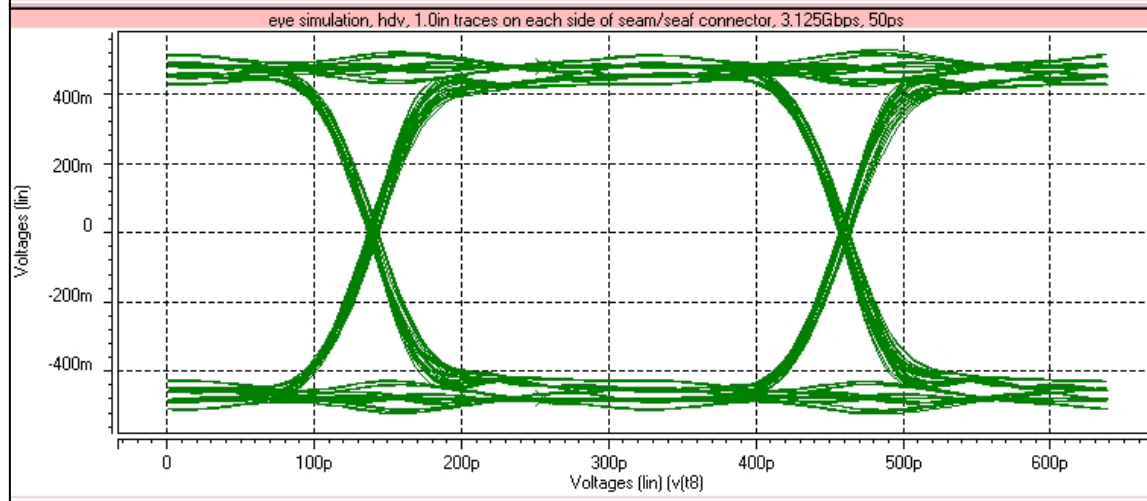
# Eye , High Density Vertical Differential - Victim pair, 3.125 Gbps, 50 psec. edges

1 inch Traces

No aggressor  
pairs active



All aggressor  
pairs active

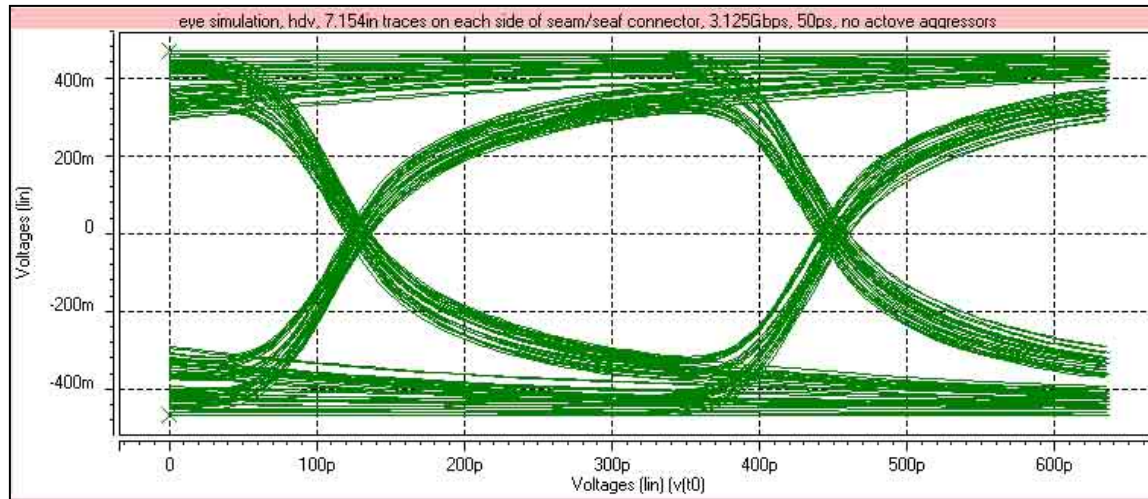




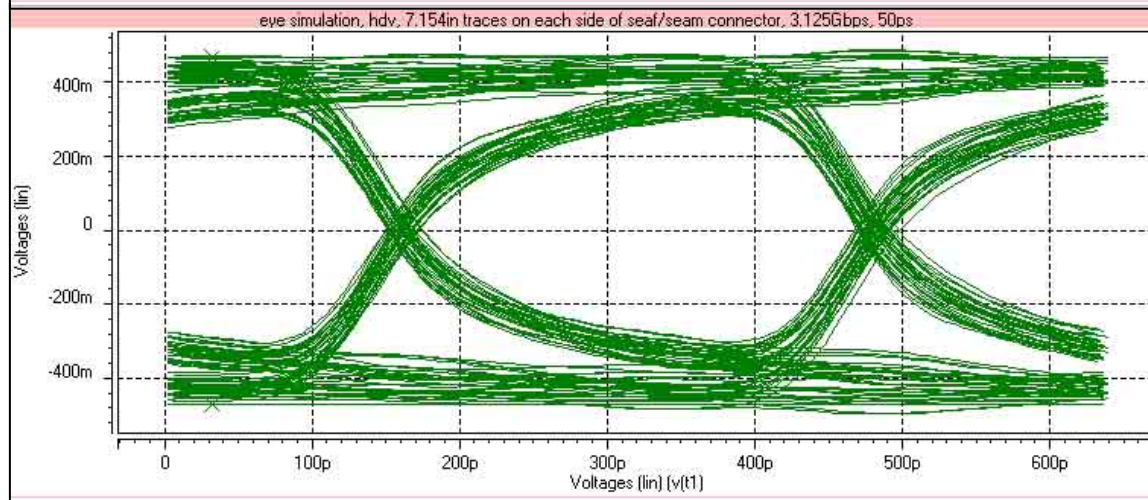
# Eye , High Density Vertical Differential - Victim pair, 3.125 Gbps, 50 psec. edges

7.154 inch Traces

No aggressor  
pairs active



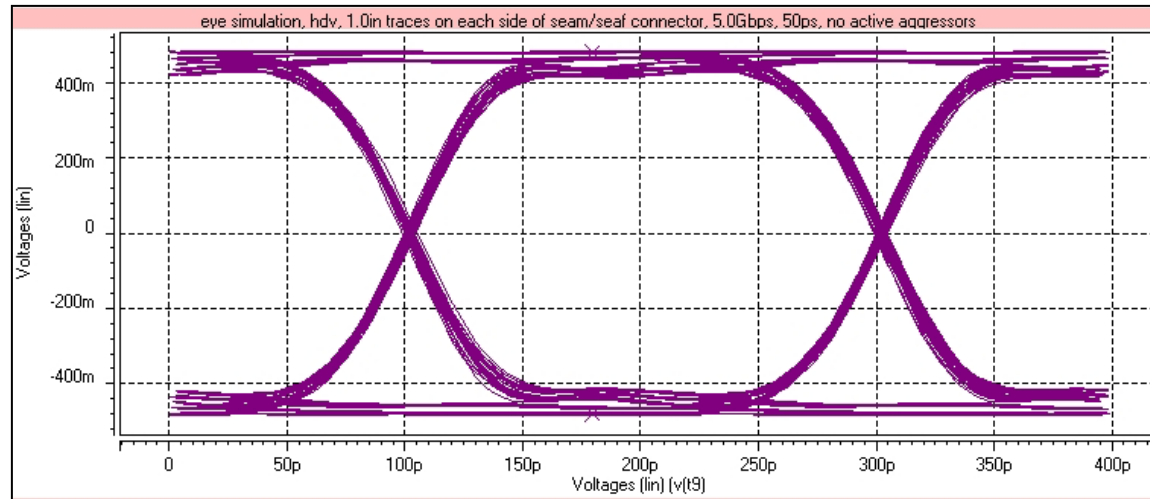
All aggressor  
pairs active



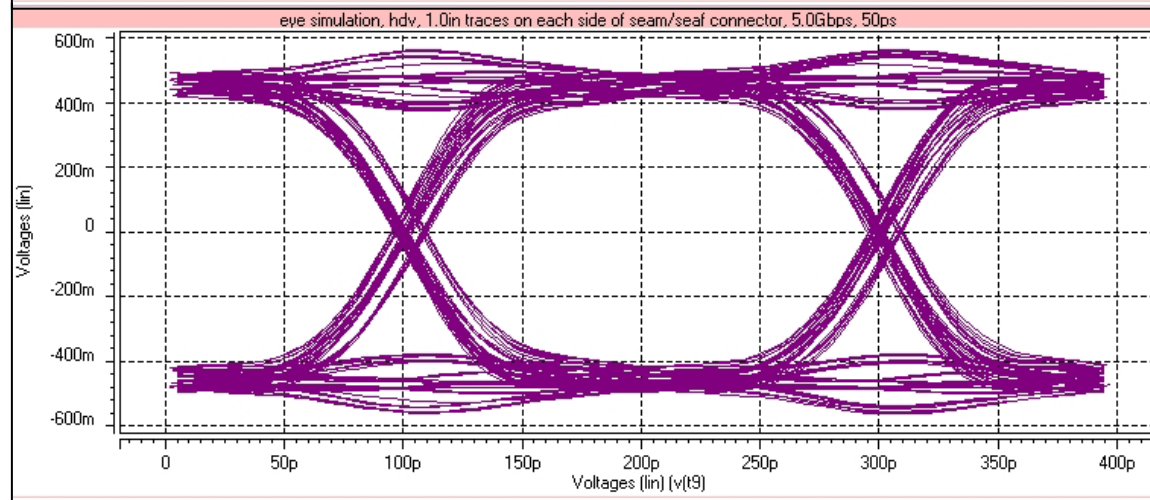
# Eye , High Density Vertical Differential - Victim pair, 5.0 Gbps, 50 psec. edges

1 inch Traces

No aggressor  
pairs active



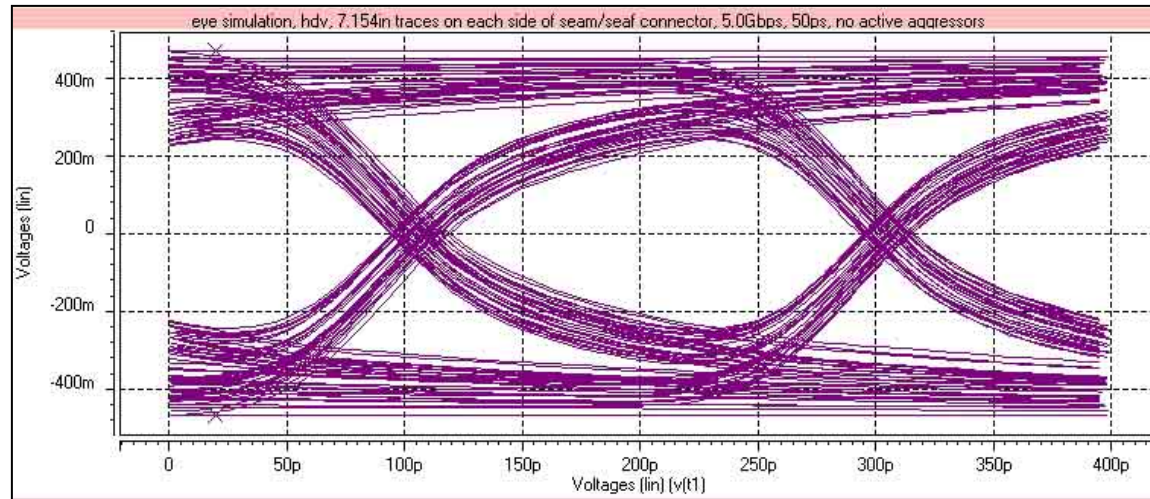
All aggressor  
pairs active



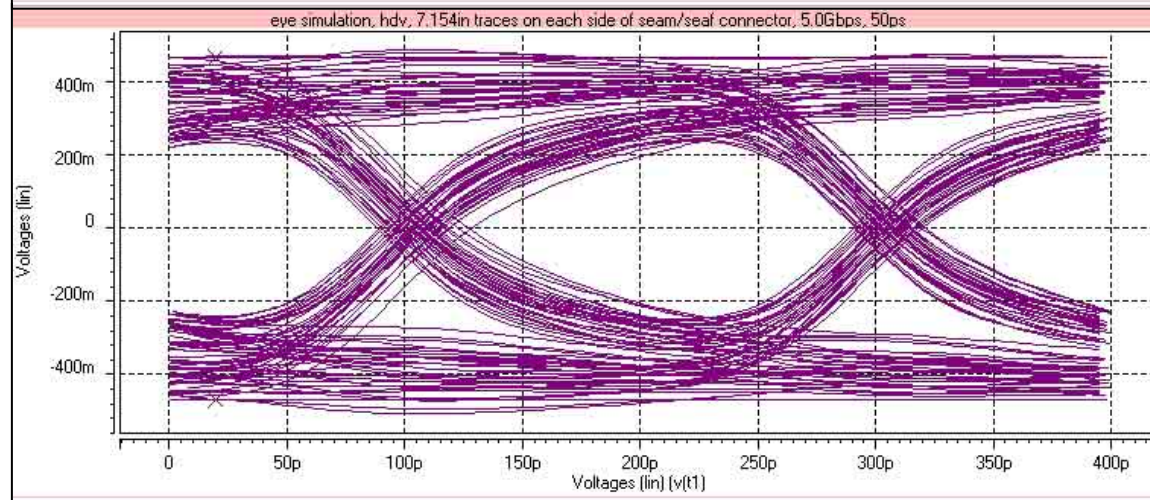
# Eye , High Density Vertical Differential - Victim pair, 5.0 Gbps, 50 psec. edges

7.154 inch Traces

No aggressor  
pairs active



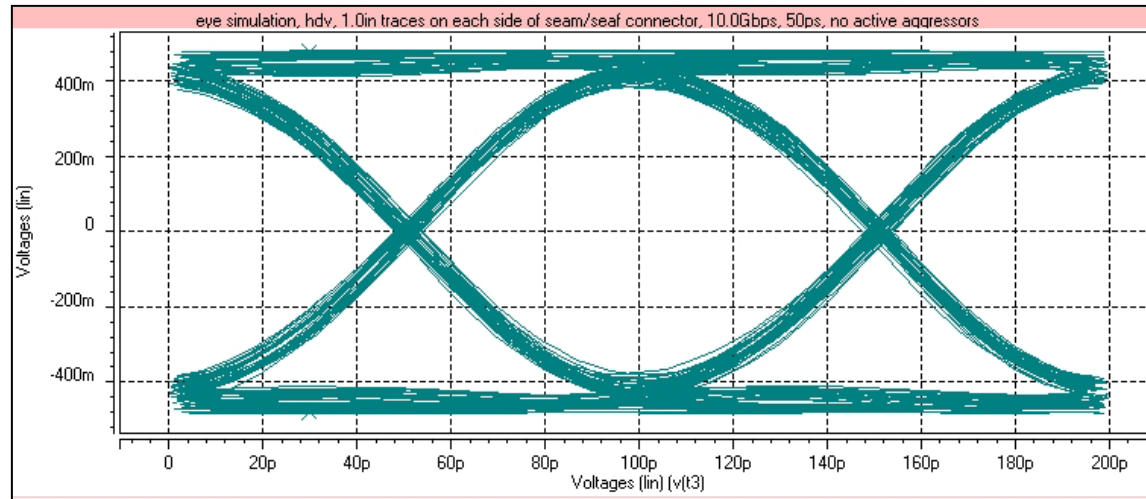
All aggressor  
pairs active



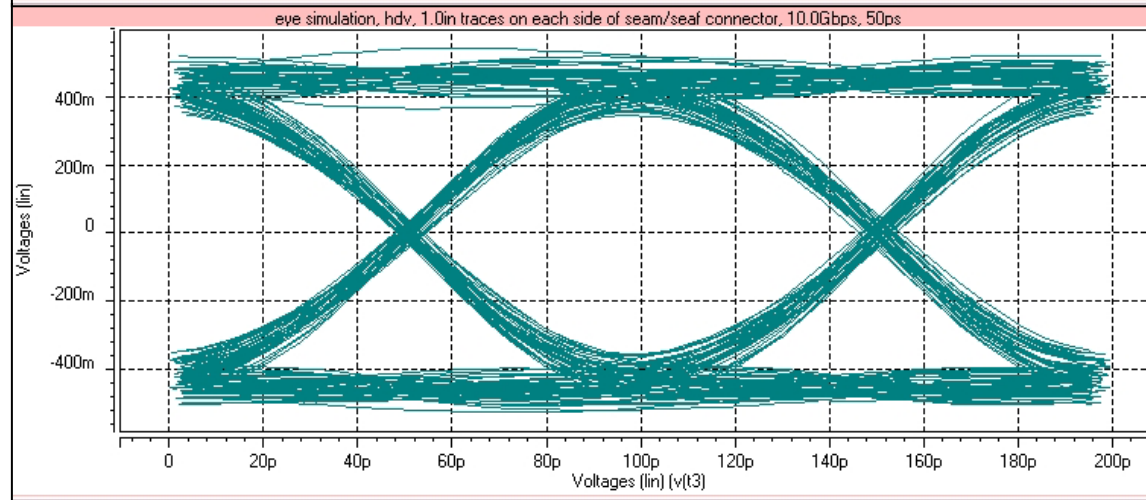
# Eye , High Density Vertical Differential - Victim pair, 10.0 Gbps, 50 psec. edges

1 inch Traces

No aggressor  
pairs active



All aggressor  
pairs active

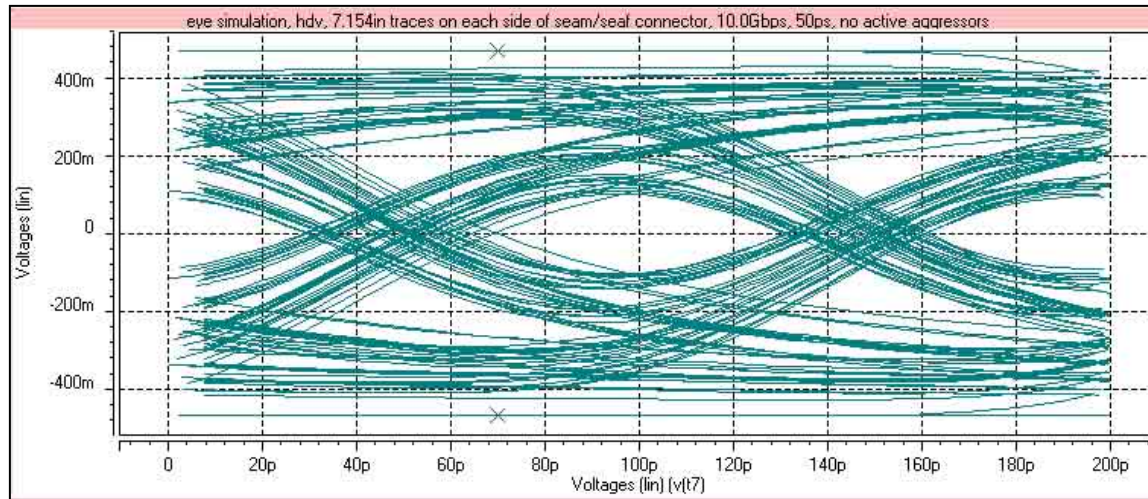




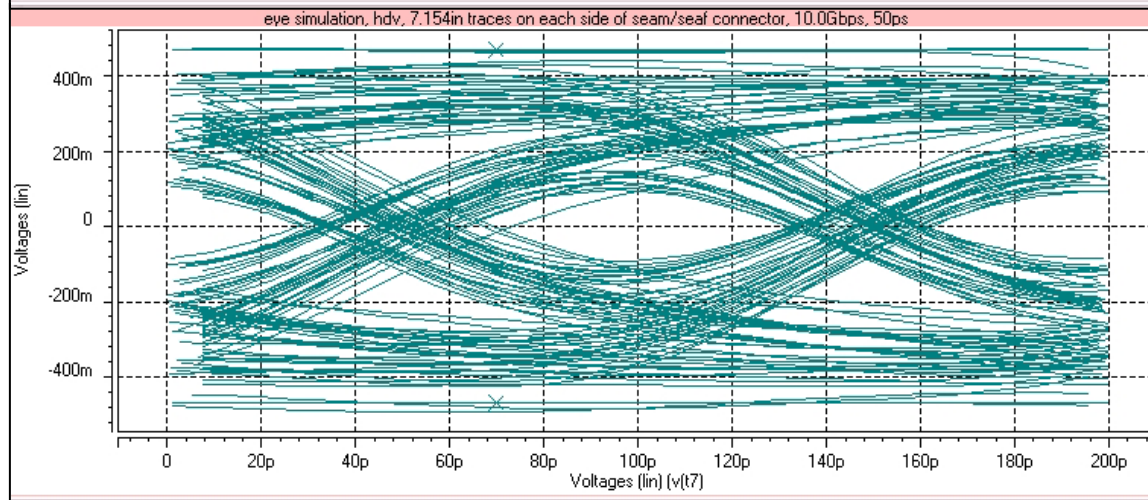
# Eye , High Density Vertical Differential - Victim pair, 10.0 Gbps, 50 psec. edges

7.154 inch Traces

No aggressor  
pairs active



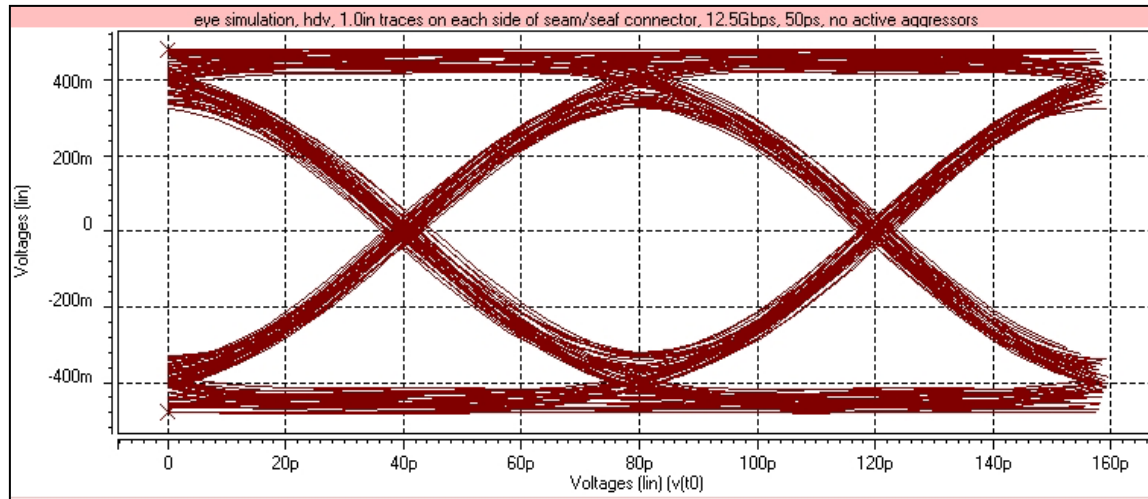
All aggressor  
pairs active



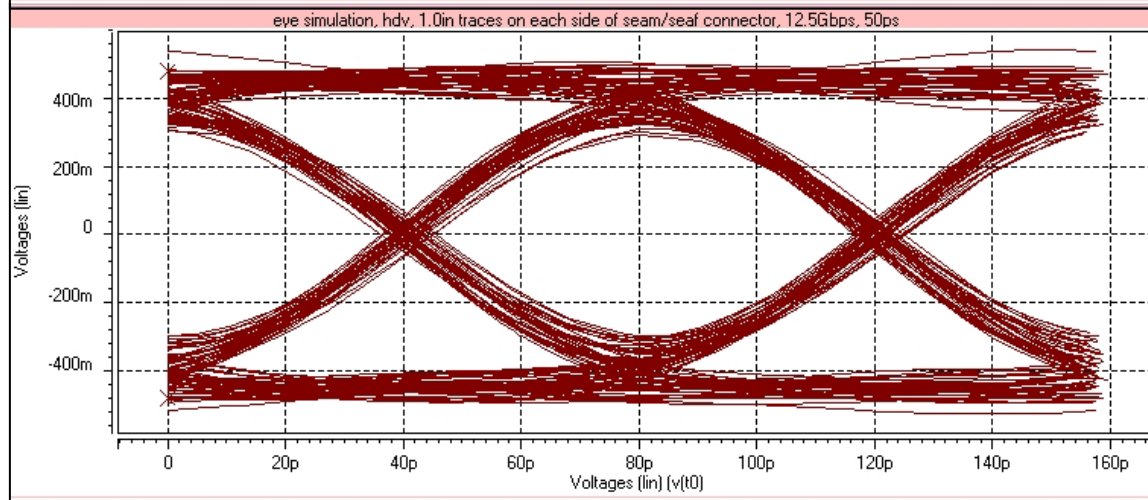
# Eye , High Density Vertical Differential - Victim pair, 12.5 Gbps, 50 psec. edges

1 inch Traces

No aggressor  
pairs active



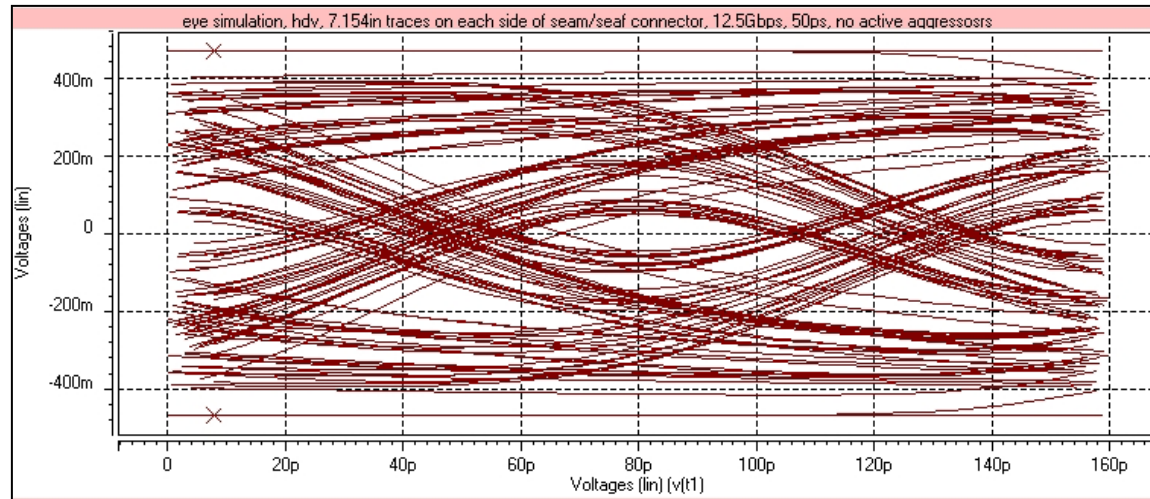
All aggressor  
pairs active



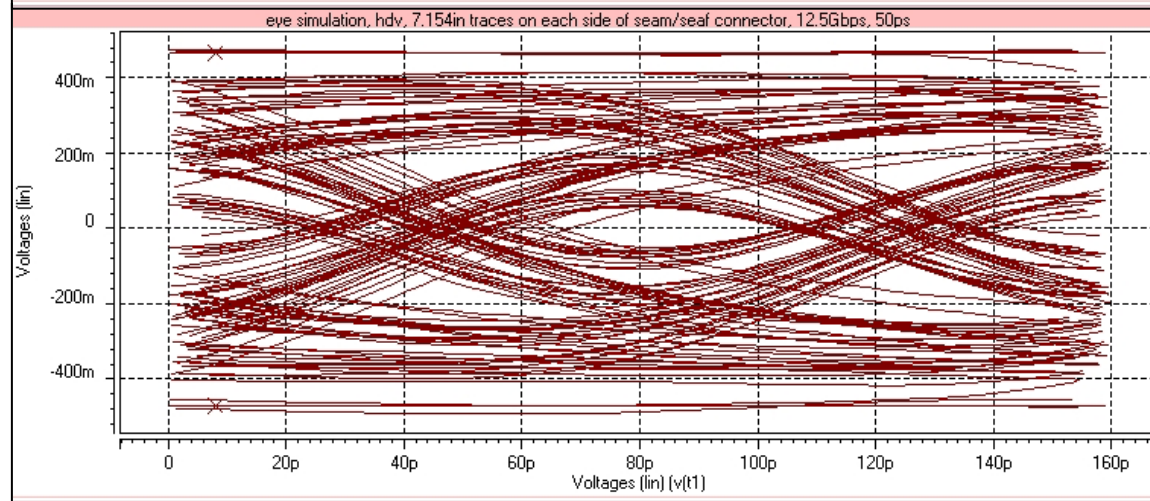
# Eye , High Density Vertical Differential - Victim pair, 12.5 Gbps, 50 psec. edges

7.154 inch Traces

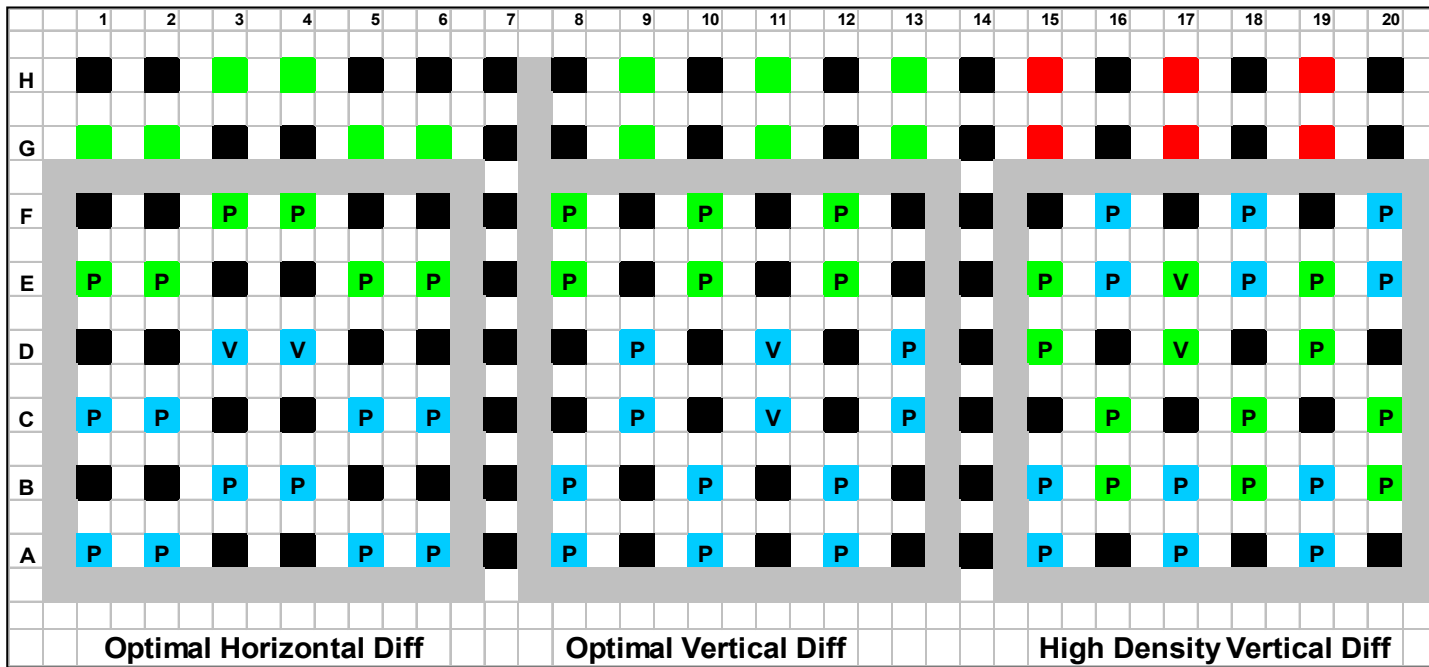
No aggressor  
pairs active



All aggressor  
pairs active



# Channel Loss



■ = Signal Layer 10 (driven by Layer 1 SMAs)

■ = Signal Layer 8 (driven by Layer 1 SMAs)

■ = Signal Layer 5 (driven by Layer 12 SMAs)

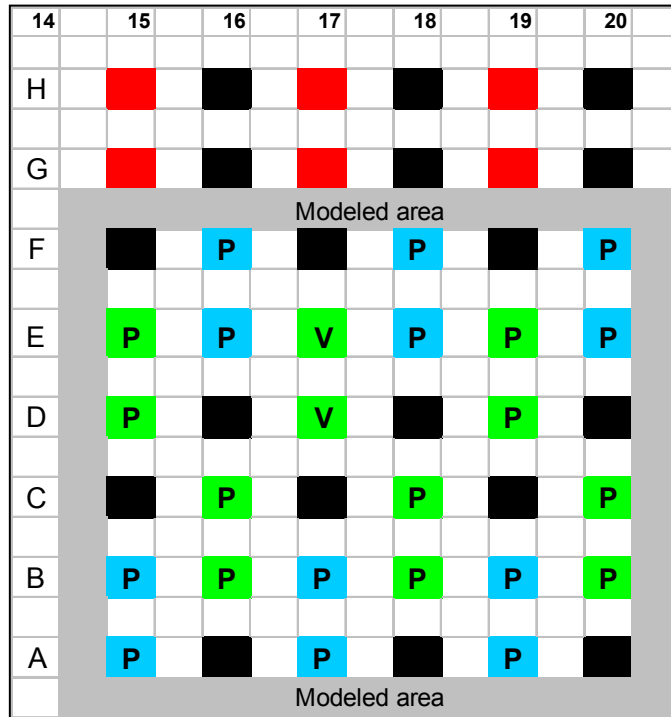
■ = Ground

V = Victim pair

P = Passive pairs



# Insertion Loss , High Density Vertical Differential



■ = Signal Layer 10 (driven by Layer 1 SMAs)

■ = Signal Layer 8 (driven by Layer 1 SMAs)

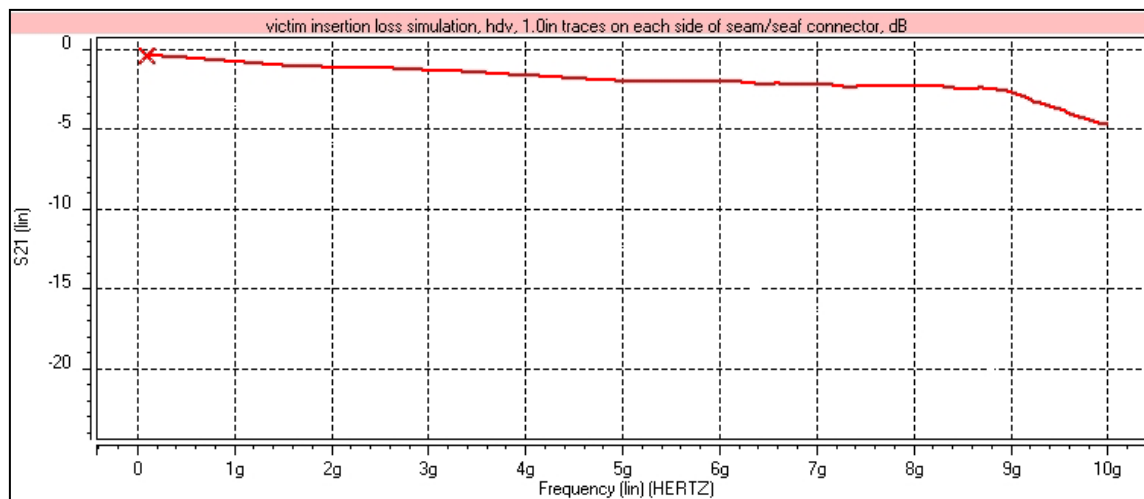
■ = Ground

V = Victim pair

P = Passive pairs

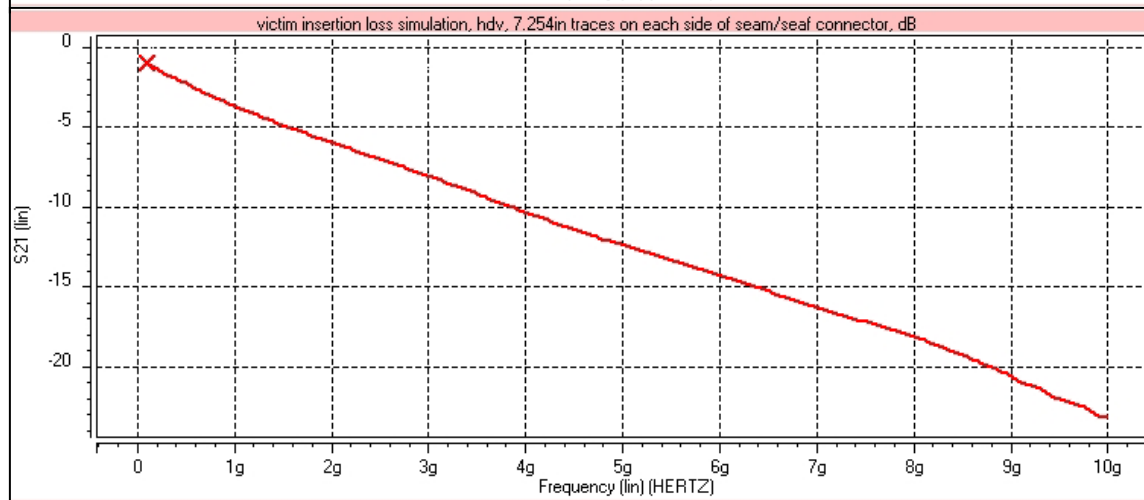
# Insertion Loss , High Density Vertical Differential

1 inch Traces



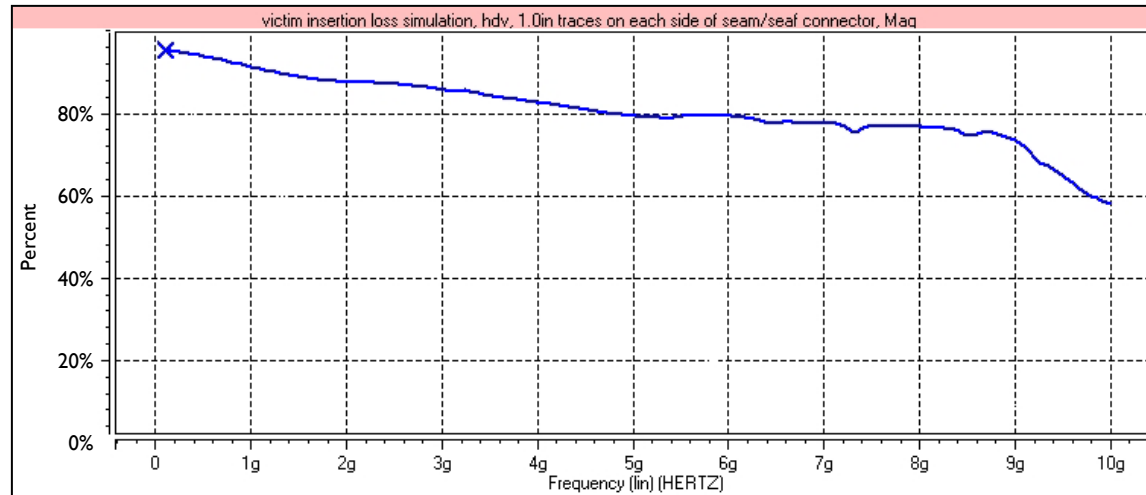
**dB vs. Frequency**

7.154 inch Traces



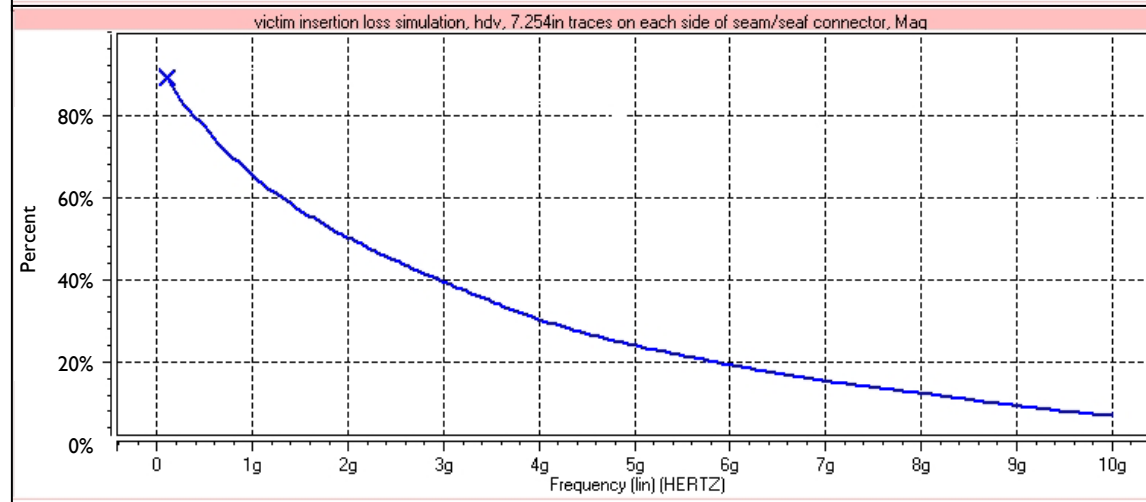
# Insertion Loss , High Density Vertical Differential

1 inch Traces



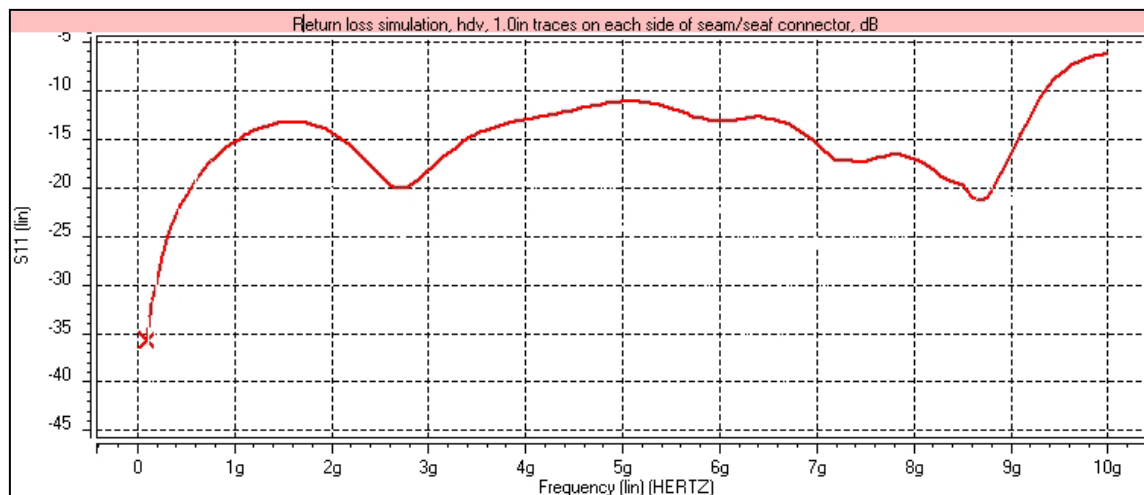
% vs. Frequency

7.154 inch Traces



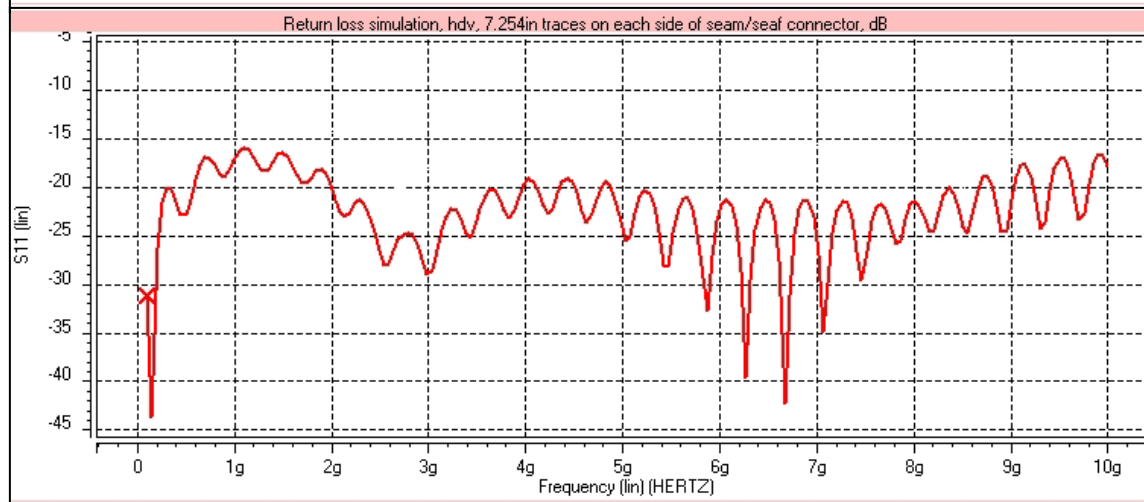
# Return Loss , High Density Vertical Differential

1 inch Traces



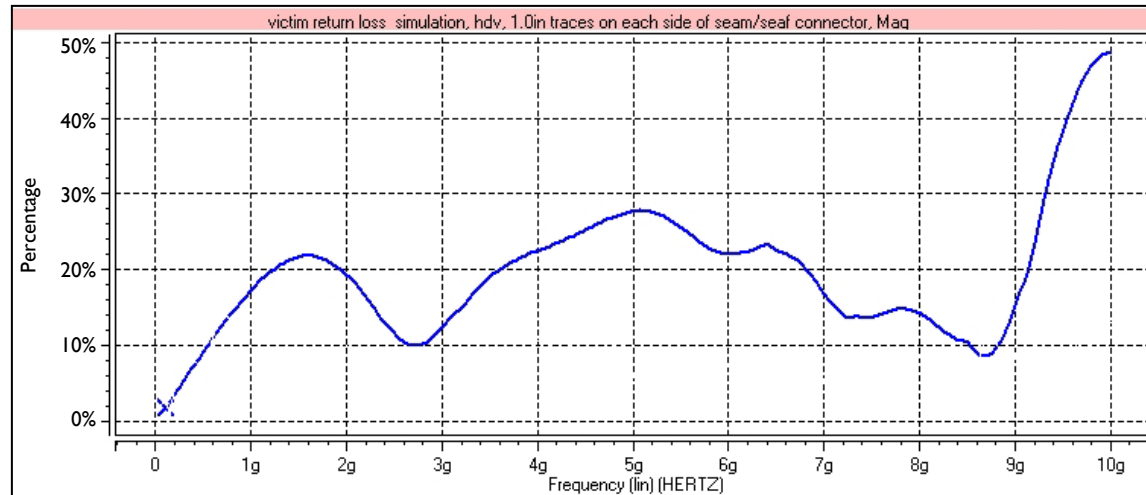
**dB vs. Frequency**

7.154 inch Traces



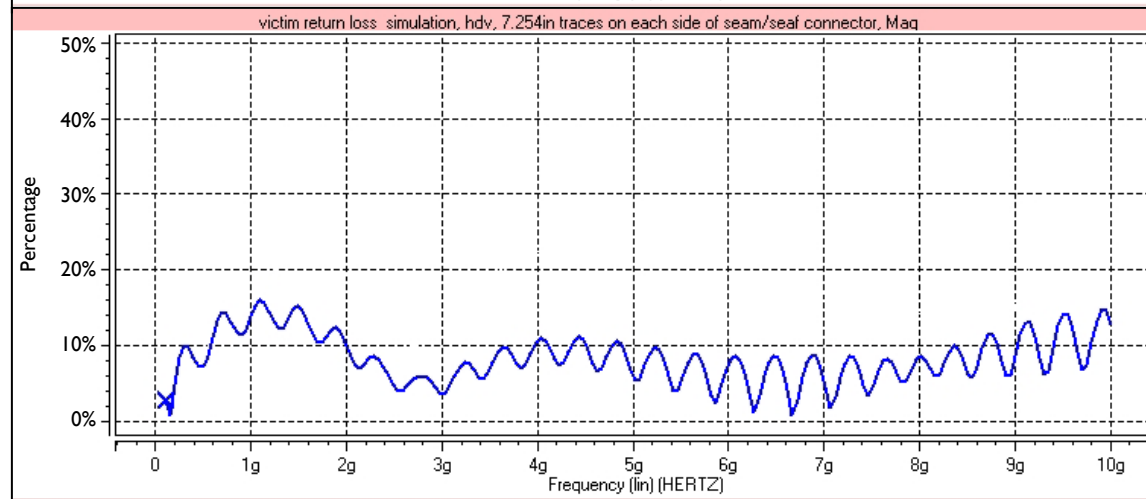
# Return Loss , High Density Vertical Differential

1 inch Traces

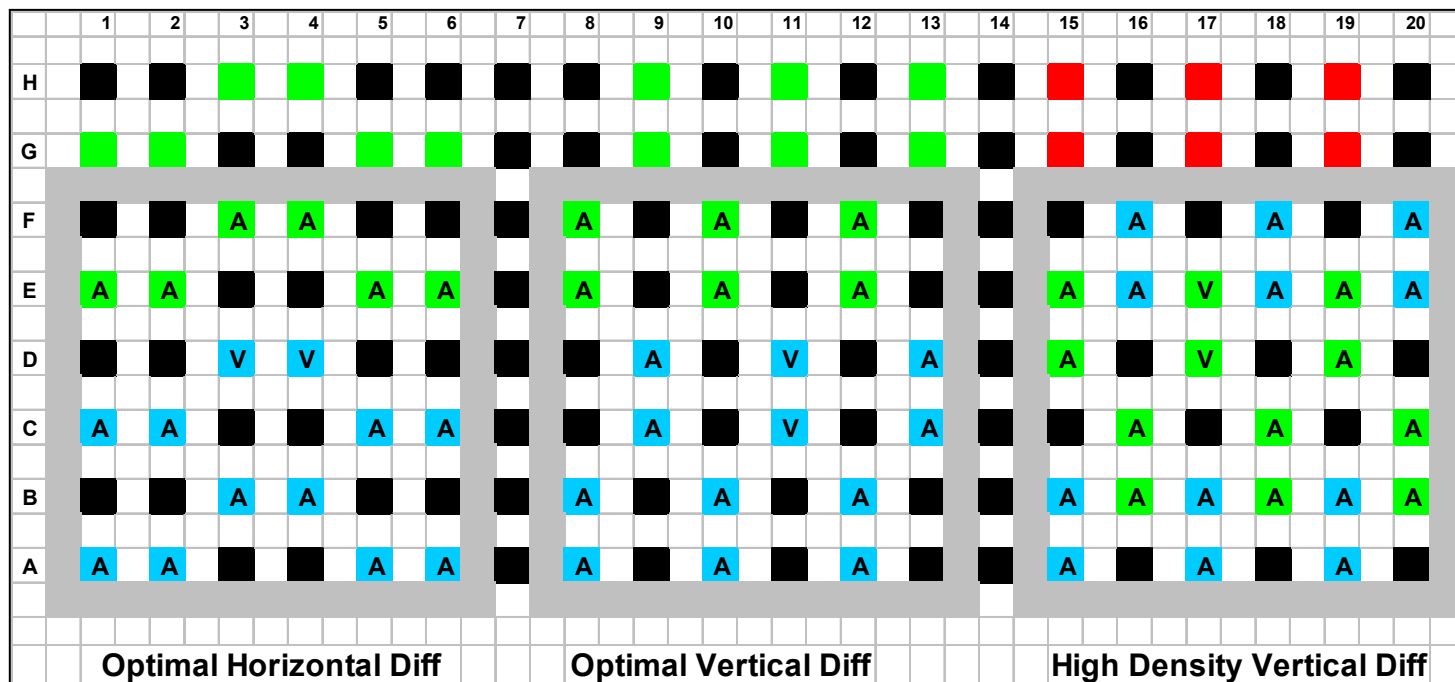


% vs. Frequency

7.154 inch Traces



# Channel Crosstalk



■ = Signal Layer 10 (driven by Layer 1 SMAs)

■ = Signal Layer 8 (driven by Layer 1 SMAs)

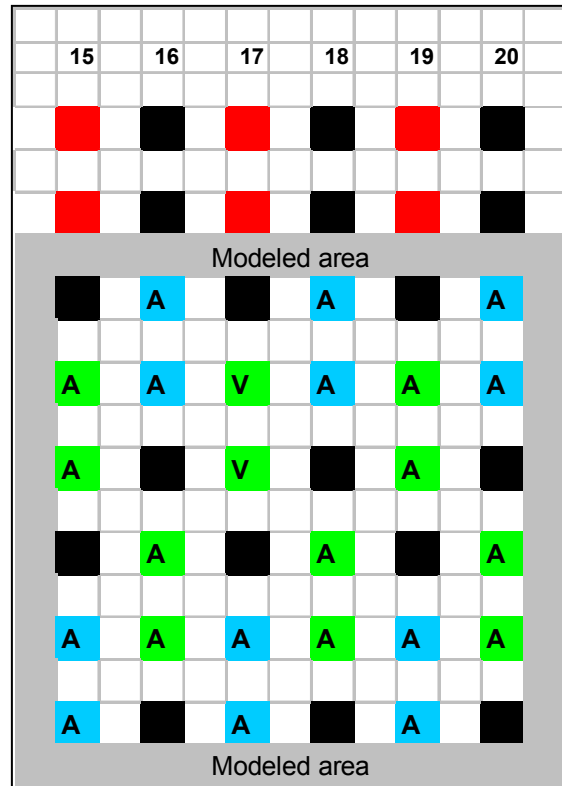
■ = Signal Layer 5 (driven by Layer 12 SMAs)

■ = Ground

V = Victim pair

A = Aggressor pairs

# Crosstalk, High Density Vertical Differential



■ = Signal Layer 10 (driven by Layer 1 SMAs)

■ = Signal Layer 8 (driven by Layer 1 SMAs)

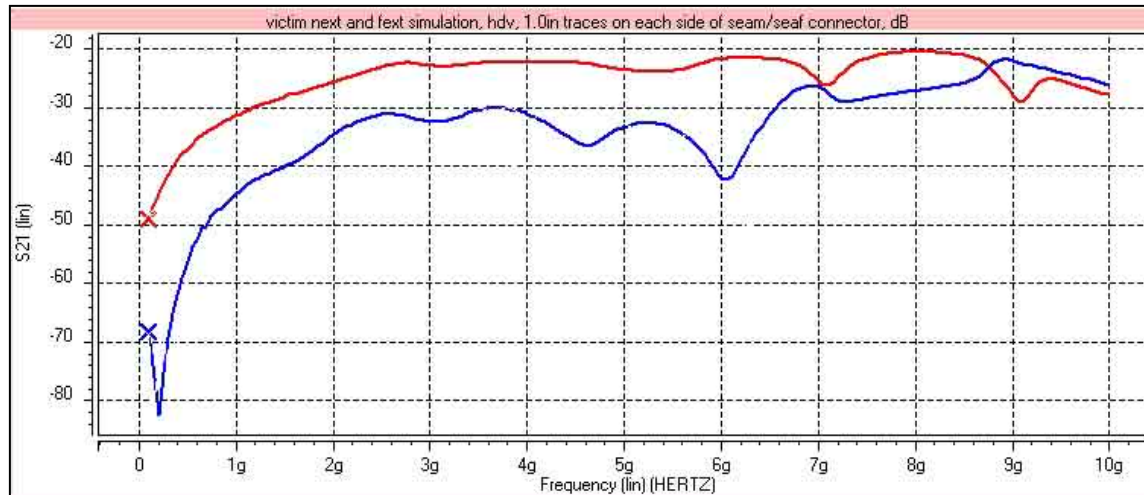
■ = Ground

V = Victim pair

A = Aggressor pairs

# Crosstalk, High Density Vertical Differential

1 inch Traces

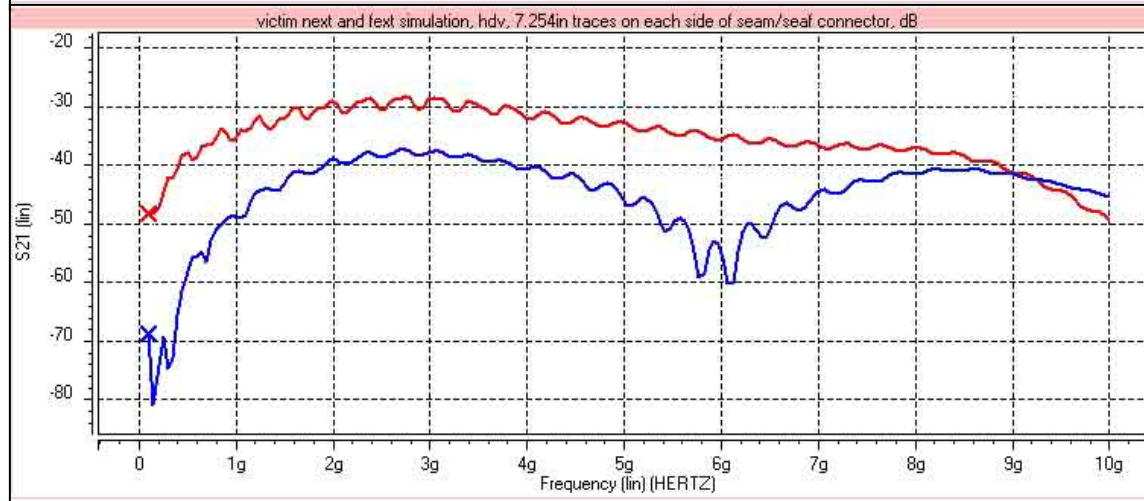


**dB vs. Frequency**

**Red = NEXT**

**Blue = FEXT**

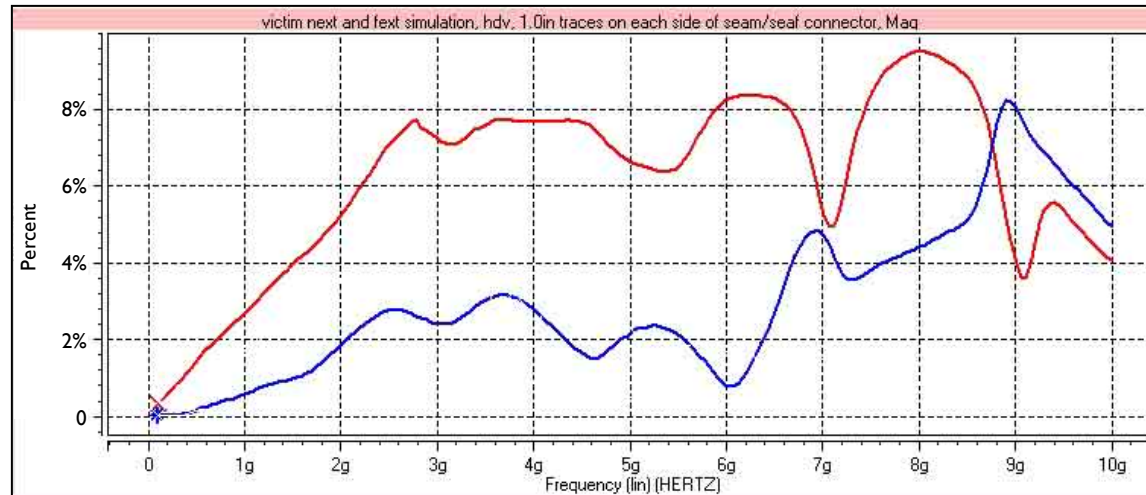
7.154 inch Traces





# Crosstalk, High Density Vertical Differential

1 inch Traces

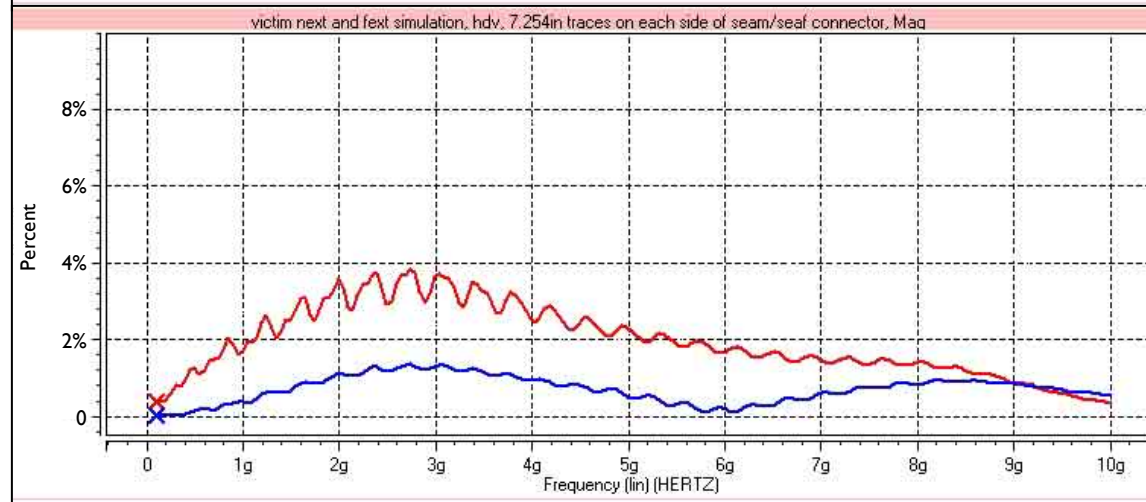


% vs. Frequency

Red = NEXT

Blue = FEXT

7.154 inch Traces



# SE Array<sup>®</sup> Final Inch<sup>™</sup> High Density Vertical Differential Test Case - Conclusions

- SE Array<sup>®</sup> connectors used in the high density vertical differential configuration show excellent performance in simulated applications between 5 and 10 Gbps.