

THN 15-WI Series

Application Note

DC/DC Converter 9 to 36Vdc or 18 to 75Vdc Input 3.3 to 15Vdc Single Outputs and ±5Vdc to ±15Vdc Dual Outputs 15W Output Power



Complete THN 15-WI datasheet can be downloaded at: http://www.tracopower.com/products/thn15wi.pdf

Features

- Single output current up to 4A
- 15 watts output power
- 4:1 ultra wide input voltage range of 9-36 and 18-75VDC
- Industry standard pin-out TEN 15 series compatible
- High efficiency up to 87%
- Low profile: 25.4 x 25.4 x 9.9mm (1.0 x 1.0 x 0.39 inch)
- Fixed switching frequency
- RoHS directive compliant
- No minimum load
- Input to output isolation: 1500Vdc, min
- Input to output isolation (BASIC INSULATION)
- Input under-voltage protection
- Output over-voltage protection
- Over-current protection, auto-recovery
- Output short circuit protection
- Remote ON/OFF control
- Adjustable output voltage

Options

Heat sinks available for extended operation temperature

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Applications

- Wireless Network
- Telecom/Datacom
- Industry Control System
- Measurement
- Semiconductor Equipment

General Description

THN 15-WI single output DC/DC converters provide up to 15 watts of output power in an industry standard package and footprint. These units are specifically designed to meet the power needs of low profile. All models feature with 4:1 ultra wide input voltage of 9-36 Vdc and 18-75 Vdc ,comprehensively protected against over-current, over-voltage and input under-voltage protection conditions, and adjustable output voltage.

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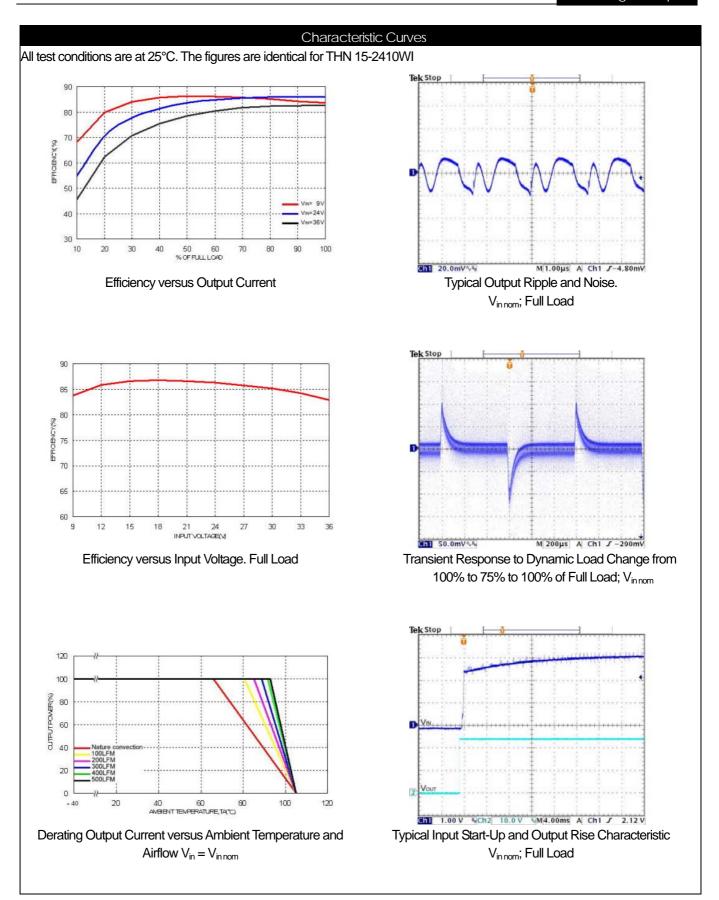
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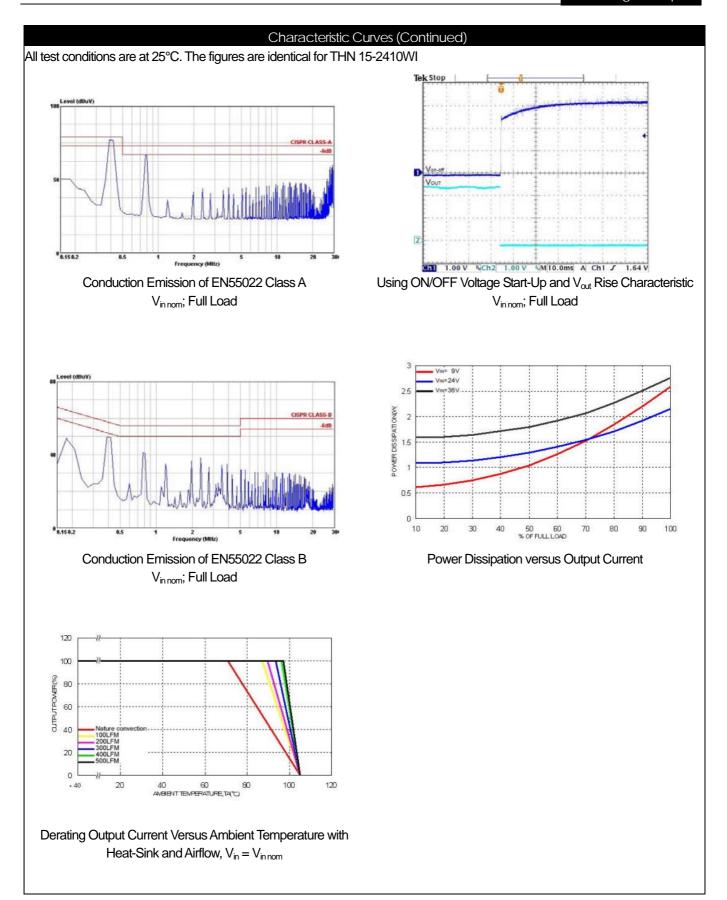
Abso	Absolute Maximum Rating					
Parameter	Model	Min	Max	Unit		
Input Voltage						
Continuous	THN 15-24xxWI		36			
	THN 15-48xxWI		75	Vdc		
Transient (100mS)	THN 15-24xxWI		50			
	THN 15-48xxWI		100			
Input Voltage Variation	All		5	V/mS		
(complies with ETS300 132 part 4.4)	All		5	V/IIIO		
Operating Ambient Temperature (with derating)	All	-40	85	°C		
Storage Temperature	All	-55	125	°C		

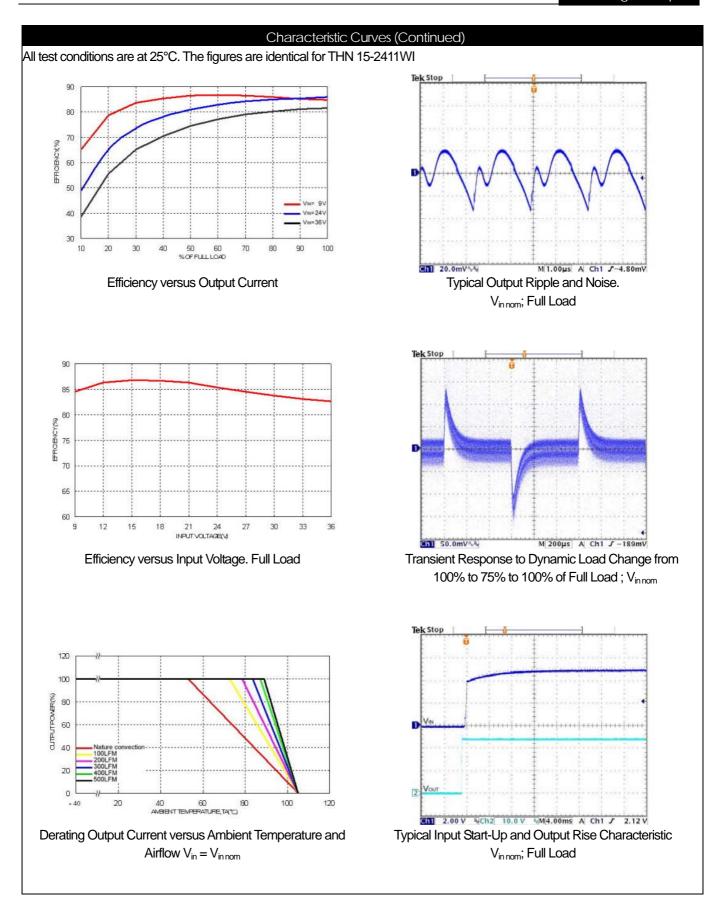
Output Voltage Range		utput Specification				
Output Voltage Range (V _{nnmi} Full Load; T _A = 25 °C) THN 15-xx10WI THN 15-xx12WI THN 15-xx12WI THN 15-xx12WI THN 15-xx2WI THN 15-xx10WI THN 15			Min	Tvp	Max	Unit
(V _{Intrami}) Full Load; T _A = 25 °C) THN 15-xx12WI THN 15-xx12WI THN 15-xx12WI THN 15-xx2WI THN 15-xx1WI THN 15-xx2WI THN 15-xx2WI THN 15-xx2WI THN 15-xx1WI THN 15-xx2WI THN 15-xx1WI THN 15-xx2WI THN 15-xx1WI THN 15-xx1WI THN 15-xx1WI THN 15-xx2WI THN 15-xx1WI THN 15-xx2WI THN 15-xx1WI THN 15-xx2WI THN 15-xx1WI THN 15-xx1WI THN 15-xx1WI THN 15-xx2WI THN 15-xx1WI THN 15-xx2WI THN 15-xx1WI THN 15-xx2WI THN 15-xx2WI						
THN 15-xx12Wi		THN 15-xx11WI				
THN 15-xx21WI	,	THN 15-xx12WI	11.88	12	12.12	
THN 15-xx21Wi		THN 15-xx13WI	14.85	15	15.15	V_{DC}
THN 15-xx23WI ±14.85 ±15 ±15.15 Voltage Adjustability (See Page 25) All -10 +10 % Output Regulation Line (V _{Inmin} to V _{Inmax} at Full Load) All -0.2 +0.2 % Load (0% to 100% of Full Load) THN 15-xx10WI 75 mrV P _{In-Pax} (Measured with a 1 μF M/C and a 10 μF T/C) All other 100 3 % V _{Out} V _{Inmin} to V _{Inmax} Table (1 and page 25°C) All -0.02 +0.22 % Cutput Voltage Overshoot (V _{Inmin} to V _{Inmax} Full Load; T _A = 25°C) All -0.02 +0.02 % PC Output Over Voltage Protection THN 15-xx10WI 0 1300 mrV P _{In-Pax} Voltage Clamped THN 15-xx12WI 0 1300 mrV P _{In-Pax} THN 15-xx12WI 0 1300 mrV P _{In-Pax} THN 15-xx12WI 0 1500 1500 THN 15-xx12WI 0 1500 1500 THN 15-xx12WI 13.5 19.6 THN 15-xx12WI (Voltage Clamped) THN 15-xx12WI 15.6 7.0 THN 15-xx12WI 16.8 20.5 Vdc THN 15-xx22WI 13.5 19.6 THN 15-xx22WI THN 15-xx22WI 13.5 19.6		THN 15-xx21WI	±4.95	±5	±5.05	
Voltage Adjustability (See Page 25) All -10 +10 %		THN 15-xx22WI	±11.88	±12	±12.12	
Output Regulation All -0.2 +0.2 % Load (0% to 100% of Full Load) All -0.2 +0.2 % Output Ripple & Noise(See Page 21) THN 15-xx10WI 75 mV _{Pk-Pk} Peak-to-Peak (20MHz bandwidth) All other 100 mV _{Pk-Pk} (Measured with a 1µF M/C and a 10µF T/C) All other 100 3 % C Output Voltage Overshoot (V _{nmax} , Full Load; T _A = 25°C) All 0 3 % V _{out} Dynamic Load Response (V _{nmax} , T _A = 25°C) All 300 mV mV Dynamic Load Step change from 75% to 100% or 100 to 75% of Full Load Peak Deviation All 300 mV mV Setting Time (V _{OUT} <10% peak deviation)		THN 15-xx23WI	±14.85	±15	±15.15	
Line (V _{nnmi} to V _{nmax} at Full Load) Load (0% to 100% of Full Load) Output Ripple & Noise(See Page 21) Peak-to-Peak (20MHz bandwidth) (Measured with a 1µF M/C and a 10µF T/C) Temperature Coefficient All -0.02 +0.02 */°C Output Voltage Overshoot (V _{nnmi} to V _{nmax} Full Load; T _A = 25°C) Dynamic Load Response (V _{nnmi} to V _{nmax} Full Load; T _A = 25°C) Load step change from 75% to 100% or 100 to 75% of Full Load Peak Deviation Setting Time (V _{CUT} <10% peak deviation) All 300	Voltage Adjustability (See Page 25)	All	-10		+10	%
Load (0% to 100% of Full Load) -0.2 +0.2 Output Ripple & Noise(See Page 21) THN 15-xx10WI 75 Peak-to-Peak (20MHz bandwidth) All other 100 (Measured with a 1µF M/C and a 10µF T/C) All other 100 Temperature Coefficient All -0.02 +0.02 %°C Output Voltage Overshoot (V _{nmin} to V _{inmax} ; Full Load; T _A = 25°C) All 0 3 %°V _{out} Dynamic Load Response (V _{nmin} T _A = 25°C) Load step change from 75% to 100% or 100 to 75% of Full Load Peak Deviation Setting Time (V _{OUT} < 10% peak deviation)	Output Regulation					
Output Ripple & Noise(See Page 21) THN 15-xx10WI 75 mV _{Pk-Pk} Peak-to-Peak (20MHz bandwidth) (Measured with a 1µF M/C and a 10µF T/C) All other 100 mV _{Pk-Pk} Temperature Coefficient All -0.02 +0.02 %°C Output Voltage Overshoot (V _{Inmin} to V _{Inmax} ; Full Load; T _A = 25°C) All 0 3 % V _{out} Dynamic Load Response (V _{Innom} ; T _A = 25°C) Load step change from 75% to 100% or 100 to 75% of Full Load Peak Deviation Setting Time (V _{Out} < 10% peak deviation)	Line (V _{in min} to V _{in max} at Full Load)	All	-0.2		+0.2	%
Peak-to-Peak (20MHz bandwidth) (Measured with a 1µF M/C and a 10µF T/C) All other 100 mV _{Pk-Pk}	Load (0% to 100% of Full Load)		-0.2		+0.2	
Measured with a 1μF M/C and a 10μF T/C All other 100	Output Ripple & Noise(See Page 21)	THN 15-xx10WI		75		
Temperature Coefficient	Peak-to-Peak (20MHz bandwidth)					mV _{Pk-Pk}
Output Voltage Overshoot (V _{nmin} to V _{nmax} ; Full Load; T _A = 25°C) All 0 3 % V _{out} Dynamic Load Response (V _{nmin} ; T _A = 25°C) Load step change from 75% to 100% or 100 to 75% of Full Load Peak Deviation Setting Time (V _{OUT} < 10% peak deviation)	(Measured with a 1µF M/C and a 10µF T/C)	All other		100		
Cynmin to Vnmax; Full Load; T _A = 25°C)	Temperature Coefficient	All	-0.02		+0.02	%/°C
Dynamic Load Response CV _{nrom} ; T _A = 25°C)	Output Voltage Overshoot	٨١١		0	2	0/ \/
(V _{nnom} ; T _A = 25°C) Load step change from 75% to 100% or 100 to 75% of Full Load Peak Deviation All 300 250 mV μS Setting Time (V _{OUT} < 10% peak deviation)	$(V_{in min} \text{ to } V_{in max}; \text{ Full Load}; T_A = 25^{\circ}\text{C})$	All		U	3	∕o ∨ _{out}
Load step change from 75% to 100% or 100 to 75% of Full Load Peak Deviation Setting Time (V _{OUT} < 10% peak deviation)	Dynamic Load Response					
All Setting Time (Vout < 10% peak deviation) All All S50 mV μS	$(V_{in nom}; T_A = 25^{\circ}C)$					
Peak Deviation Setting Time (V _{OUT} < 10% peak deviation) All 250 mV μS						
Setting Time (V _{OUT} < 10% peak deviation) All 250		All		300		mV
Output Current THN 15-xx10WI 0 4000 THN 15-xx11WI 0 3000 THN 15-xx12WI 0 1300 THN 15-xx21WI 0 1000 mA THN 15-xx22WI 0 ±1500 THN 15-xx22WI 0 ±625 THN 15-xx23WI 0 ±500 Output Over Voltage Protection THN 15-xx10WI 3.7 THN 15-xx11WI 5.6 THN 15-xx11WI 5.6 THN 15-xx12WI 13.5 THN 15-xx12WI 13.5 THN 15-xx12WI 15.6 THN 15-xx22WI 16.8 THN 15-xx22WI 16.8 Output Over Current Protection All 150 % FL.		All		250		иS
THN 15-xx11W 0 3000 THN 15-xx12W 0 1300 THN 15-xx12W 0 1000 mA 1500		THN 15-vv10\\/\	0		4000	'
THN 15-xx12WI 0	Ouput Guiterit		_			
THN 15-xx13WI 0			_			
THN 15-xx21W 0			_			mΔ
THN 15-xx22WI 0 ±625			_			1101
THN 15-xx23WI 0 ±500			_			
Output Over Voltage Protection THN 15-xx10WI 7.0 3.7 5.4 (Voltage Clamped) THN 15-xx11WI 5.6 7.0 THN 15-xx12WI 13.5 19.6 THN 15-xx21WI 5.6 7.0 THN 15-xx21WI 5.6 7.0 THN 15-xx22WI 13.5 19.6 THN 15-xx23WI 16.8 20.5 Output Over Current Protection All 150 % FL.			_			
(Voltage Clamped) THN 15-xx11WI THN 15-xx11WI THN 15-xx12WI THN 15-xx12WI THN 15-xx13WI THN 15-xx13WI THN 15-xx21WI THN 15-xx21WI THN 15-xx21WI THN 15-xx22WI THN 15-xx22WI THN 15-xx22WI THN 15-xx23WI THN 15-x	Output Over Voltage Protection		_			
THN 15-xx12WI 13.5 19.6 20.5 Vdc THN 15-xx21WI 5.6 7.0 THN 15-xx22WI 13.5 19.6 7.0 THN 15-xx22WI 13.5 19.6 7.0 THN 15-xx22WI 16.8 20.5 Cutput Over Current Protection All 150 % FL.	I =					
THN 15-xx13WI 16.8 20.5 Vdc THN 15-xx21WI 5.6 7.0 THN 15-xx22WI 13.5 19.6 THN 15-xx23WI 16.8 20.5 Output Over Current Protection All 150 % FL.	(1-1-1-1-30 3-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1-1					
THN 15-xx21Wl 5.6 7.0						Vdc
THN 15-xx22WI THN 15-xx22WI THN 15-xx23WI 13.5 THN 15-xx23WI 16.8 THN 15-xx23WI 150 THN 15-xx23WI <t< td=""><td></td><td></td><td></td><td></td><td></td><td></td></t<>						
THN 15-xx23WI 16.8 20.5 Output Over Current Protection All 150 % FL.						
Output Over Current Protection All 150 % FL.						
	Output Over Current Protection			150		% FL.
	Output Short Circuit Protection	All	H	l .	natics recover	

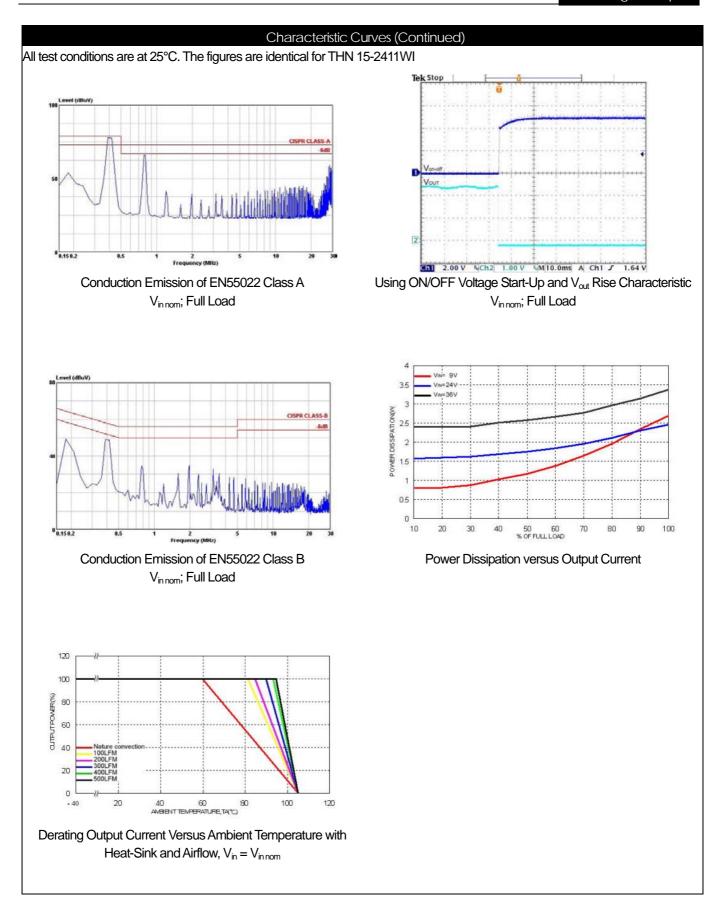
	Input Specification				
Parameter	Model	Min	Тур	Max	Unit
Operating Input Voltage	THN 15-24xxWI	9	24	36	\
	THN 15-48xxWI	18	48	75	Vdc
Input Current	THN 15-2410WI			688	
(Maximum value at V _{in nom} ; Full Load)	THN 15-2411WI			782	
,	THN 15-2412WI			803	
	THN 15-2413WI			772	
	THN 15-2421WI			772	
	THN 15-2422WI			753	
	THN 15-2423WI			744	Л
	THN 15-4810WI			336	mA
	THN 15-4811WI			382	
	THN 15-4812WI			392	
	THN 15-4813WI			377	
	THN 15-4821WI			386	
	THN 15-4822WI			382	
	THN 15-4823WI			377	
Input Standby Current	THN 15-2410WI		50		
(Typical value at V _{in nom} ; No Load)	THN 15-2411WI		50		
	THN 15-2412WI		20		
	THN 15-2413WI		20		
	THN 15-2421WI		20		
	THN 15-2422WI		25		
	THN 15-2423WI		25		m Λ
	THN 15-4810WI		40		mA
	THN 15-4811WI		40		
	THN 15-4812WI		15		
	THN 15-4813WI		15		
	THN 15-4821WI		15		
	THN 15-4822WI		15		
	THN 15-4823WI		20		
Under Voltage Lockout Turn-on Threshold	THN 15-24xxWI			9	Vdc
	THN 15-48xxWI			18	Vuc
Under Voltage Lockout Turn-off Threshold	THN 15-24xxWI		8		Vdc
	THN 15-48xxWI		16		Vac
Input Reflected Ripple Current (See Page 21)	All		30		mA _{Pk-Pk}
(5 to 20MHz, 12µH source impedance)	All		30		ITIA Pk-Pk
Start Up Time					
(V _{in nom} and constant resistive load)					
Power up	All		30		mS
Remote ON/OFF			30		
Remote ON/OFF Control (See Page 27)					
(The ON/OFF pin voltage is referenced to $-V_{in}$)	All				
Positive Logic DC-DC ON(Open)	\tag{\tau}	3		15	Vdc
DC-DC OFF(Short)		0		1.2	
Remote Off Input Current	All		2.5		mA
Input Current of Remote Control Pin	All	-0.5		1.0	mA

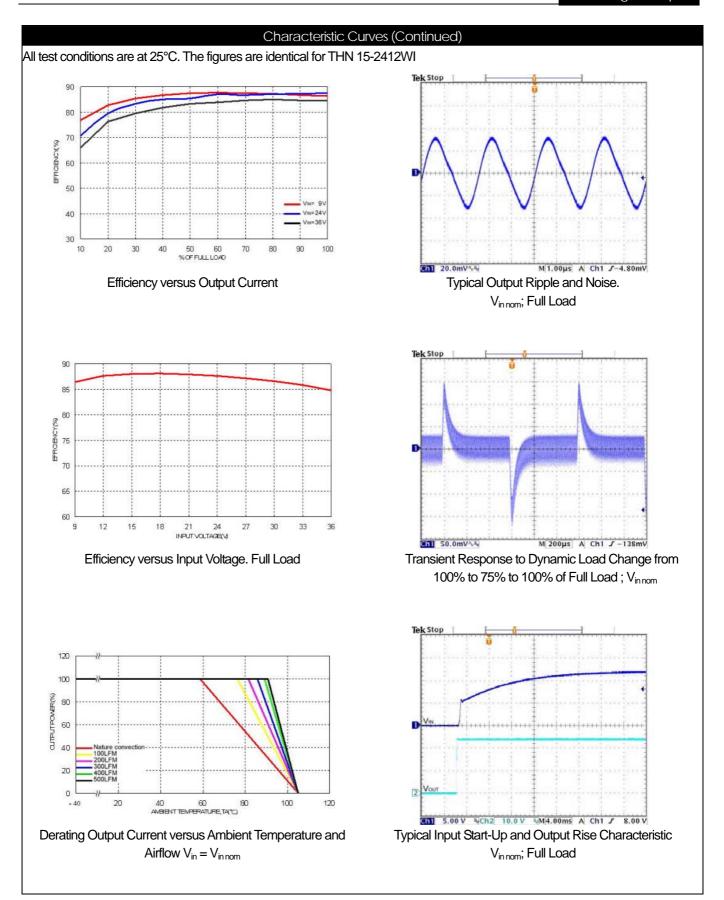
	General Specification				
Parameter	Model	Min	Тур	Max	Unit
Efficiency (See Page 21)	THN 15-2410WI		86		
$(V_{in nom}; Full Load; T_A = 25^{\circ}C)$	THN 15-2411WI		84		
	THN 15-2412WI		86		
	THN 15-2413WI		85		
	THN 15-2421WI		85		
	THN 15-2422WI		87		
	THN 15-2423WI		88		0/
	THN 15-4810WI		86		%
	THN 15-4811WI		86		
	THN 15-4812WI		87		
	THN 15-4813WI		87		
	THN 15-4821WI		85		
	THN 15-4822WI		86		
	THN 15-4823WI		87		
Isolation Voltage (Basic Insulation)					
Input to Output (1 minute)	All	1600			Vdc
Input/Output to Case (1 minute)		1000			
Isolation Resistance	All	1			GΩ
Isolation Capacitance	All			1000	pF
Switching Frequency	All		400		KHz
Weight	All		15		g
MTBF (See Page 31)					
Bellcore TR-NWT-000332, T _C = 40°C	All		1'330'000		hours
MIL-STD-217F			563'000		

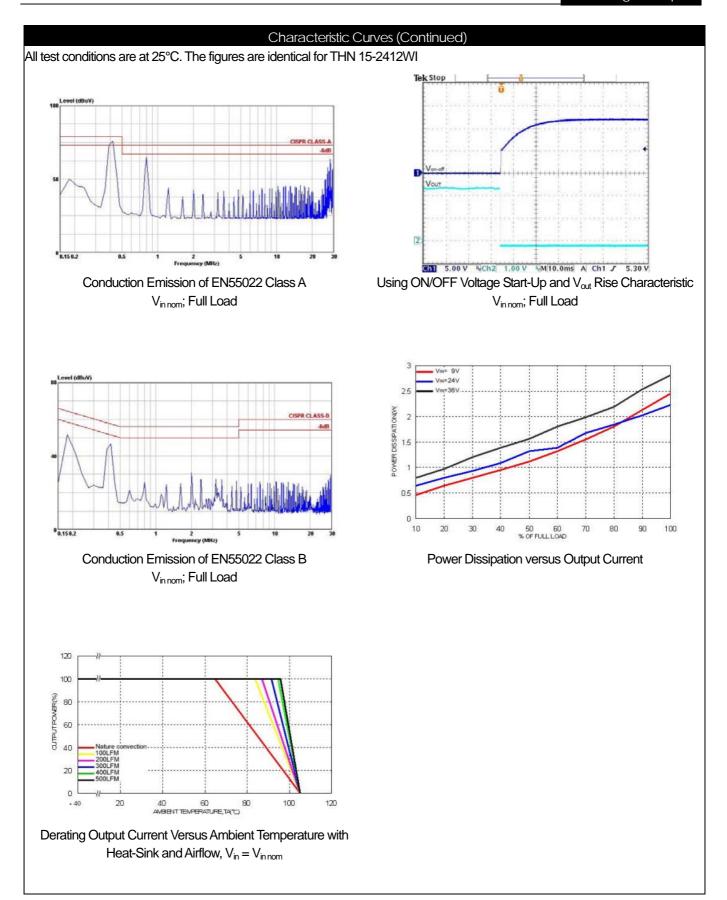


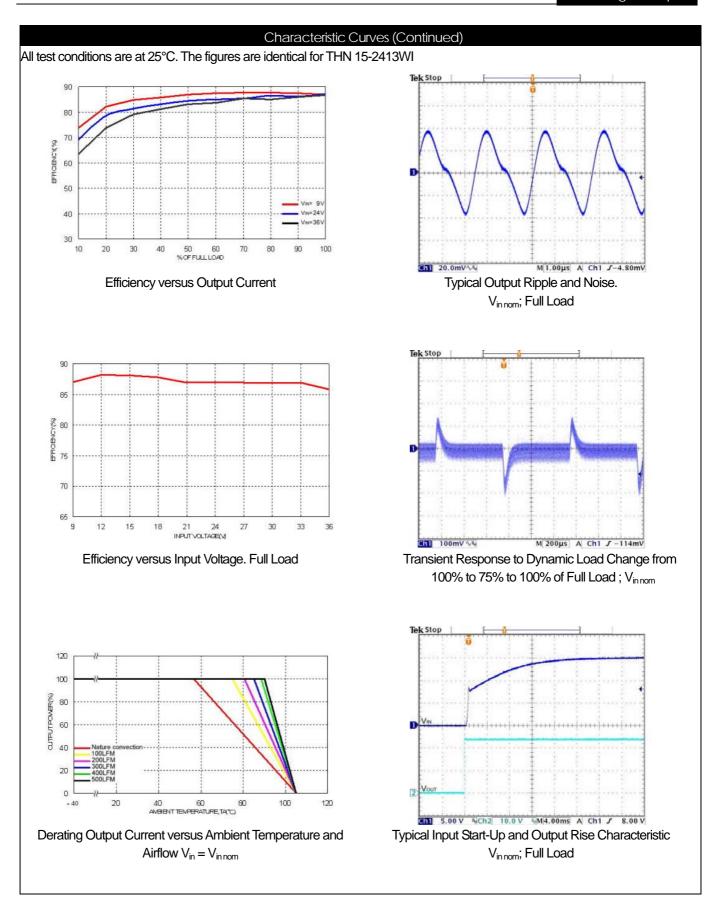


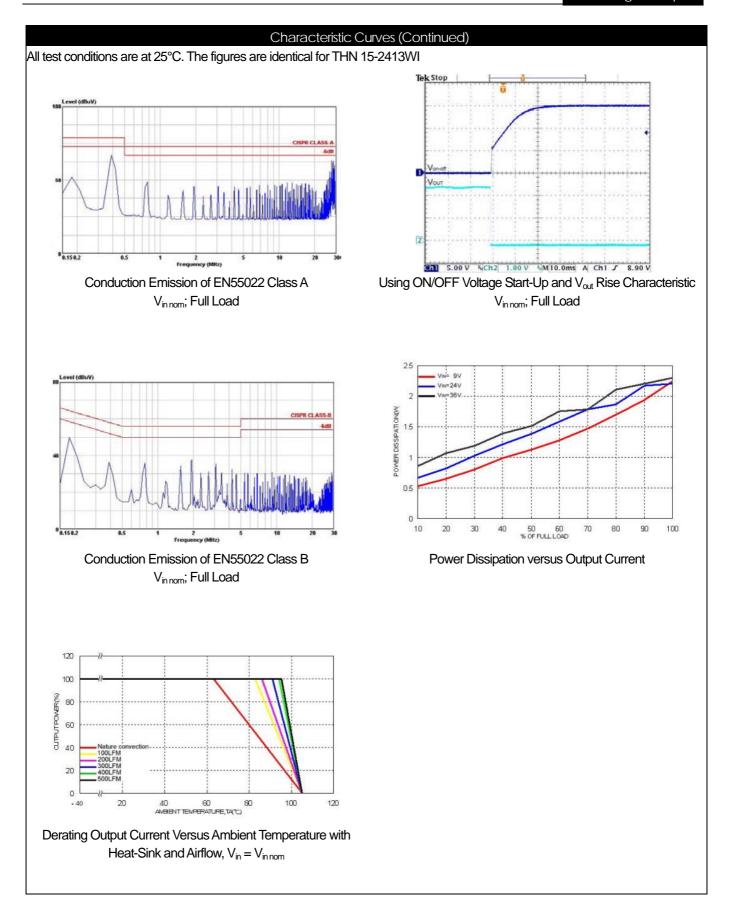


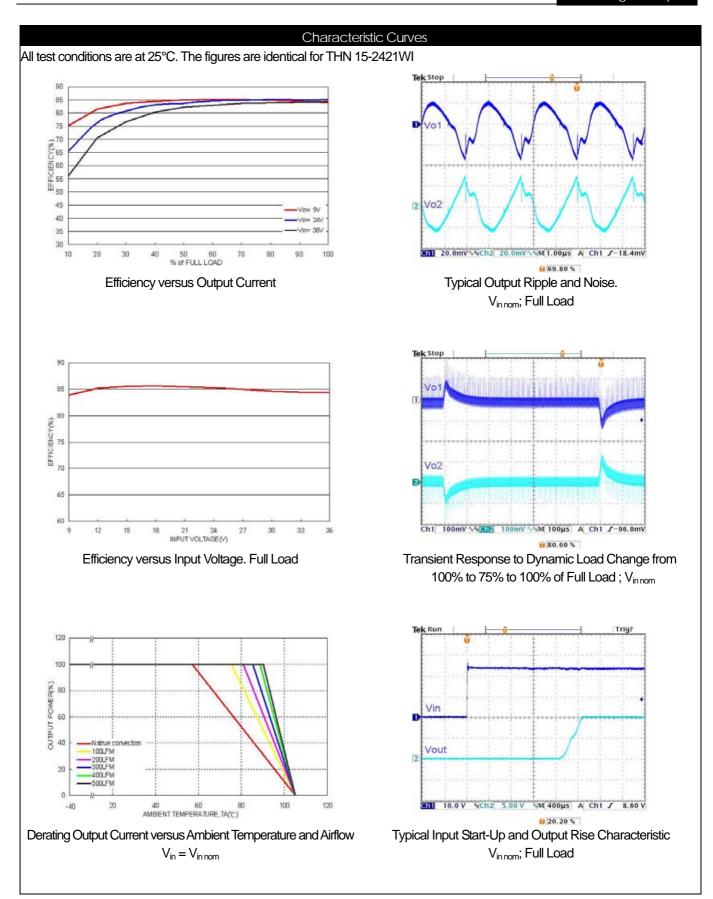


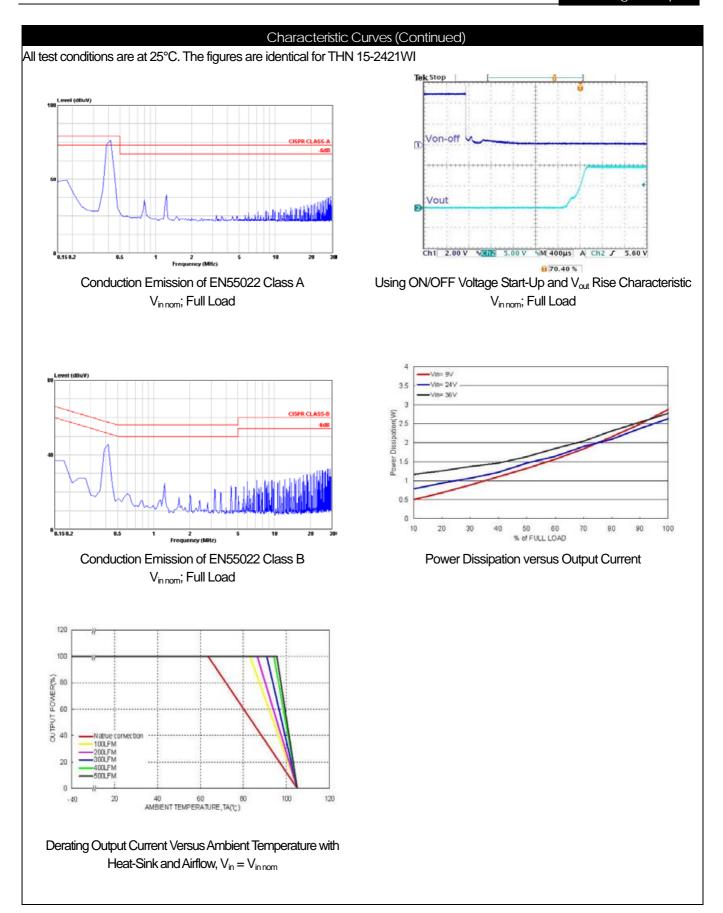


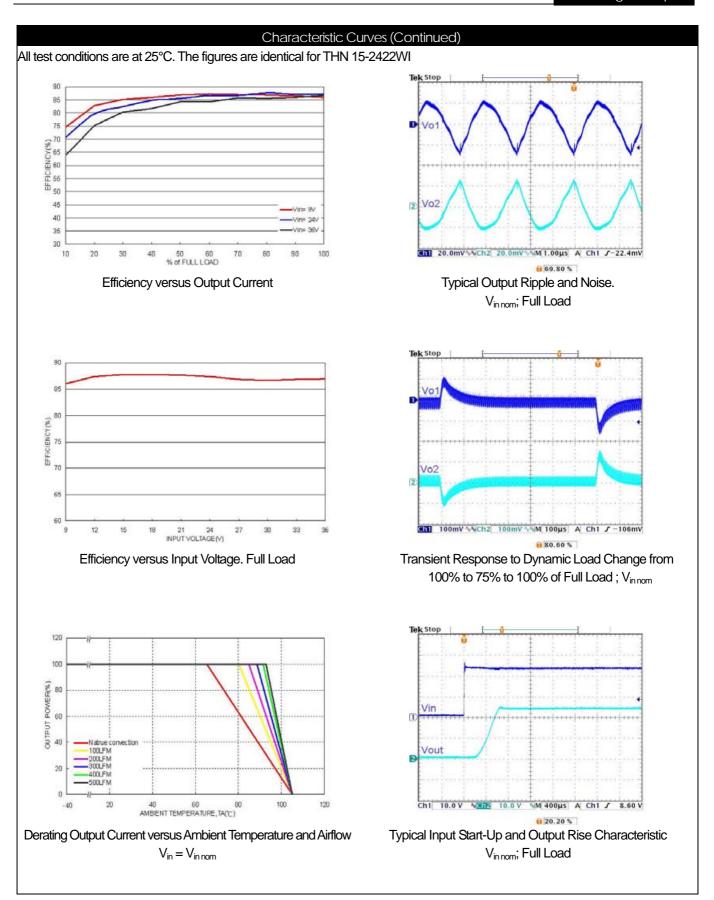


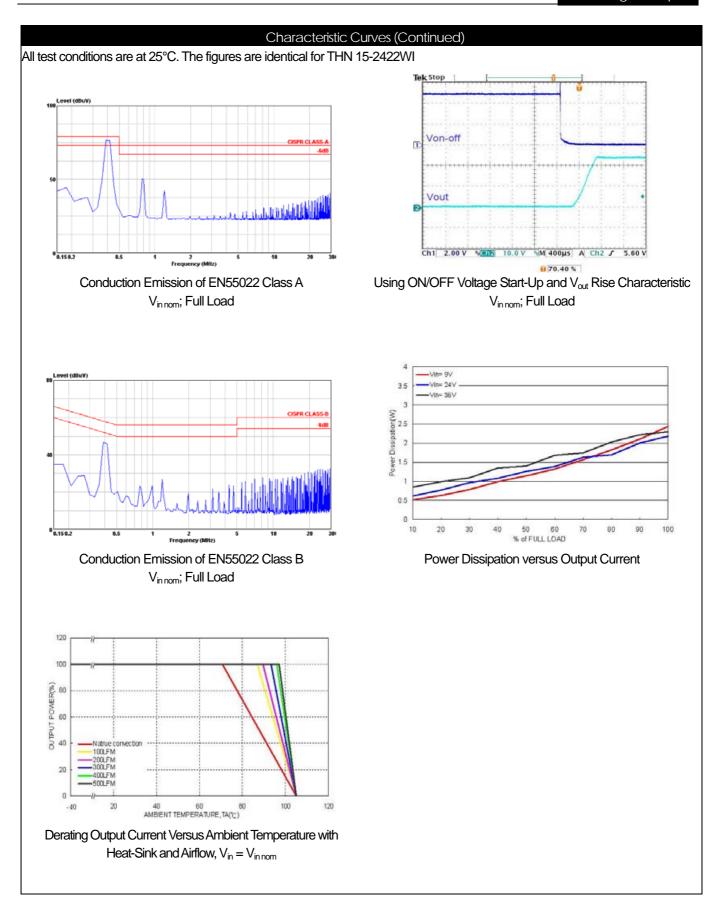


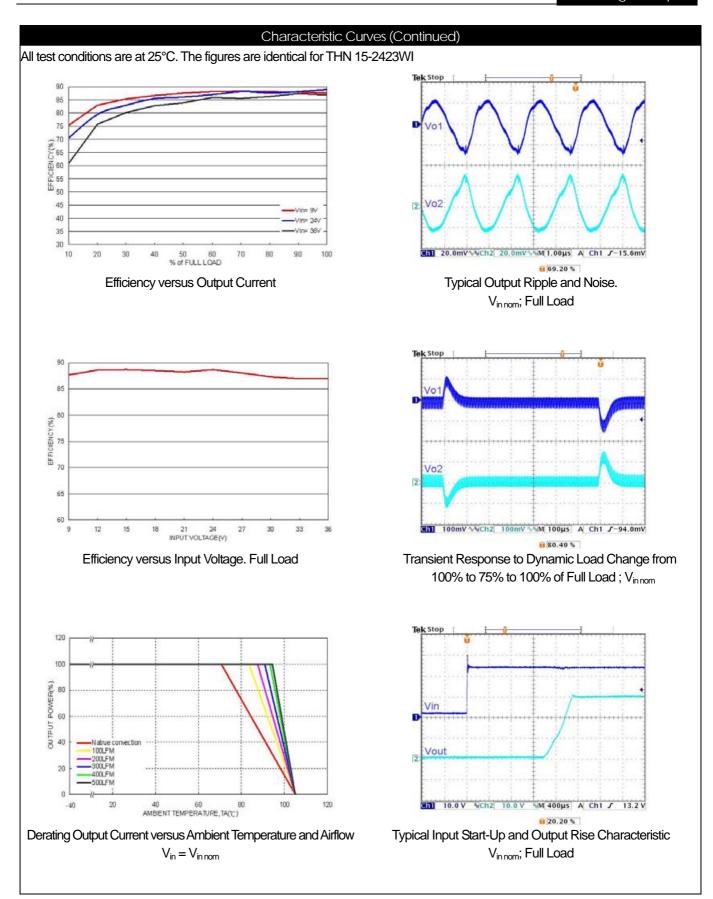


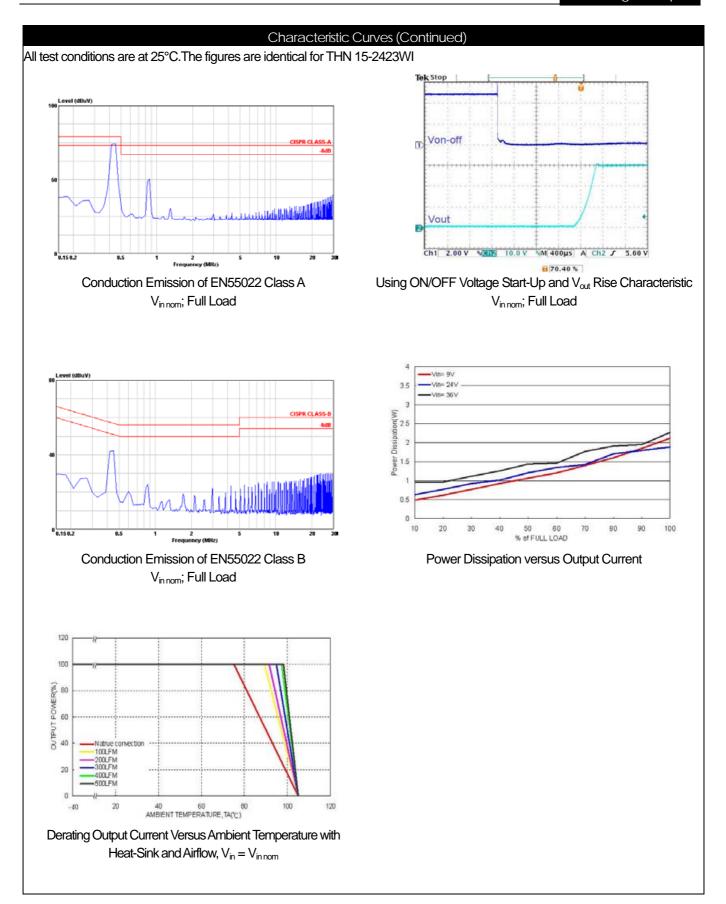


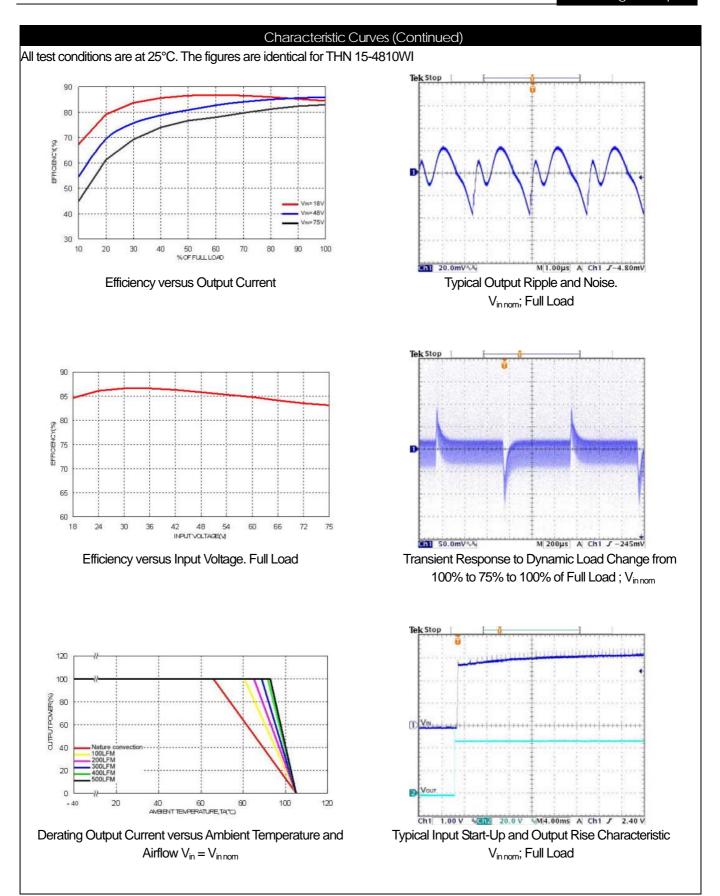


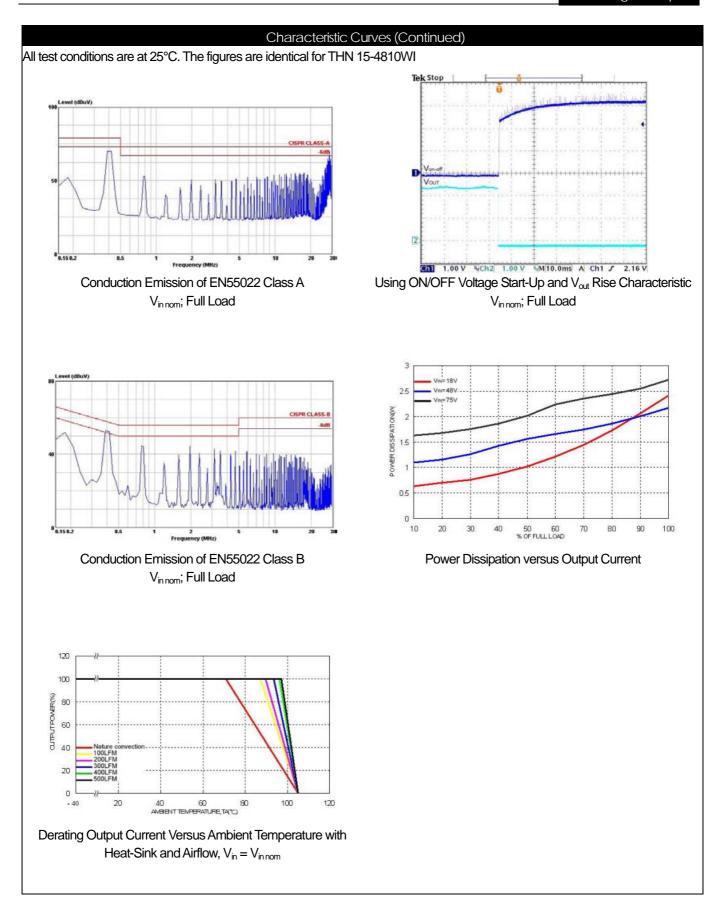


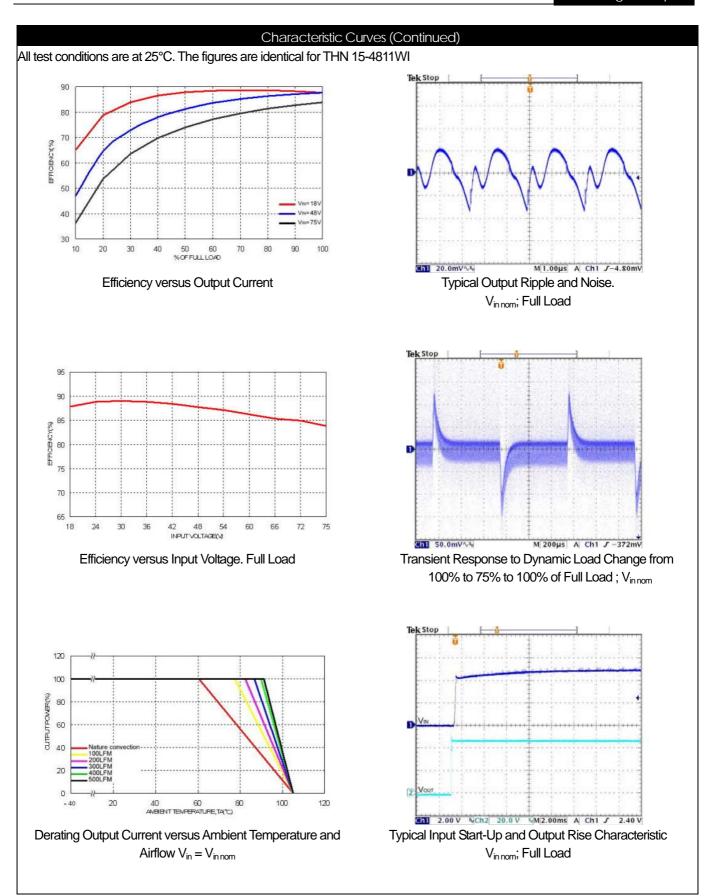


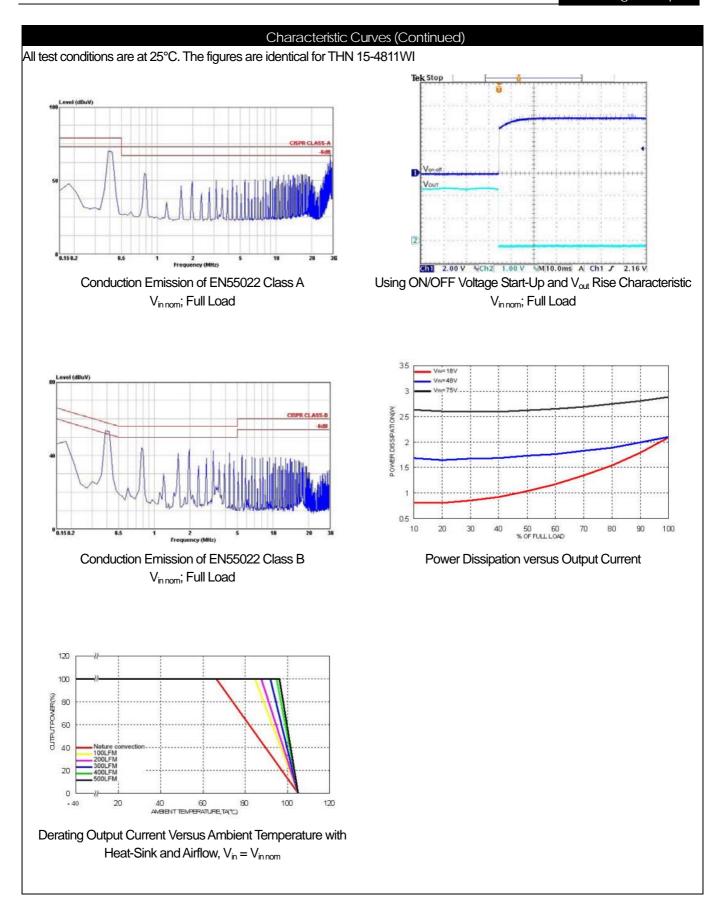


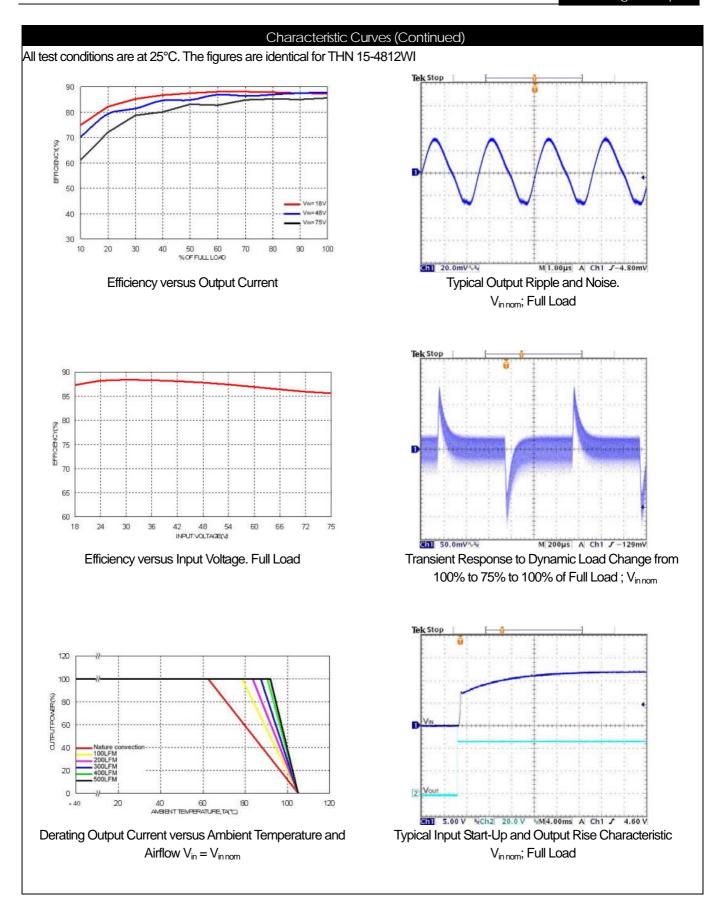


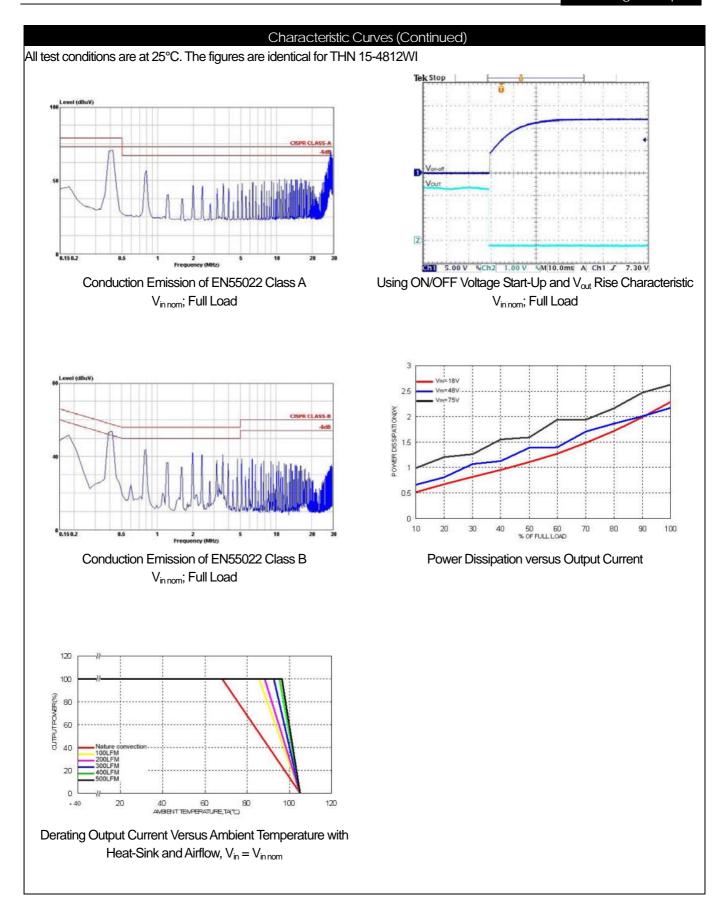


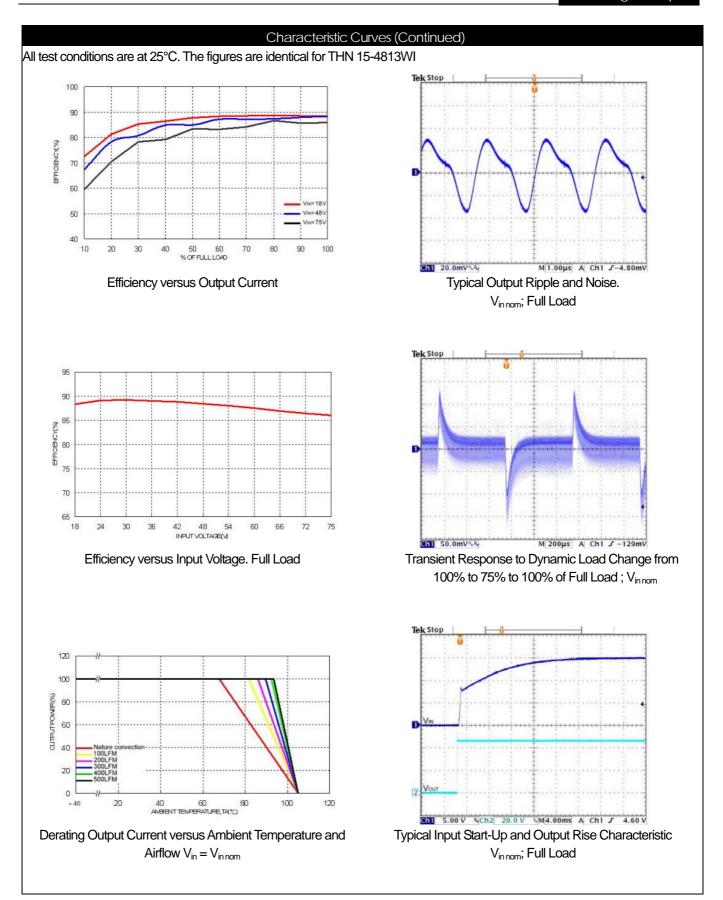


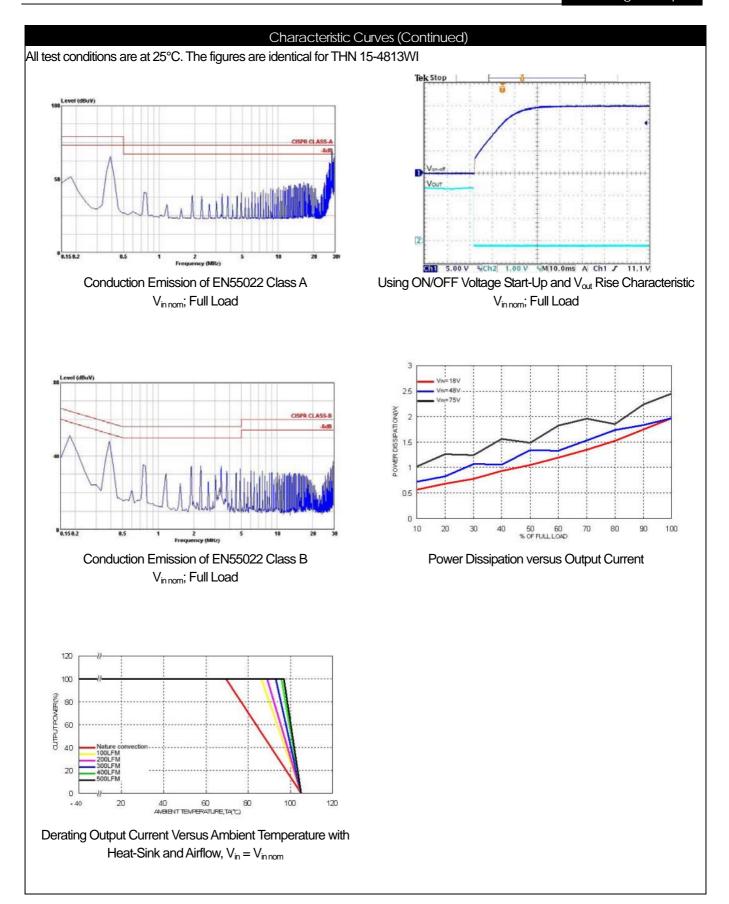


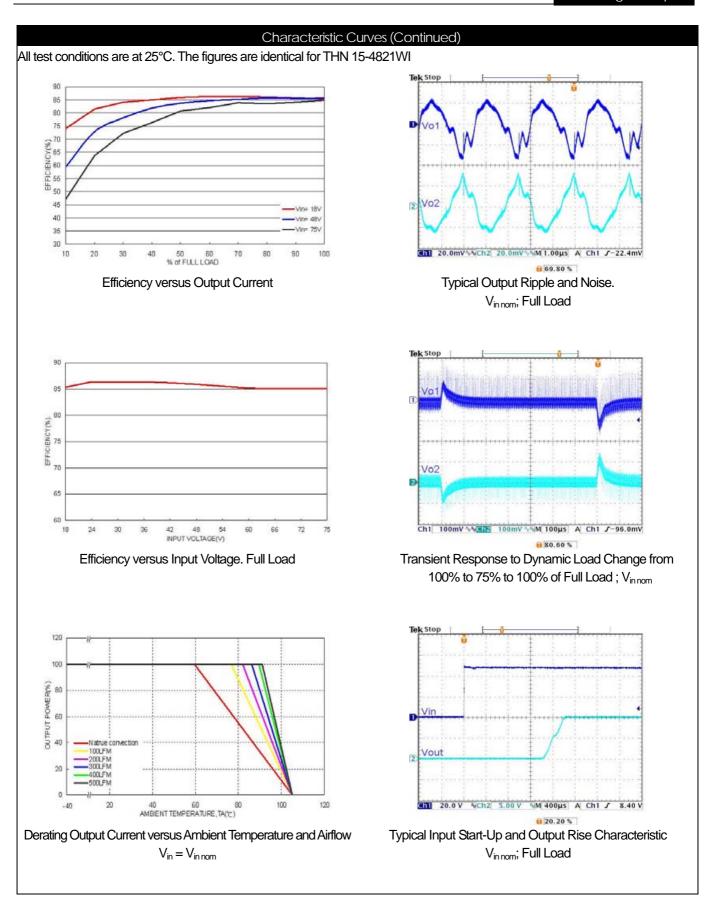


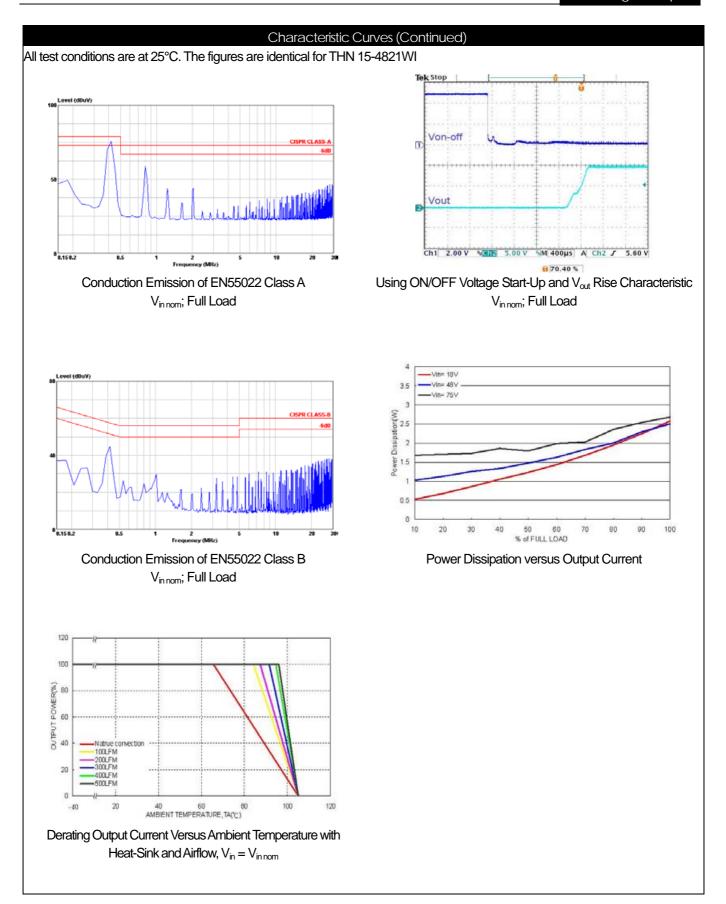


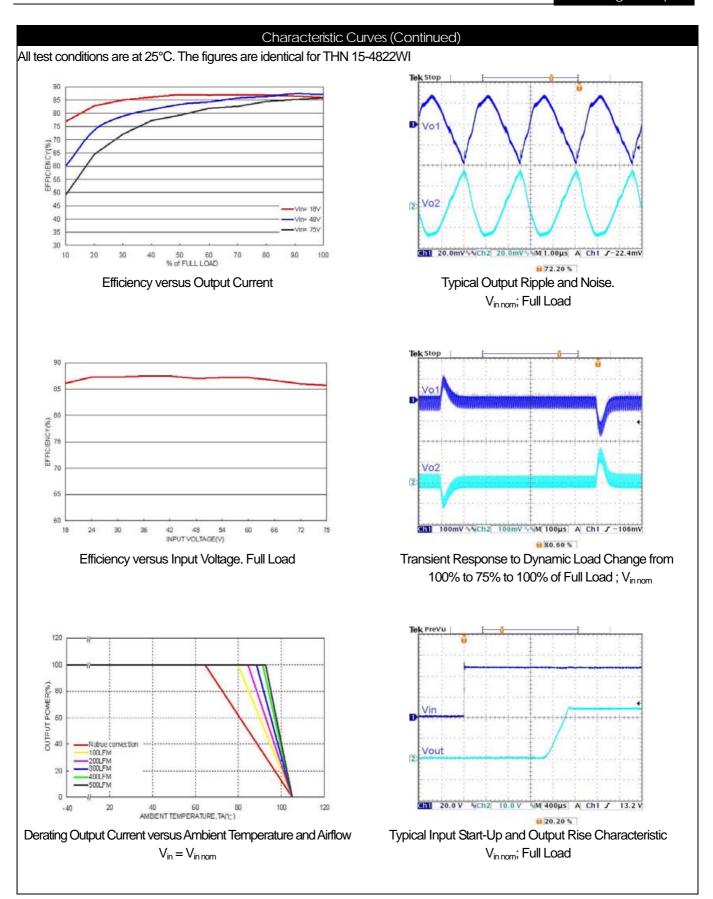


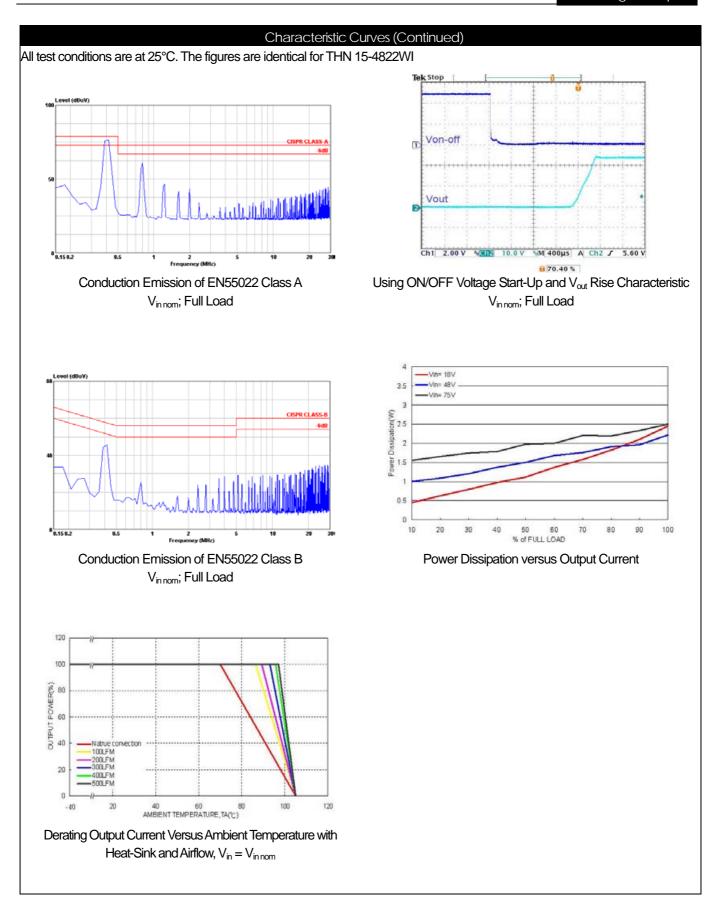


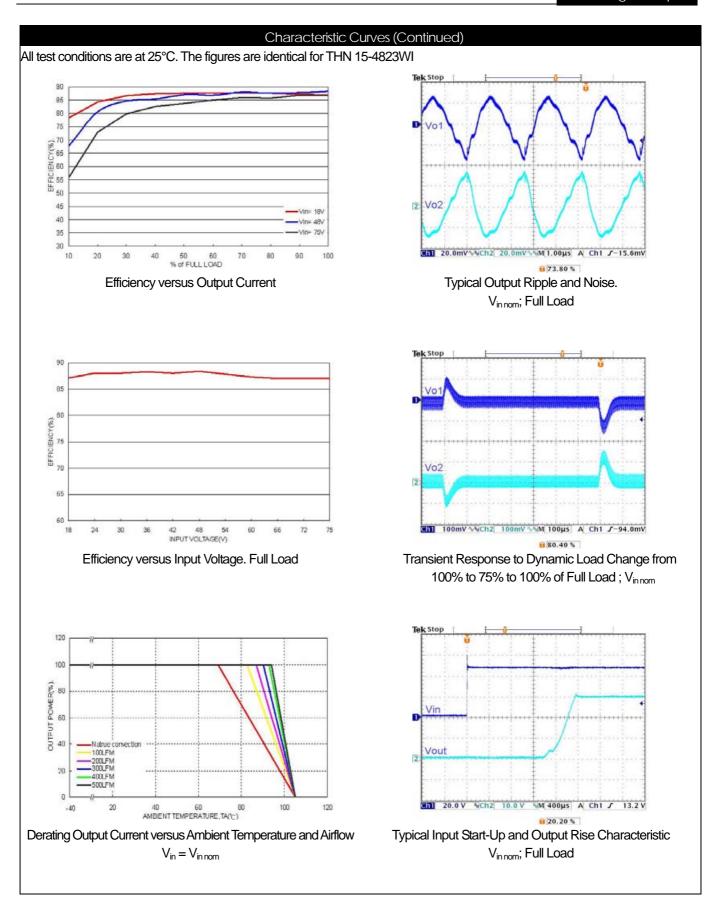


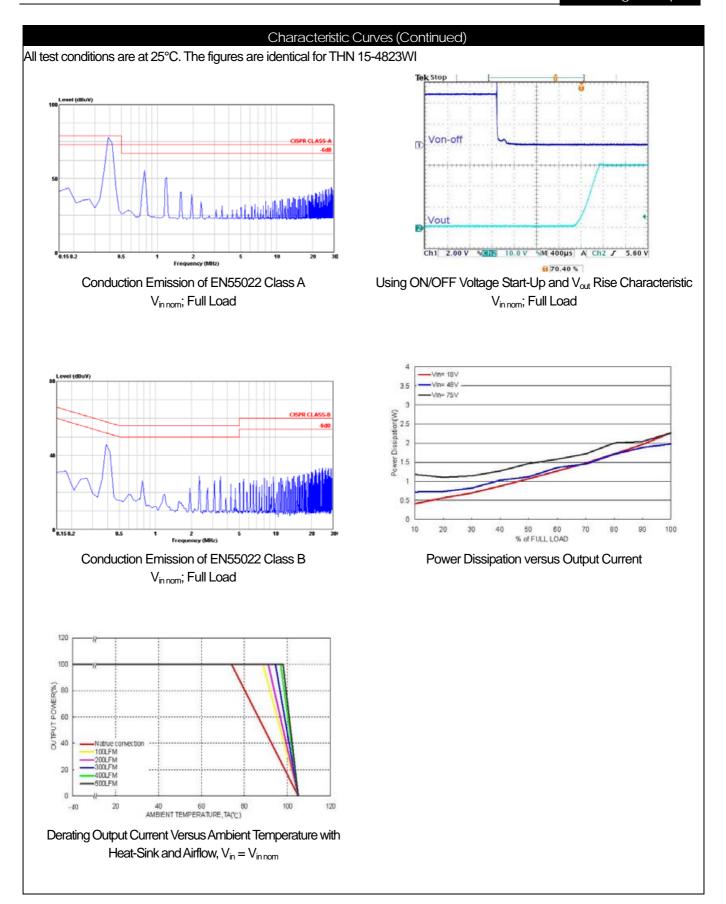








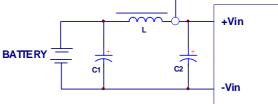




Testing Configurations

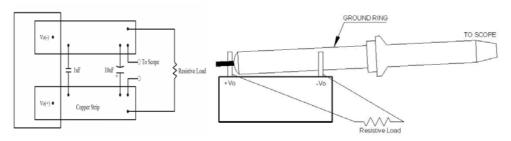
Input reflected-ripple current measurement test up

CURRENT PROBE MEASURE POINT

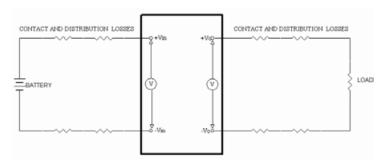


Component	Value	Voltage	Reference
L	12µH		
C1	10µF	100V	Aluminum Electrolytic Capacitor
C2	10µF	100V	Aluminum Electrolytic Capacitor

Peak-to-peak output ripple & noise measurement test up



Output voltage and efficiency measurement test up

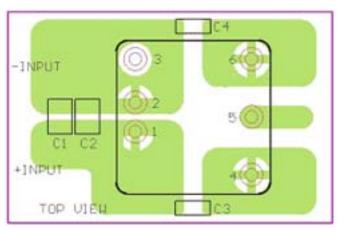


Note: All measurements are taken at the module terminals.

$$Efficiency = \left(\frac{V_o \times I_o}{V_{in} \times I_{in}}\right) \times 100\%$$

LINE O C1 C2 COM O -Vin Trim Load C3

Suggested schematic to comply with EN55022 conducted noise emission Class A



Recommended layout with input filter

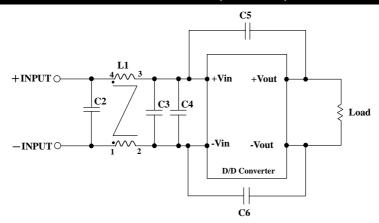
To comply with the conducted noise emissions according to EN55022 CLASS A following components are required:

THN 15-24xxWI

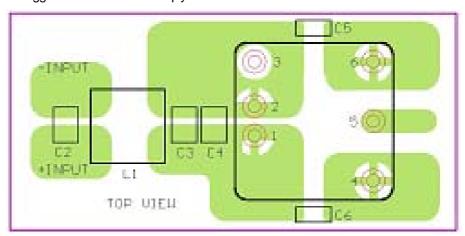
Component	Value	Voltage	Reference
C1	6.8µF	50V	1812 MLCC
C2	6.8µF	50V	1812 MLCC
C3, C4	470pF	2KV	1808 MLCC

THN 15-48xxWI

Component	Value	Voltage	Reference
C1	2.2µF	100V	1812 MLCC
C2	2.2µF	100V	1812 MLCC
C3, C4	470pF	3KV	1808 MLCC



Suggested schematic to comply with EN55022 conducted noise emission Class B



Recommended layout with input filter

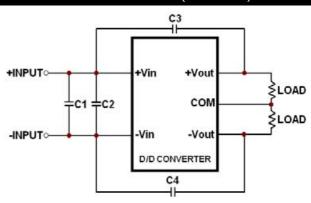
To comply with the conducted noise emissions according to EN55022 CLASS B following components are required:

THN 15-24xxWI

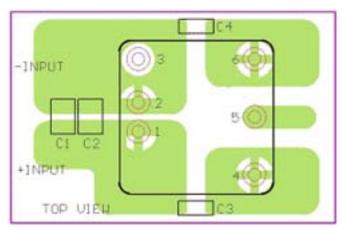
Component	Value	Voltage	Reference
C2, C3	6.8µF	50V	1812 MLCC
C4			
C5, C6	470pF	2KV	1808 MLCC
L1	325µH		Common Choke, P/N: TCK-050

THN 15-48xxWI

Component	Value	Voltage	Reference
C2, C3	2.2µF	100V	1812 MLCC
C4	2.2µF	100V	1812 MLCC
C5, C6	470pF	2KV	1808 MLCC
L1	620µH		Common Choke, P/N: TCK-019



Suggested schematic to comply with EN55022 conducted noise emission Class A



Recommended layout with input filter

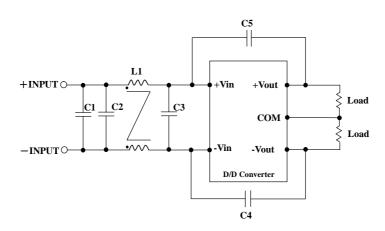
To comply with the conducted noise emissions according to EN55022 CLASS A following components are required:

THN 15-24xxWI

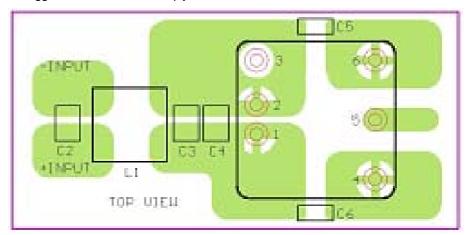
Component	Value	Voltage	Reference
C1	6.8µF	50V	1812 MLCC
C2	6.8µF	50V	1812 MLCC
C3, C4	470pF	3KV	1808 MLCC

THN 15-48xxWI

Component	Value	Voltage	Reference
C1	2.2µF	100V	1812 MLCC
C2	2.2µF	100V	1812 MLCC
C3, C4	470pF	3KV	1808 MLCC



Suggested schematic to comply with EN55022 conducted noise emission Class B



Recommended layout with input filter

To comply with the conducted noise emissions according to EN55022 CLASS B following components are required:

THN 15-24xxWI

Component	Value	Voltage	Reference
C1, C3	6.8µF	50V	1812 MLCC
C2			
C4, C5	470pF	3KV	1808 MLCC
L1	325µH		Common Choke, P/N: TCK-050

THN 15-48xxWI

Component	Value	Voltage	Reference
C1, C3	2.2µF	100V	1812 MLCC
C2	2.2µF	100V	1812 MLCC
C4, C5	1000pF	3KV	1808 MLCC
L1	325µH		Common Choke, P/N: TCK-050

This Common Choke L1 has been define as follow:

TCK-019

L: $620\mu H \pm 35\%$ / DCR: $80m\Omega$, max A height: 7.8 mm, Max

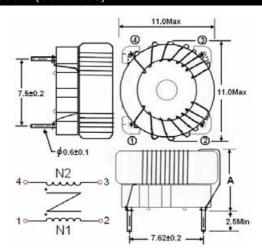
■ TCK-050

L: $325\mu H \pm 35\% / DCR$: 35Ω , max A height: 8.8 mm, Max

■ Test condition: 100KHz / 100mV

Recommended through hole: Φ0.8mm

All dimensions in millimeters



Input Source Impedance

The power module should be connected to a low impedance input source. Highly inductive source impedance can affect the stability of the power module. Input external C-L-C filter is recommended to minimize input reflected ripple current. The inductor is simulated source impedance of 12µH and capacitor is Nippon Chemi-con KZE series 10µF/100V&10µF/100V. The capacitor must be equipped as close as possible to the input terminals of the power module for lower impedance.

Output Over Current Protection

When excessive output currents occur in the system, circuit protection is required on all power supplies. Normally, overload current is maintained at approximately about 150 percent of rated current for THN 15-WI single output series.

Hiccup-mode is a method of operation in a power supply whose purpose is to protect the power supply from being damaged during an over-current fault condition. It also enables the power supply to restart when the fault is removed.

One of the problems resulting from over current is that excessive heat may be generated in power devices; especially MOSFET and Shottky diodes and the temperature of those devices may exceed their specified limits. A protection mechanism has to be used to prevent those power devices from being damaged.

Output Over Voltage Protection

The output over-voltage protection consists of a Zener diode that monitors the output voltage on the feedback loop. If the voltage on the output terminals exceeds the over-voltage protection threshold, then the Zener diode will send a current signal to the control IC to limiting the output voltage.

Output Voltage Adjustment

Output voltage set point adjustment allows the user to increase or decrease the output voltage set point of a module. This is accomplished by connecting an external resistor between the TRIM pin and either the $+V_{out}$ or $+V_{out}$ pins. With an external resistor between the TRIM and $+V_{out}$ pin, the output voltage set point increases. With an external resistor between the TRIM and $+V_{out}$ pin, the output voltage set point decreases.

Trim up equation

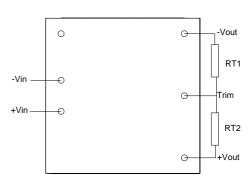
$$RT1 = \left[\frac{G \times L}{(Vo, up - L - K)} - H\right] \Omega$$

Trim down equation

$$RT2 = \left[\frac{(Vo, down - L) \times G}{(Vo - Vo, down)} - H\right]\Omega$$

Trim constants

Module	G	Н	K	L
THN 15-xx10WI	5110	2050	0.8	2.5
THN 15-xx11WI	5110	2050	2.5	2.5
THN 15-xx12WI	10000	5110	9.5	2.5
THN 15-xx13WI	10000	5110	12.5	2.5



TRIM TABLE THN 15-xx10WI

Trim up (%)	1	2	3	4	5	6	7	8	9	10
V _{OUT} (Volts) =	3.333	3.366	3.399	3.432	3.465	3.498	3.531	3.564	3.597	3.630
$R_{T1}(K\Omega) =$	385.071	191.511	126.990	94.730	75.374	62.470	53.253	46.340	40.963	36.662
Trim down (%)	1	2	3	4	5	6	7	8	9	10
Trim down (%) $V_{OUT} (Volts) =$	1 3.267	2 3.234	3 3.201	4 3.168	5 3.135	6 3.102	7 3.069	8 3.036	9 3.003	10 2.970

THN 15-xx11WI

Trim up (%)	1	2	3	4	5	6	7	8	9	10
V _{OUT} (Volts) =	5.050	5.100	5.150	5.200	5.250	5.300	5.350	5.400	5.450	5.500
$R_{T1}(K\Omega) =$	253.450	125.700	83.117	61.825	49.050	40.533	34.450	29.888	26.339	23.500
Trim down (0/)	4	_	_		_	•	_	0	9	10
Trim down (%)	. 1	2	3	4	5	6	/	8		
V_{OUT} (Volts) =	4.950	4.900	4.850	4.800	5 4.750	4.700	4.650	4.600	4.550	4.500

THN 15-xx12WI

Trim up (%)	1	2	3	4	5	6	7	8	9	10
V _{OUT} (Volts) =	12.120	12.240	12.360	12.480	12.600	12.720	12.840	12.960	13.080	13.200
$R_{T1}(K\Omega) =$	203.223	99.057	64.334	46.973	36.557	29.612	24.652	20.932	18.038	15.723
Trim down (%)	1	2	3	4	5	6	7	8	9	10
Trim down (%) $V_{OUT} (Volts) =$	1 11.880	2 11.760	3 11.640	4 11.520	5 11.400	6 11.280	7 11.160	8 11.040	9 10.920	10 10.800

THN 15-xx13WI

Trim up (%)	1	2	3	4	5	6	7	8	9	10
V _{OUT} (Volts) =	15.150	15.300	15.450	15.600	15.750	15.900	16.050	16.200	16.350	16.500
$R_{T1}(K\Omega) =$	161.557	78.223	50.446	36.557	28.223	22.668	18.700	15.723	13.409	11.557
Trim down (%)	1	2	3	4	5	6	7	8	9	10
Trim down (%) V _{OUT} (Volts) =		2 14.700	3 14.550	4 14.400	5 14.250	6 14.100	7 13.950	8 13.800	9 13.650	10 13.500

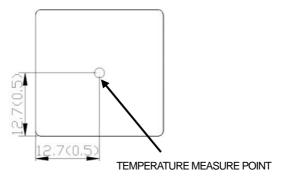
Short Circuitry Protection

Continuous, hiccup and auto-recovery mode.

During short circuit, converter still shut down. The average current during this condition will be very low and the device can be safety in this condition.

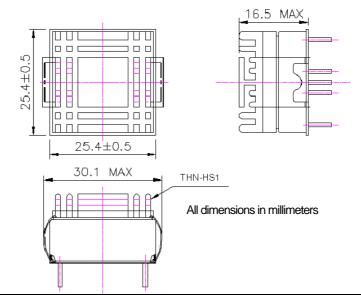
Thermal Consideration

The power module operates in a variety of thermal environments. However, sufficient cooling should be provided to help ensure reliable operation of the unit. Heat is removed by conduction, convection, and radiation to the surrounding Environment. Proper cooling can be verified by measuring the point as the figure below. The temperature at this location should not exceed 105°C. When Operating, adequate cooling must be provided to maintain the test point temperature at or below 105°C. Although the maximum point Temperature of the power modules is 105°C, you can limit this Temperature to a lower value for extremely high reliability.



Heat Sink Consideration

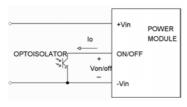
Equip heat-sink THN-HS1 for lower temperature and higher reliability of the module. Considering space and air-flow is the way to choose which heat-sink is needed.



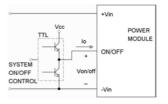
Remote ON/OFF Control

The Remote ON/OFF Pin is controlled DC/DC power module to turn on and off; the user must use a switch to control the logic voltage high or low level of the pin referenced to -V_{in}. The switch can be open collector transistor, FET and Photo-Couple. The switch must be capable of sinking up to 1 mA at low-level logic Voltage. High-level logic of the ON/OFF signal maximum voltage is allowable leakage current of the switch at 15V is 50µA.

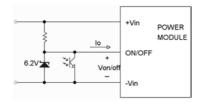
Remote ON/OFF Implementation Circuits



Isolated-Closure Remote ON/OFF



Level Control Using TTL Output



Level Control Using Line Voltage

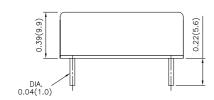
The Positive logic structure turned on of the DC/DC module when the ON/OFF pin is at high-level logic and low-level logic is turned off it.

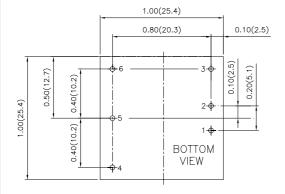


THN 15-WI module is turned off at Low-level logic

THN 15-WI module is turned on at High-level logic

Mechanical Data



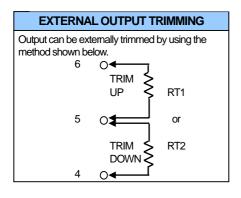


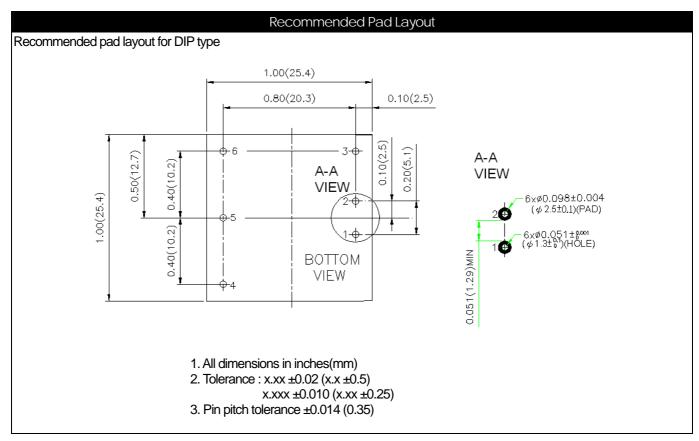
PIN (PIN CONNECTION							
PIN	Single Output	Dual Output						
1	+ INPUT	+ INPUT						
2	- INPUT	- INPUT						
3	ON/OFF	ON/OFF						
4	+VOUT	+VOUT						
5	TRIM	Com						
6	-VOUT	-VOUT						

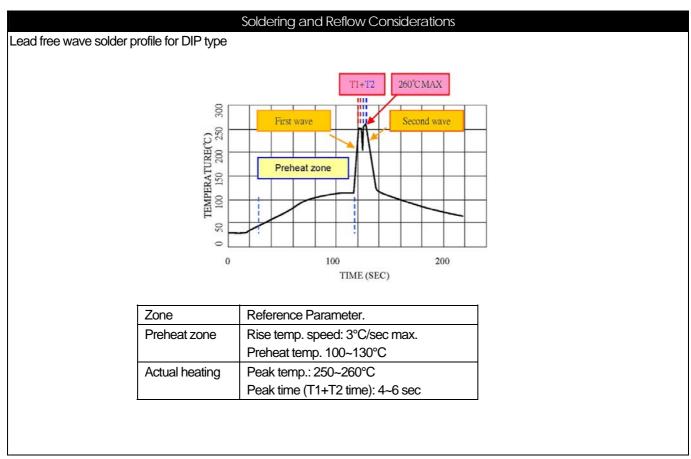
- 1. All dimensions in inches (mm)
- 2. Tolerance : x.xx ±0.02 (x.x ±0.5)

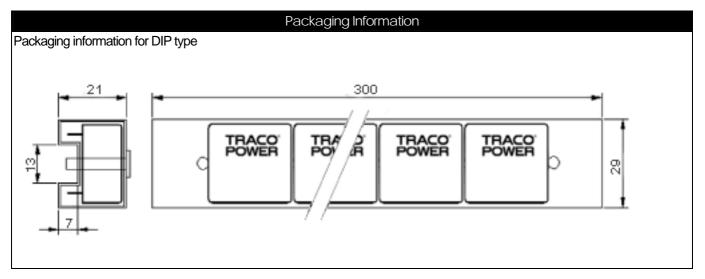
x.xxx ±0.010 (x.xx ±0.25)

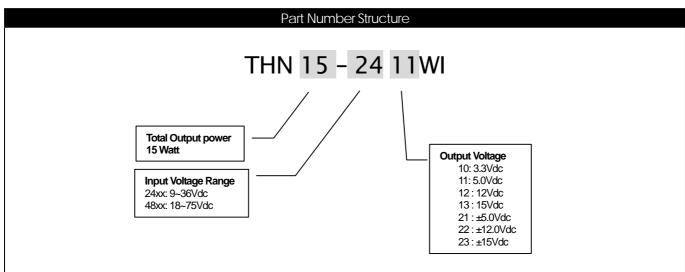
3. Pin pitch tolerance ± 0.014 (0.35)











Model	Input	Output	Output Current	Input Current	Efficiency (2)
Number	Range	Voltage	Full Load	Full Load (1)	(%)
THN 15-2410WI	9 - 36 VDC	3.3 VDC	4000mA	688mA	86
THN 15-2411WI	9 - 36 VDC	5.0 VDC	3000mA	782mA	84
THN 15-2412WI	9 - 36 VDC	12.0 VDC	1300mA	803mA	86
THN 15-2413WI	9 - 36 VDC	15.0 VDC	1000mA	772mA	85
THN 15-2421WI	9 - 36 VDC	±5.0 VDC	±1500mA	722mA	85
THN 15-2422WI	9 - 36 VDC	±12.0 VDC	±625mA	753mA	87
THN 15-2423WI	9 - 36 VDC	±15.0 VDC	±500mA	744mA	88
THN 15-4810WI	18 - 75 VDC	3.3 VDC	4000mA	336mA	86
THN 15-4811WI	18 - 75 VDC	5.0 VDC	3000mA	382mA	86
THN 15-4812WI	18 - 75 VDC	12.0 VDC	1300mA	392mA	87
THN 15-4813WI	18 - 75 VDC	15.0 VDC	1000mA	377mA	87
THN 15-4821WI	18 - 75 VDC	±5.0 VDC	±1500mA	386mA	85
THN 15-4822WI	18 - 75 VDC	±12.0 VDC	±625mA	382mA	86
THN 15-4823WI	18 - 75 VDC	±15.0 VDC	±500mA	377mA	87

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Note 1. Maximum value at nominal input voltage and full load.

Note 2. Typical value at nominal input voltage and full load.

Safety and Installation Instruction

Fusing Consideration

Caution: This power module is not internally fused. An input line fuse must always be used.

This encapsulated power module can be used in a wide variety of applications, ranging from simple stand-alone operation to an integrated part of sophisticated power architecture. To maximum flexibility, internal fusing is not included; however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a normal-blow fuse with maximum rating of 3A for THN 15-24xxWI modules and 1.5A for THN 15-48xxWI modules. Based on the information provided in this data sheet on Inrush energy and maximum DC input current; the same type of fuse with lower rating can be used. Refer to the fuse manufacturer's data for further information.

MTBF and Reliability

The MTBF of THN 15-WI SERIES of DC/DC converters has been calculated using

Bellcore TR-NWT-000332 Case I: 50% stress, Operating Temperature at 40°C (Ground fixed and controlled environment). The resulting figure for MTBF is 1'330'000 hours.

MIL-HDBK 217F NOTICE2 FULL LOAD, Operating Temperature at 25°C. The resulting figure for MTBF is 563'000 hours.