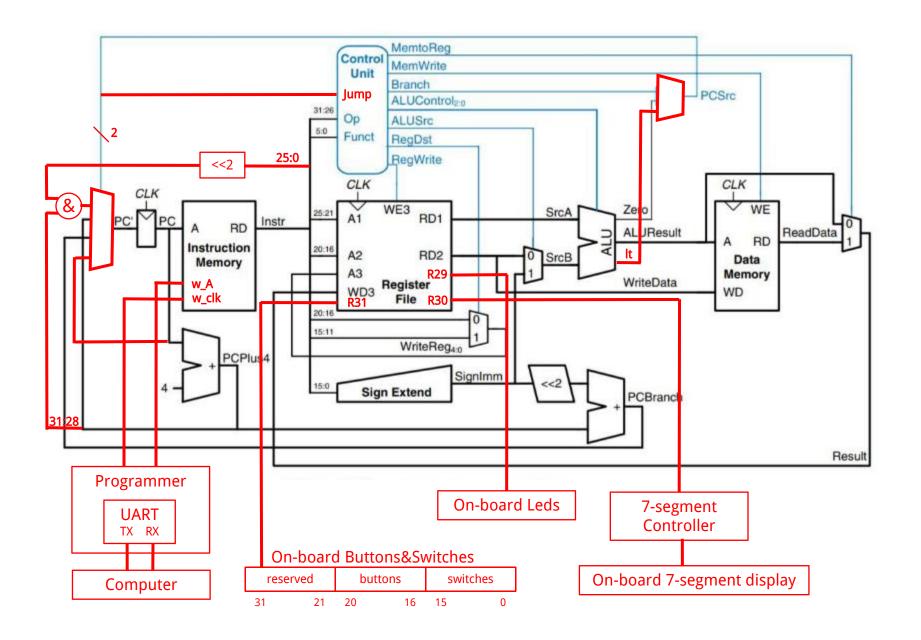
RC5 Assembly to Binary

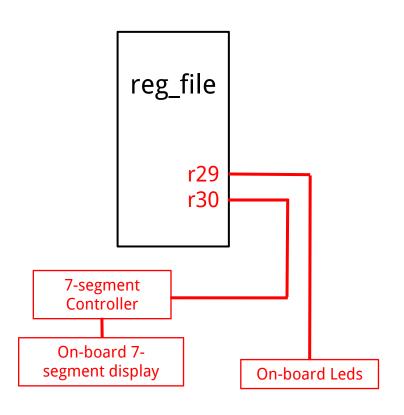
```
NOR r10 r10 r0
                          # r10 <- A^B = (B nand (A nar
                                                             decode = {
                                                                          {'type': 'R', 'op': 0x00, 'func': 0x10},
351
      OR r11 r10 r10
                                                                  'ADD':
                                                                  'ADDI':
352
                                                                          {'type': 'I',
                                                                                        'op': 0x01},
                                                                  'SUB':
                                                                                        'op': 0x00, 'func': 0x11},
353
      ORI r6 r0 0
                                                                  'SUBI':
                                                                           'type': 'I',
                                                                                        'op': 0x02},
354
      AND r16 r26 r8
                                                                  'AND':
                                                                                        'op': 0x00, 'func': 0x12},
355
      BEQ r0 r0 2
                                                                          {'type': 'I',
                                                                  'ANDI':
                                                                                        'op': 0x03},
356
      SHL r11 r11 1
                                                                  'OR':
                                                                                        'op': 0x00, 'func': 0x13},
357
      ADDI r6 r6 1
                                                                  'NOR':
                                                                                        'op': 0x00, 'func': 0x14},
                                                                  'ORI':
358
      BLT r16 r6 -3
                         # loop if r6 < rotation bits
                                                                           'type': 'I',
                                                                                        'op': 0x04},
                                                                  'SHL':
359
                                                                                        'op': 0x05}.
                                                                  'SHR':
                                                                           type':
                                                                                        'op': 0x06},
360
      ORI r16 r0 32
                                                                  'LW':
                                                                                        'op': 0x07},
361
      BEQ r0 r0 2
                                                                  'SW':
                                                                           'type': 'I',
                                                                                        'op': 0x08},
362
      SHR r10 r10 1
                                                                  'BLT':
                                                                                        'op': 0x09},
363
      ADDI r6 r6 1
                                                                  'BEQ':
                                                                                        'op': 0x0a},
364
      BLT r16 r6 -3
                                                                          {'type':
                                                                                        'op': 0x0b},
                                                                  'BNE':
                                                                           'type': 'J',
365
                                                                  'JMP':
                                                                                        'op': 0x0c},
                                                                  'HAL':
                                                                           type': 'J',
                                                                                        'op': 0x3f}, }
      OR r10 r11 r10
```

Part of RC5 assembly (rotate operation)

Decoding table



Leds and 7-segment display



Example code:

```
# Turn on led(7)

# r0 = 0, r1 = 1

SHL r28, r1, 7 # r28(7) = 1

OR r29, r29, r28

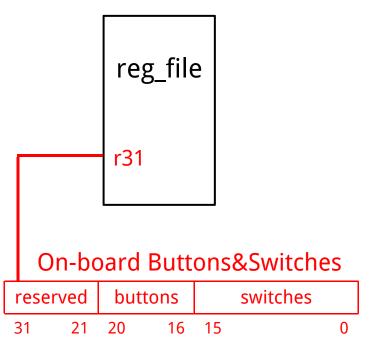
# display data[30]

LW r30 r0 30
```

VHD pseudocode:

```
led <= r29(15 downto 0);
U_7seg_ctrl(r30, seg, an);
```

Buttons & Switches



VHD pseudocode: r31(20 downto 0) <= btn&sw;</pre>

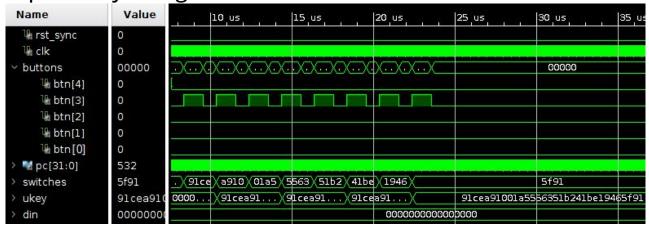
Example code:

```
# Wait for btn0
# r22 = 1<<16 for picking up r31(16)
LOOP:
AND r28, r22, r31  # pick r31(16)
BNE r28, r22, LOOP
# Do something here...</pre>
```

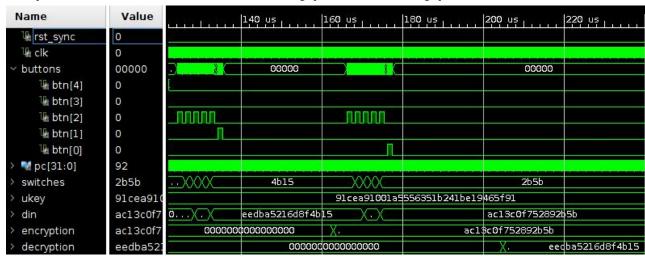
- If btn0 is not pressed, r31 = '0'. BNE becomes True. The code will keep looping.
- If btn0 is pressed, r31 = '1'. BNE becomes False. The code will continue.

Buttons & Switches

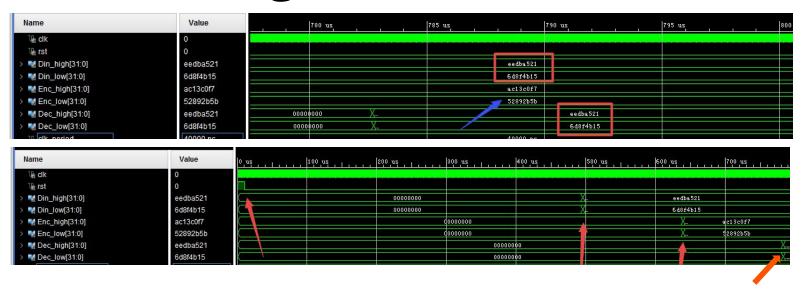
Input Ukey using btn3:



Input Din (btn3); start encryption/decryption (btn1/btn0)



RC5 Timing



Critial path delay: 38 ns Clock frequency: 25 MHz

Latency (cycles used in key-gen, encryption and decryption):

	Start	End	Time-Diff	Latency	clk period
Key-Gen	10460	495860	485400	12135	40
Encryption	495860	639260	143400	3585	
Decryption	639260	782700	143440	3586	