```
oid SystemClock_Config(void);
tatic void MX_GPIO_Init(void);
nt main(void)
```

```
HAL_Init();
SystemClock_Config();
MX_GPIO_Init();
while (1)
       HAL_GPIO_WritePin(SOUND_GPIO_Port, SOUND_Pin, 1);
       HAL Delay(1000);
       HAL_GPIO_WritePin(SOUND_GPIO_Port, SOUND_Pin, 0);
       HAL_Delay(1000);
void SystemClock_Config(void)
RCC_OscInitTypeDef RCC OscInitStruct = {0};
RCC_ClkInitTypeDef RCC_ClkInitStruct = {0};
 _HAL_RCC_PWR_CLK_ENABLE();
 HAL PWR VOLTAGESCALING CONFIG(PWR REGULATOR VOLTAGE SCALE2);
RCC_OscInitStruct.OscillatorType = RCC_OSCILLATORTYPE_HSI;
RCC OscInitStruct.HSIState = RCC HSI ON;
RCC OscInitStruct.HSICalibrationValue = RCC HSICALIBRATION DEFAULT;
RCC_OscInitStruct.PLL.PLLState = RCC_PLL_ON;
RCC OscInitStruct.PLL.PLLSource = RCC PLLSOURCE HSI;
RCC_OscInitStruct.PLL.PLLM = 8;
RCC OscInitStruct.PLL.PLLN = 64;
RCC OscInitStruct.PLL.PLLP = RCC PLLP DIV2;
RCC OscInitStruct.PLL.PLLQ = 4;
if (HAL_RCC_OscConfig(&RCC_OscInitStruct) != HAL_OK)
 Error_Handler();
```

```
RCC ClkInitStruct.ClockType = RCC CLOCKTYPE HCLK|RCC CLOCKTYPE SYSCLK
              |RCC_CLOCKTYPE_PCLK1|RCC_CLOCKTYPE_PCLK2;
RCC_ClkInitStruct.SYSCLKSource = RCC_SYSCLKSOURCE_PLLCLK;
RCC_ClkInitStruct.AHBCLKDivider = RCC_SYSCLK_DIV4;
RCC ClkInitStruct.APB1CLKDivider = RCC HCLK DIV2;
RCC ClkInitStruct.APB2CLKDivider = RCC HCLK DIV1;
if (HAL_RCC_ClockConfig(&RCC_ClkInitStruct, FLASH_LATENCY_0) != HAL_OK)
Error_Handler();
tatic void MX_GPIO_Init(void)
GPIO_InitTypeDef GPIO_InitStruct = {0};
HAL RCC GPIOA CLK ENABLE();
__HAL_RCC_GPIOC_CLK_ENABLE();
HAL_GPIO_WritePin(GPIOC, LED_Pin|SOUND_Pin, GPIO_PIN_RESET);
GPIO_InitStruct.Pin = LED_Pin|SOUND_Pin;
GPIO InitStruct.Mode = GPIO MODE OUTPUT PP:
GPIO InitStruct.Pull = GPIO NOPULL;
GPIO_InitStruct.Speed = GPIO_SPEED_FREQ_LOW;
HAL_GPIO_Init(GPIOC, &GPIO_InitStruct);
void Error_Handler(void)
 _disable_irq();
while (1)
```