

# **PARTC: 8 BIT SUBTRACTION**

### **PROGRAM:**

MVI C,00H

LXIH,0042H

MOV A,M

INX H

MOV B,M

SUB B

**JNCLOOP** 

INR C

CMA

**INRA** 

LOOP:STA 0047H MOV A,C

MOV A,C STA 0048H

0x0042	05 <u>/</u>	4 MVT C 00H
		1 MVI C,00H
0x0043	0E <u>//</u>	2 LXI H,0042H
		3 MOV A,M
0x0044	00 <u>0</u>	4 INX H
0.0045	00 0	5 MOV B,M
0x0045	00 <u>/</u>	6 SUB B
0,,0046	00 4	7 JNC LOOP
0x0046	00 <u>0</u>	8 INR C
0,,0047	00 4	
0x0047	09 <u>/</u>	9 CMA
0,,0040	01 /	10 INR A
0x0048	01 <u>/</u>	11 LOOP:
0x0049	00 <u>/</u>	12 STA 0047H
00049	<u> </u>	13 MOV A,C
0x004A	00 <u>0</u>	14 STA 0048H
0X004A	00 <u>v</u>	
0x004B	00 <u>0</u>	15 HLT
0A004B	<u>v</u>	16
0x004C	00 <u>0</u>	
0X004C	<u> </u>	



## **EXPERIMENT NO:2**

# AIM: PART A:WRITE AN 8085 ASSEMBLY LANGUAGE TO PERFORM MULTIPLICATION OF TWO 8 BIT NOS.

#### **PROGRAM:**

MVI D, 00H MVI A, 00H LXI H,4150H MOV B,M INX H MOV C,M

LOOP:

ADD B JNC NEXT INR D

NEXT: DCR C

JNZ LOOP STA 4152H MOV A,D STA 4153H

HLT

00035	00 4	<b>A</b>
0x003E	00 <u>/</u>	1 MVI D,00H
0x003F	00 <u>0</u>	2 MVI A,00H
	_	3 LXI H, 0042H
0x0040	00 <u>/</u>	4 MOV B,M
0x0041	00 1	5 INX H
0X0041	00 <u>/</u>	6 MOV C,M
0x0042	05 <u>/</u>	7 LOOP:
		8 ADD B 9 JNC NEXT
0x0043	03 <u>//</u>	9 JNC NEXT 10 INR D
0x0044	00 <u>/</u>	11 NEXT:
0,0011	<u> </u>	12 DCR C
0x0045	0F <u>/</u>	13 JNZ LOOP
0,0046	00 4	14 STA 0045H
0x0046	00 <u>/</u>	15 MOV A,D
0x0047	00 <u>0</u>	16 STA 0046H
		17 HLT
0x0048	00 <u>0</u>	18

### **OBSERVATION:**

INPUT: 05(0042)

03(0043)

OUTPUT:0F(0045)

Erollment no. - 2303031050280



COMALAB -303105211 CSE Semester-III Parul Institute of Engineering &Technology

Erollment no. - 2303031050280