Practical 5
LNGANG002 & BTJMAL001

ADD

Test bench output:

A	B	ALU_Sel	ALU_Out	Operation
00010110	00010111	0000	00101101	Add

Waveform:
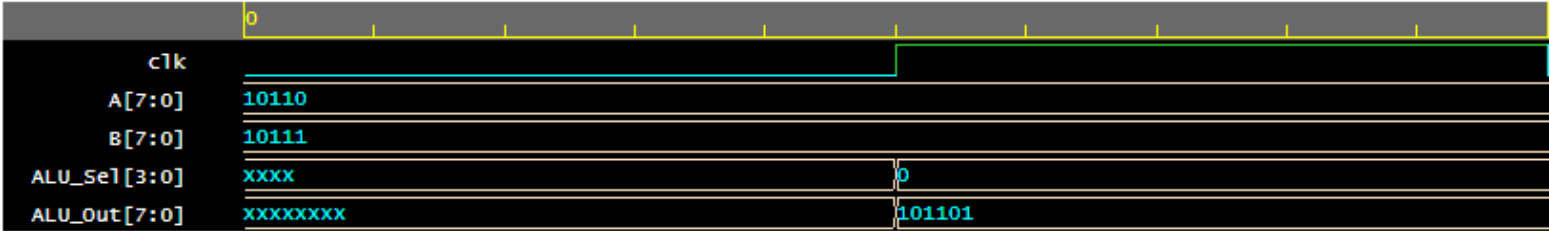
MAC

Test bench output:

A	B	ALU_Sel	ALU_Out	Operation
00010110	00010111	xxxx	00010110	Set Out as A
00010110	00010111	0110	00010000	MAC

Waveform:
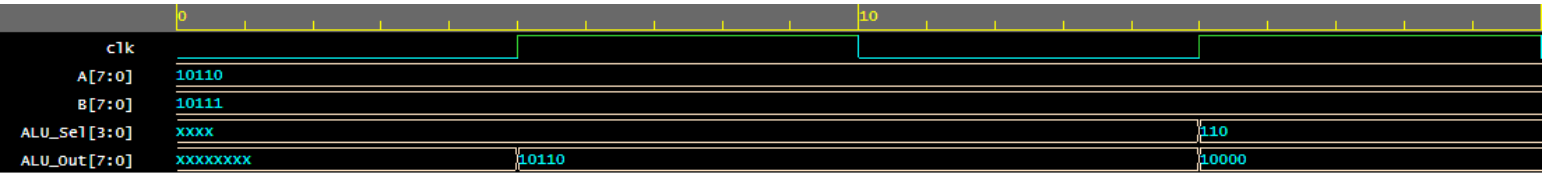
ROR

Test bench output:

A	B	ALU_Sel	ALU_Out	Operation
00010110	00010111	1000	00001011	Rotate Right

Waveform:

clk	
A[7:0]	10110
B[7:0]	10111
ALU_Sel[3:0]	xxxx 1000
ALU_Out[7:0]	xxxxxxxx 1011
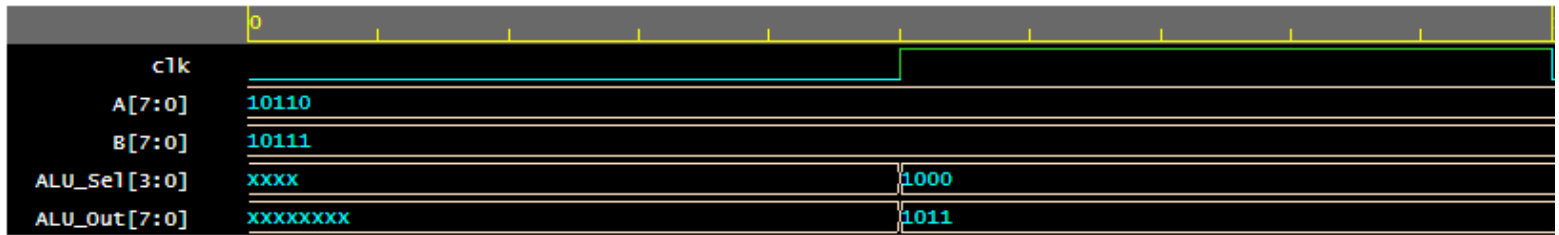
LTH

Test bench output:

A	B	ALU_Sel	ALU_Out	Operation
00010110	00010111	1111	11111111	Less Than

Waveform:

clk	
A[7:0]	10110
B[7:0]	10111
ALU_Sel[3:0]	xxxx 1111
ALU_Out[7:0]	xxxxxxxx 11111111