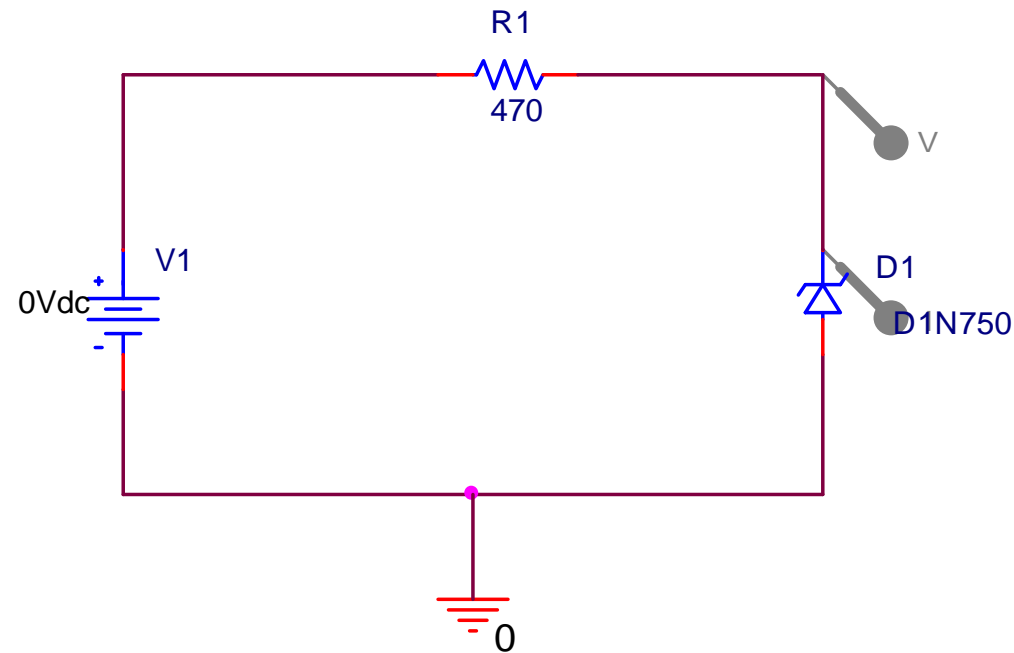

Lab. 3

Zener Diodes & Voltage Bias Control



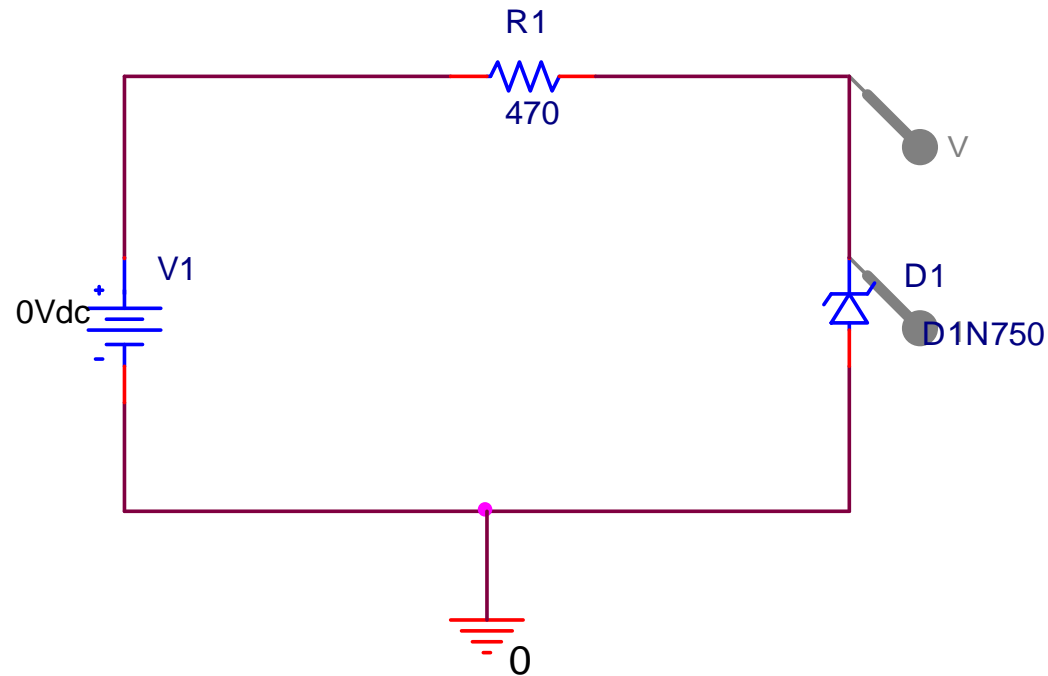
Zener Diode V-I Characteristics

- ✓ Reverse-bias mode, pp.83, Fig. 4-9



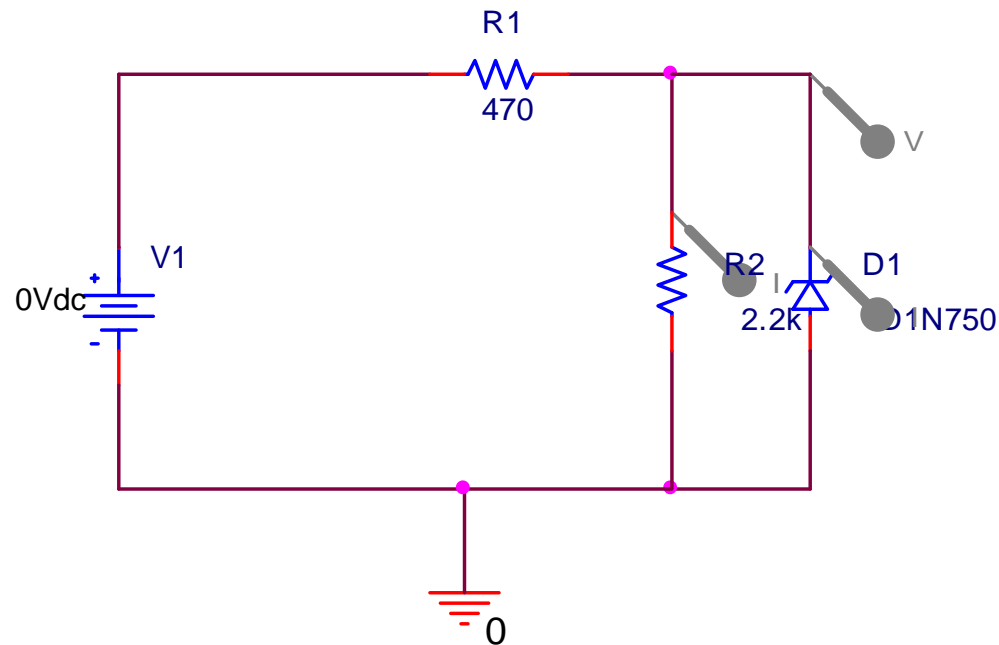
Zener Diode V-I Characteristics

- ✓ Forward-bias mode, pp. 83, Fig. 4-9



Zener-diode voltage control

✓ V_s (pp. 84, Fig. 4-10)



Contents of Lab. Report

- ✓ Team No.: PID: Name:
- ✓ Introduction
- ✓ Zener Diodes Characteristics & Voltage-Control Results
- ✓ Discussion & Conclusion



Pre-Report (Lab. 4)

- ✓ PSPICE simulations of a BJT(Bipolar Junction Transistor) test circuit!
- ✓ Submit the schematic diagrams and the simulation results.
- ✓ No need of introduction and discussion in pre-report.

