Lecture Plan

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Syllabus(2016-1st semester)

Course	Basic Circuit Laboratory II	Department	Electronics Engineering	Office Hours	화 3-5시, 목 2-4시
Course No. and Class	36095-02	Hours	3.0	Academic Credit	2.0
Professor	PARK SUNG MIN		Office		
Telephone			E-MAIL		
Value of competence			Keyword		

1. Course Description

This course mandates attendees to utilize discrete semiconductor devices such as diodes, BJTs, and MOSFETs,
to design amplifiers and Op-amps, and to conduct various labs for testing.
Also, computer simulations using PSPICE should be performed in each lab.
to understand the allocated micro-electronic circuits and furthermore to be capable of conducting projects.

2. Prerequisites

Basic Circuit Lab. I

3. Course Format

Lecture	Discussion/Presentation	Experiment/Practicum	Field Study	Other
10 %	0 %	90 %	0 %	0 %

- explanation of course format :

- Circuit description by utilizing PPT presentation
- Labs and Project

4. Course Objectives

This course mandates attendees to utilize discrete semiconductor devices such as diodes, BJTs, and MOSFETs,
to design amplifiers and Op-amps, and to conduct various labs for testing.
Also, computer simulations using PSPICE should be performed in each lab.
to understand the allocated micro-electronic circuits and furthermore to be capable of conducting projects.

5. Evaluation System

Midterm Exam	Final Exam	Quizzes	Presentation	Projects	Assignment	Participation	Other
0 %	25 %	0 %	0 %	20 %	50 %	0 %	5 %

* Evaluation of group projects may include peer evaluations.

- explain of evaluation system

- Assignments represent pre-reports & lab. reports.
- Other (attendance) 5%

6. Required Materials

Lecture notes (PDF files up-loaded in cyber-campus)

7. Supplementary Materials

전자회로실험 (김동식, 생능출판사)

8. Optional Additional Readings

Sedra & Smith, 'Microelectronics Circuits', 6th Ed, Oxford

9. Course Contents

Week	Date	Topics, Materials, Assignements
Week 1	2016/03/04(FRI)	PSPICE simulations, Team formation (lab. & project)
Week 2	2016/03/11(FRI)	Diodes & Rectifiers
Week 3	2016/03/18(FRI)	Zener Diodes & Bias Control Circuits
Week 4	2016/03/25(FRI)	Lab. (BJTs)
Week 5	2016/04/01(FRI)	Common-Emitter Amplifiers
Week 6	2016/04/08(FRI)	Common-Collector Amplifiers
Week 7	2016/04/15(FRI)	Lab. (MOSFETs)
Week 8	2016/04/22(FRI)	Project Interim Presentation
Week 9	2016/04/29(FRI)	Common-Source Amplifiers
Week 10	2016/05/06(FRI)	Lab. (Frequency Response)
Week 11	2016/05/13(FRI)	Power Amplifiers
Week 12	2016/05/20(FRI)	Op-amps
Week 13	2016/05/27(FRI)	Adder & Integrators
Week 14	2016/06/03(FRI)	Final Exam. (Jun. 4, 2:00-3:30pm)
Week 15	2016/06/10(FRI)	Project Final Presentation

10. Course Policies

* For laboratory courses, all students are required to complete lab safety training.
* Late report submission results in 30% off (only 70% grading applied)

* Project interim presentation in 8th week
* Project final presentation in 15th week
* Final exam: Jun. 4 (Sat.) 2:00-3:30pm

11. Special Accommodations

* According to the University regulation #57, students with disabilities can request special accommodation related to attendance, lectures, assignments, and/or tests by contacting the course professor at the beginning of semester. Based on the nature of the students' requests, students can receive support for such accommodations from the course professor and/or from the Support Center for Students with Disabilities (SCSD).

*** The contents of this syllabus are not final—they may be updated.**