Term Report Description

- This Term Report is about (1) Your own summary and review of the lectures and laboratories studied throughout the semester and (2) the design of your own embedded system.
- This assignment can be performed by a pair or individually. The group for this term report was created in CANVAS.
- You can adjust the group allocation by April 21. For those of you who would like to adjust the group allocation, you need to use the "CANVAS message" (Note, CANVAS message only/No email) to contact Dr. Hur directly.
- Like laboratory sections throughout the semester, groups **must** be formed within the same laboratory section. To be clear, some students would choose to work individually, and some students would end up working individually as well.
- After this date, the group will be finalized, and the group allocation cannot be changed. From April 22, the "Term Project Submission" assignment item will be released. You can submit your work through this assignment item.
- The report **must** include the following sections and content:
    - *Abstract, Introduction, Lectures/Labs, Embedded system, Summary and Conclusion, References*
- You must follow the IEEE conference Proceeding template.
    - Full paper template link: https://www.ieee.org/conferences/publishing/templates.html
    - A modified template was posted on CANVAS.
- The *Embedded System* section must include a **system block diagram.** The system block diagram must show the parts and systems that were used and the physical connections. The *Proposed Embedded System* is a conceptual design assignment. You do not need to physically build a device or system. Your proposed and designed embedded system must meet the following requirements.
    - Use at least **one** MSP430FR5994 microcontroller or MSP430FR5994 Launchpad board.
    - Use at least **two** from the following serial communications: UART, SPI, I2C.
- You can choose the level of the details of the connections in the system block diagram. In order to earn better scores, the block diagram should demonstrate properly low level and enough details of the connections. In order words, only high-level connections with just a few blocks or sloppy work of the block diagram will not be enough to earn full credit.
- The use of the required components and communications must be shown clearly in the system block diagram.
- Students should be careful not to use too many figures or big figures simply to increase the length of the paper. It may cause the deduction of the points.
- The minimum number of the references/citations in your reference section is **three**. The citatior format must be IEEE format:
    - http://journals.ieeeauthorcenter.ieee.org/wp-content/uploads/sites/7/IEEE_Reference_Guide.pdf
- The minimum page number of the report is **two**. Do not include an extra cover page. Failure to meet the minimum page number will result in a deduction.
- A recommended maximum page number is three.

- **Turnitin is enabled on this assignment**. Turnitin promotes academic integrity, streamlines grading and feedback, deters plagiarism, and improves student outcomes. Prevent plagiarism by identifying unoriginal content and manage potential academic misconduct by highlighting similarities among existing content.
- Submit your report file in a DOC file format.
- Report points: 20 points
- Due date: April 28, 2025
- Evaluation Criteria:
    - (4 points) Organization/Appearance/Format
    - (4 points) Technical soundness/Functionality
    - (4 points) Style/Grammar/Punctuation/Expression
    - (4 points) System Block diagram
    - (4 points) References/Citations