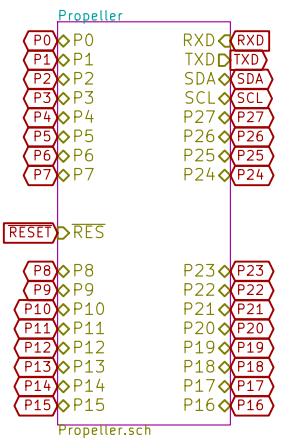
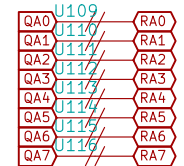
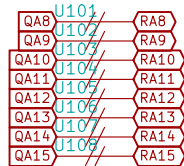
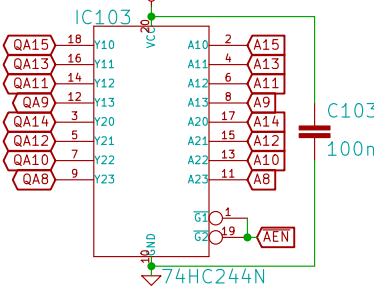
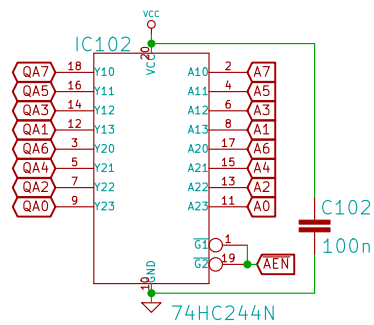


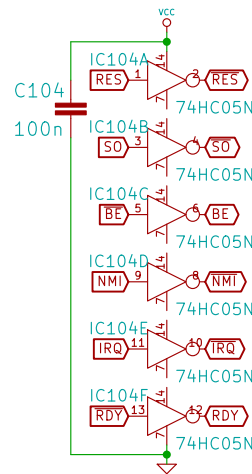
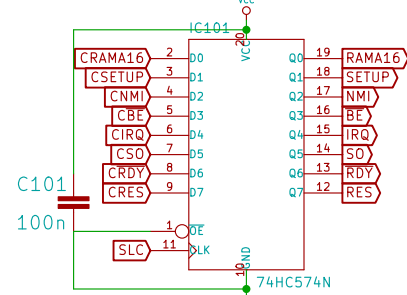
PCB jumpers, cut for 5V operation



While CLK0 is low (while the 6502 isn't using the data bus), two octal tri-state buffers transfer the address bus to P0..P15 when AEN is low.



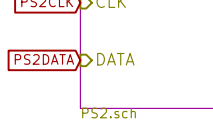
Octal D flipflop for signals
Sets the signals on the 6502 when SLC is clocked



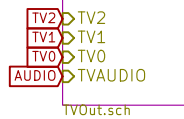
Power Supply



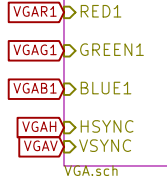
PS2



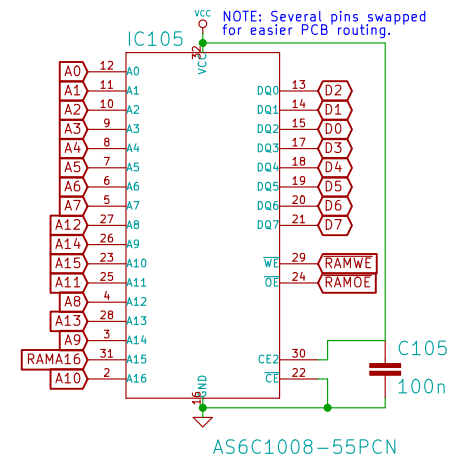
TV Out



VGA

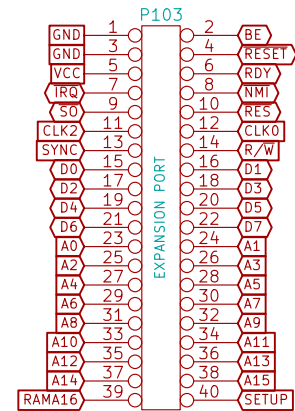
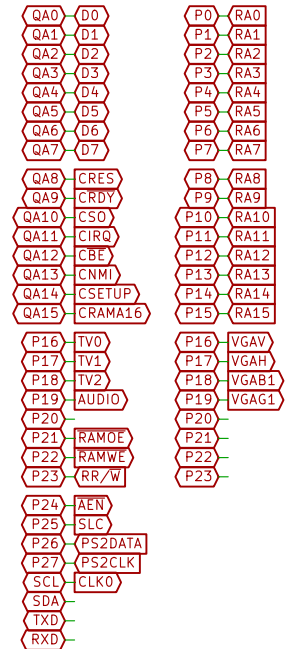


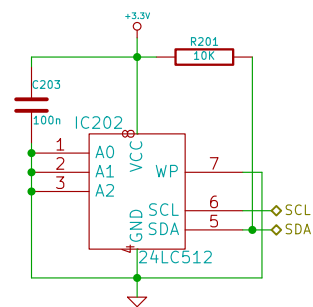
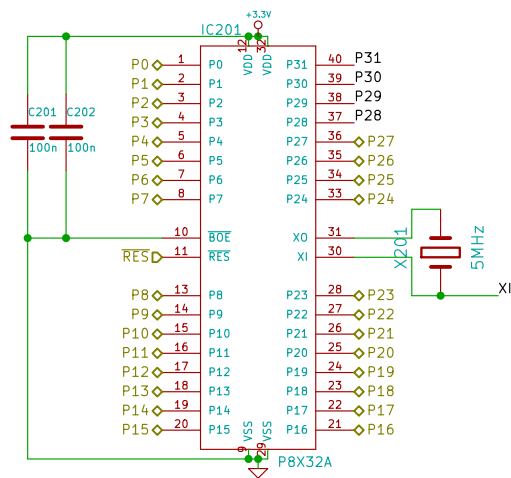
128KB Static RAM



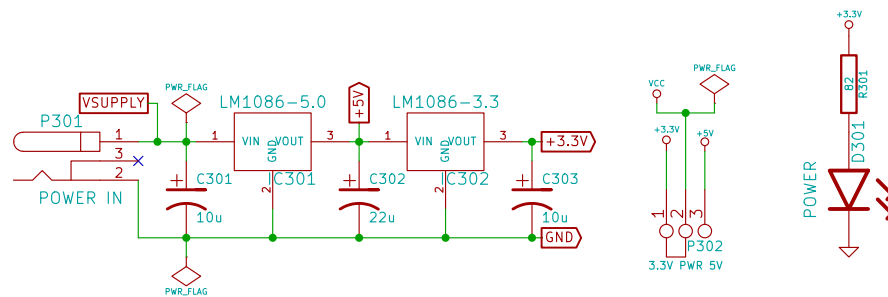
PIN USAGE OVERVIEW:
Directions are relative to Propeller

P0..P7 (in/out)=Data Bus (CLK0=H)
P0..P15 (in)=Address Bus (CLK0=L, AEN=L)
P8..P15 (out)=Signal Bus (AEN=H, SLC=L->H)
P16..P20 (out)=TV/VGA out (for VGA, bridge P101 2-3)
P20 (out)=Debug LED
P20 (in)=SYNC or CLK2 depending on P401 (if P101 1-2 bridged)
P21 (out)=RAMOE (data from RAM to data bus)
P22 (out)=RAMWE (data from data bus to RAM)
P23 (in)=R/W
P24 (out)=AEN: Address Bus Enable
P25 (out)=SLC: L->H transition updates signals
P26..P27 (in/out)=PS/2 Keyboard
P28..P29 (in/out)=EEPROM
P28 (out)=CLK0 (EEPROM inactive as long as P29 is kept High)
P30..P31 (in/out) Serial port to PC

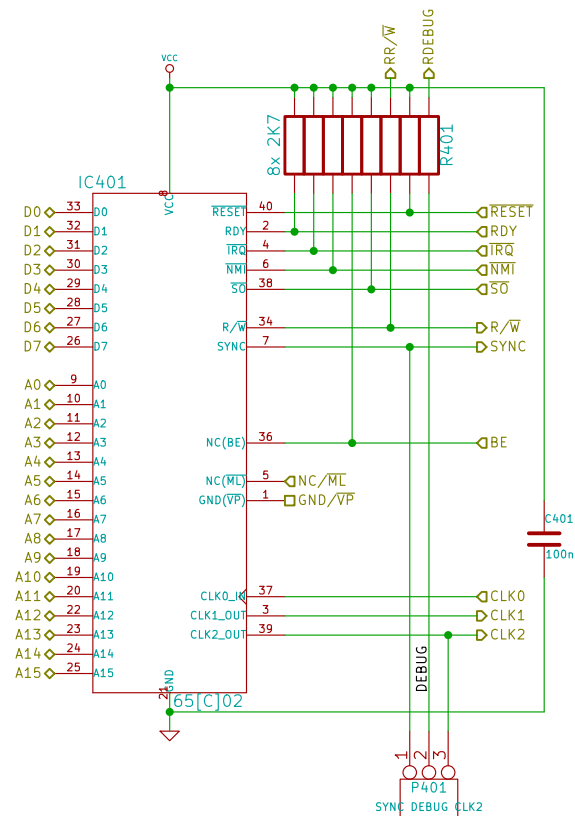




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Size: A4	Date: 2 may 2014	Rev: 11
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U401
R/W
DEBUG

RR/W
RDEBUG

Cut shorts for operation on 5V

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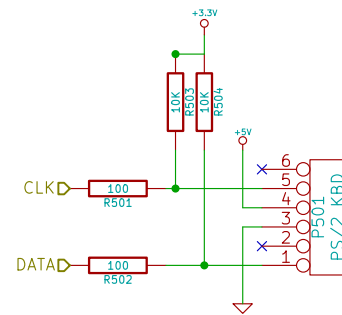
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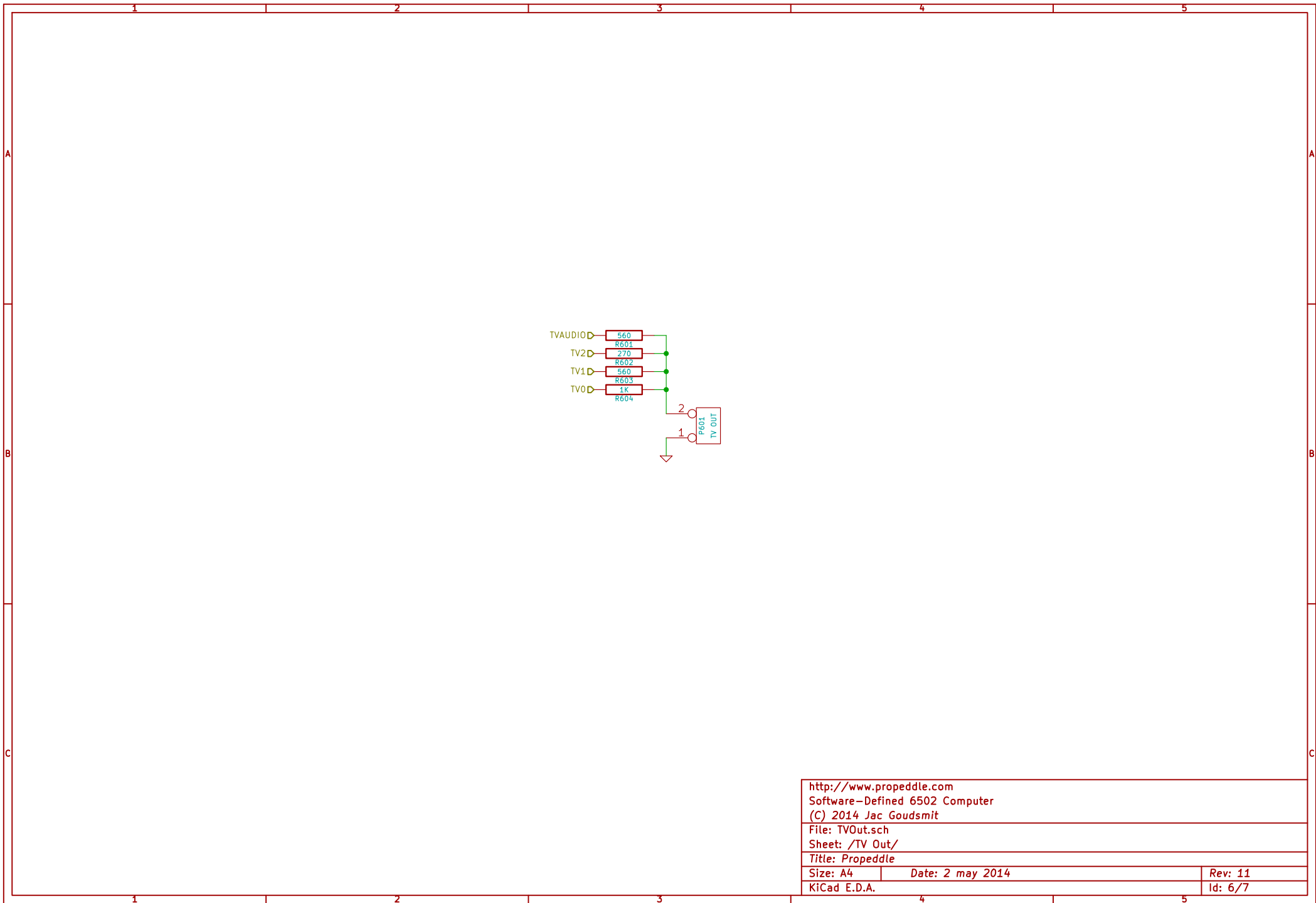
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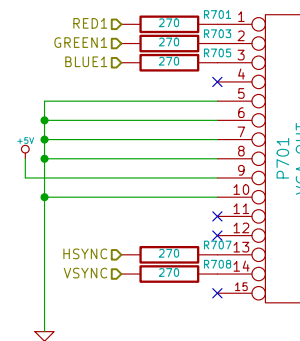
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