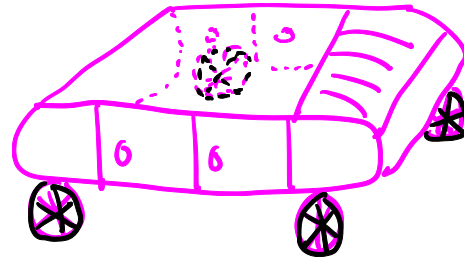


SU 11 Q+A.

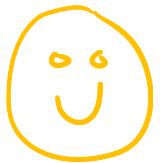
[105] - [114]

①

ABSTRACT : High level ideas ; detail missing



ABSTRACT CAR

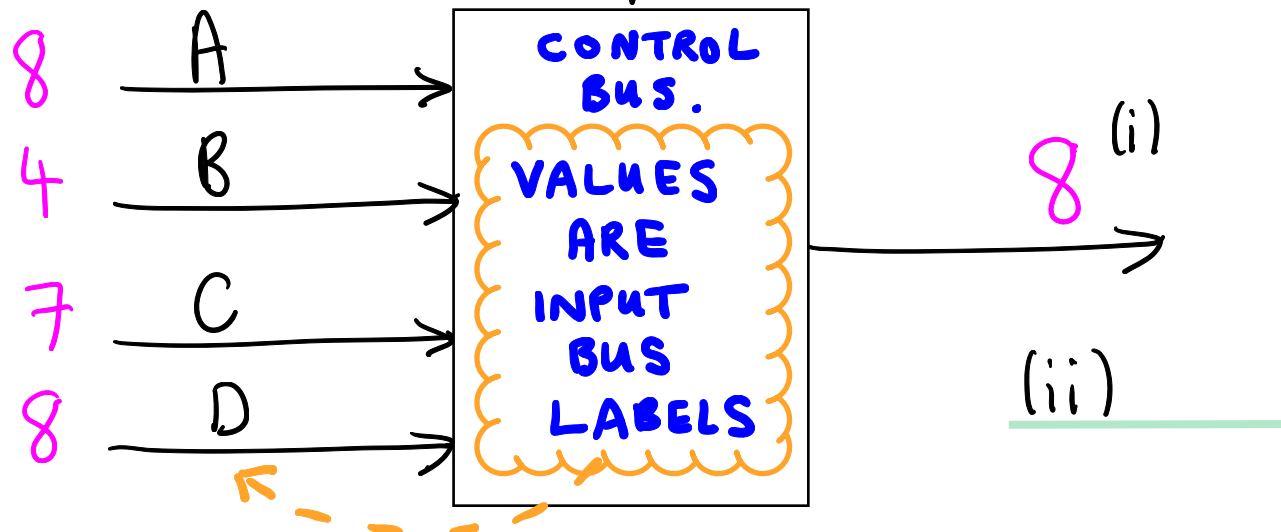


CONCRETE : Low level details.

ABSTRACT
MULTIPLEXOR



[105]



CONCRETE Multiplexor

[106]

②

Control C_1 C_0

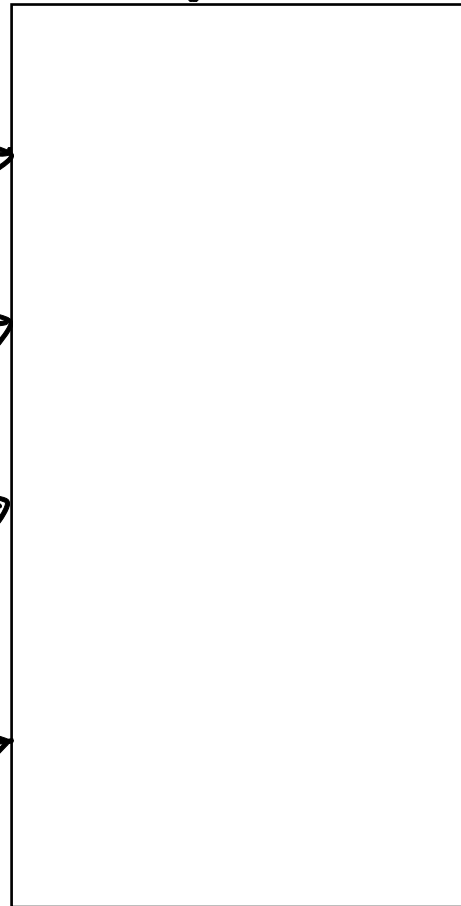
Data

D_0 00010

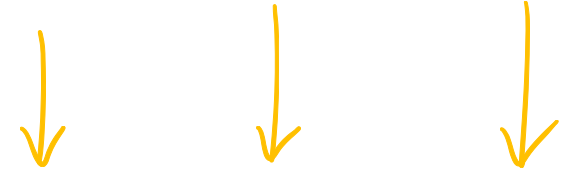
D_1 11101

D_2 11101

D_3 10011



control = 00, 01, 11

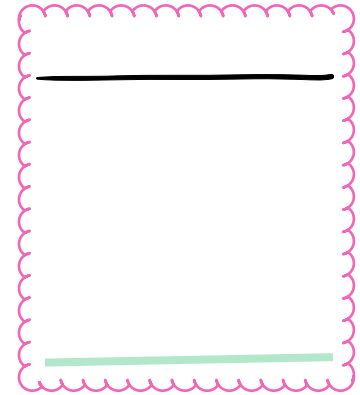


M

0

If $M = 11101$

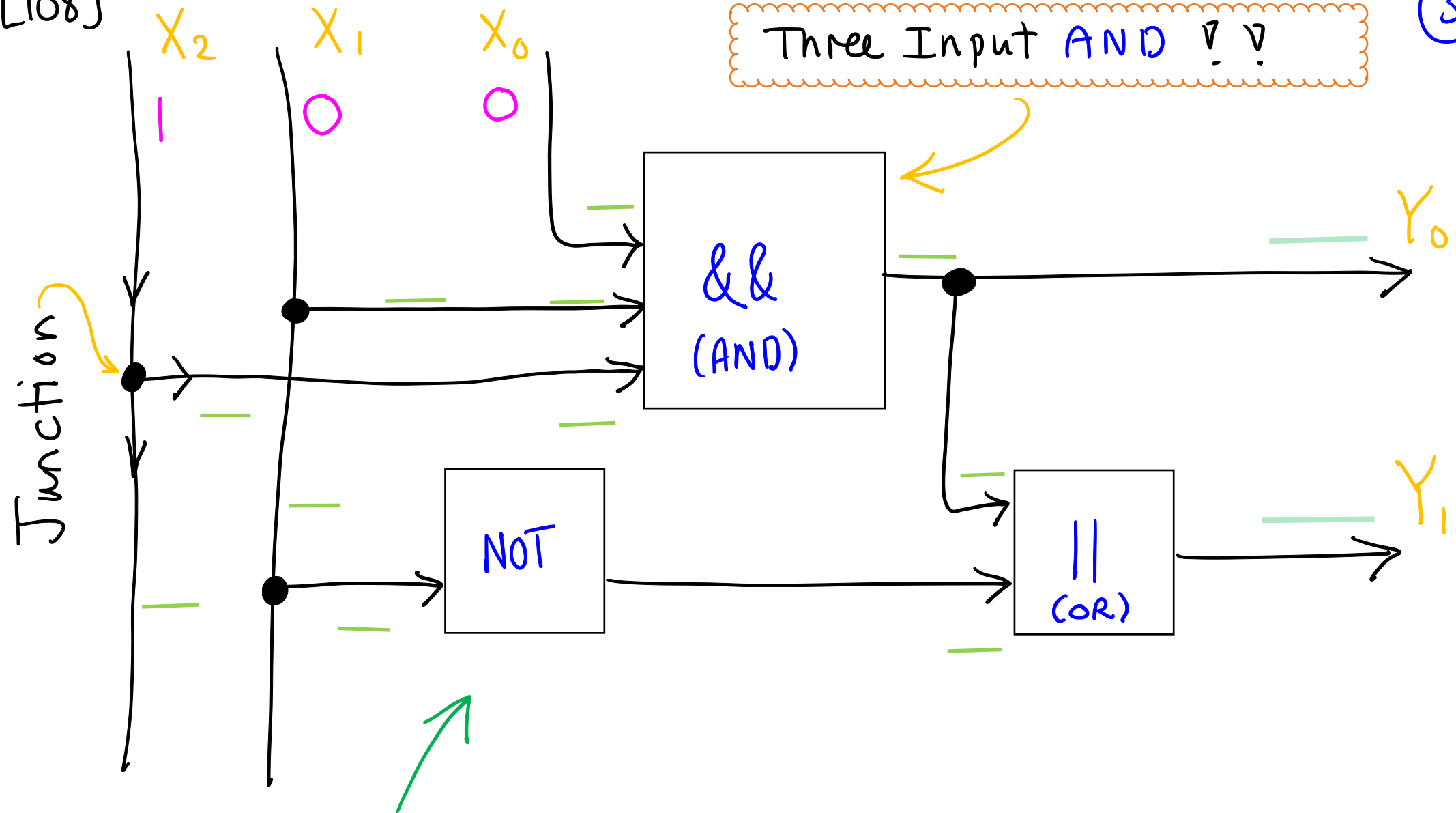
then $\vec{C} =$



[108]

3

Three Input AND !!



Example Circuit

Trace the dataflow....

... Hence compute $\vec{Y} =$ _____

[111] Discuss.

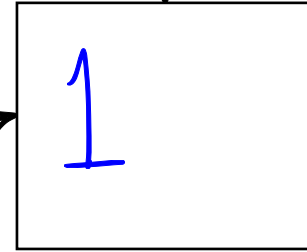
[113]

Write 0 to the bit

WE =

④

Din

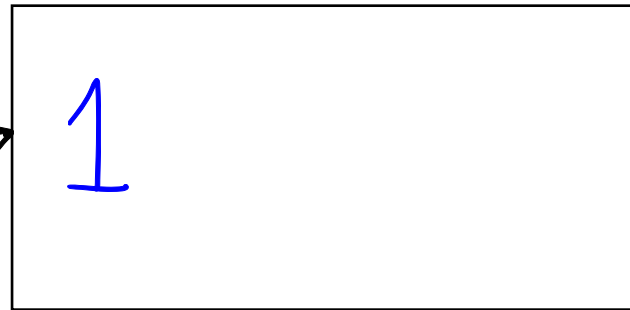


RE 0

WE 0

0

Din



CONCRETE BIT



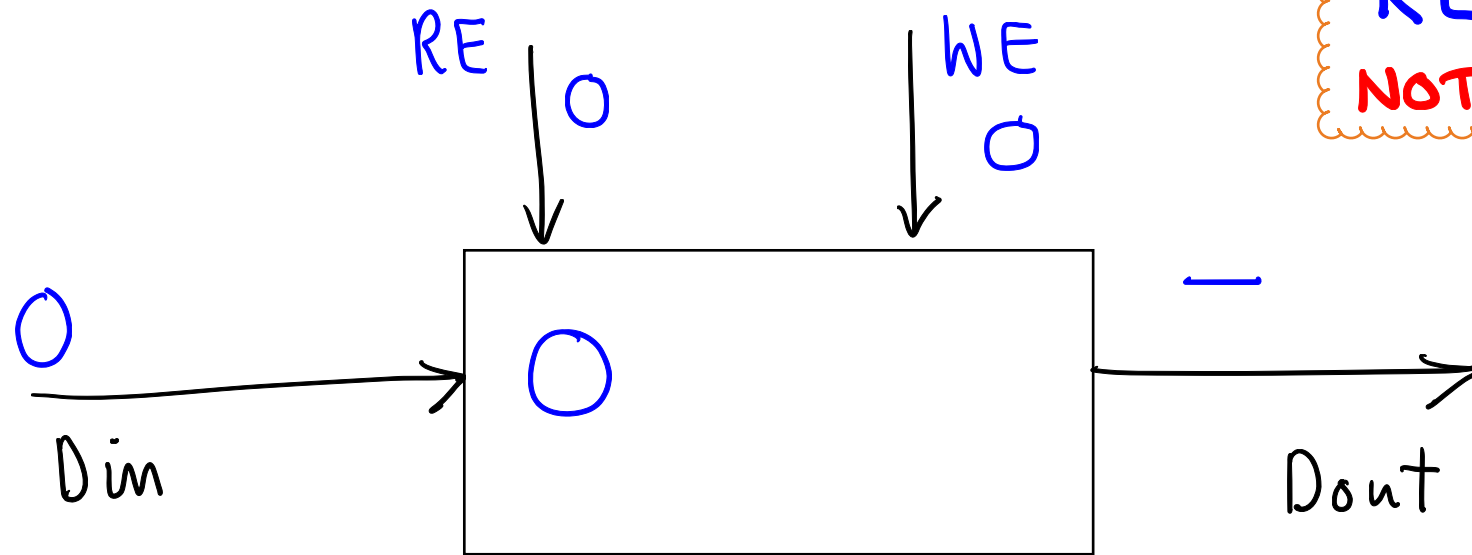
ABSTRACT BIT

$RE := 1 ; RE := 0 ; WE := 1 ; WE := 0$

Write down the dataflow/changes of state

STATE CHANGES

[114]



RE = WE = 1
NOT ALLOWED

DISCUSS!

What state changes do we make so that we end up with

first: $D_{in} = 1$, Bit value stored = 1

second: $D_{out} = 1$